

EFFECT OF W COATING ON MICROENGINE PERFORMANCE

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ABSTRACT

Two major problems associated with Si-based MEMS (MicroElectroMechanical Systems) devices are stiction and wear. Surface modifications are needed to reduce both adhesion and friction in micromechanical structures to solve these problems. In this paper, we will present a CVD (Chemical Vapor Deposition) process that selectively coats MEMS devices with tungsten and significantly enhances device durability. Tungsten CVD is used in the integrated-circuit industry, which makes this approach manufacturable. This selective deposition process results in a very conformal coating and can potentially address both stiction and wear problems confronting MEMS processing. The selective deposition of tungsten is accomplished through the silicon reduction of WF_6 . The self-limiting nature of the process ensures consistent process control. The tungsten is deposited after the removal of the sacrificial oxides to minimize stress and process integration problems. The tungsten coating adheres well and is hard and conducting, which enhances performance for numerous devices. Furthermore, since the deposited tungsten infiltrates under adhered silicon parts and the volume of W deposited is less than the amount of Si consumed, it appears to be possible to release adhered parts that are contacted over small areas such as dimples. The wear resistance of tungsten coated parts has been shown to be significantly improved by microengine test structures.

INTRODUCTION

Surface micromachined devices are currently fabricated from polycrystalline silicon, which is used by the silicon microelectronics industry as a gate electrode and local interconnect [1]. Parts fabricated from polysilicon, a material originally developed for its electronic properties, have been demonstrated to be mechanically robust [2]. However, wear has been identified as a significant failure mechanism for devices with contacting and load bearing surfaces [3-5]. Figure 1 shows a complex five level polysilicon structures with contacting and moving parts fabricated at Sandia National Laboratories.

Some approaches to the problem of wear include the introduction of a low friction polymeric coating, for example by PECVD (Teflon) or introduction of self assembled monolayers, through wet chemical routes after the release process [6, 7]. In these approaches the deposited layer itself is not hard but wear is diminished by the reduction in the coefficient of friction. The long-term behavior of these very thin layers of polymeric materials is unclear. A fundamentally different approach to the wear problem is to substitute the polysilicon with intrinsically hard materials such as diamond or silicon carbide. However, this runs counter to the great enabling strength of polysilicon surface micromachining, which is the leveraging of IC processing technology and tool sets. Thus, the big drawback to this approach involves process integration. Most devices with contacting layers consist of a minimum of three mechanical levels fabricated using a complicated combination of deposition, photolithographic, etch, and planarization processes. The introduction of completely new materials and processing technologies into these

complex process flows may be difficult and require further development efforts. Therefore, development of better surface passivation and tribological coatings using standard IC processing tool sets is of great importance for the successful widespread introduction of MEMS sensors and actuators with contacting surfaces. In this paper we demonstrate that coating with selectively deposited, self-limiting, tungsten coating can dramatically improves the wear characteristics of microengines with much less process impact.

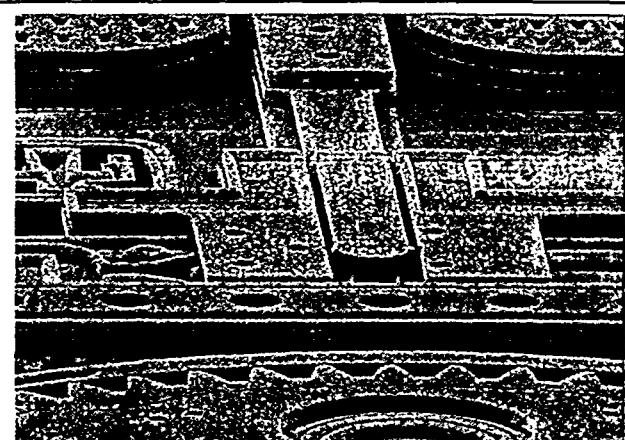


Figure 1. An example of a complex, five level MEMS structure fabricated using surface micromachining at Sandia National Laboratories. Stiction and wear are problems in processing such highly complex systems with numerous contacting, rubbing, moving and impacting surfaces.

The selective deposition of tungsten through the silicon reduction of WF_6 was studied in detail in the late 1980's but never gained acceptance by the IC industry [8-13]. However, blanket tungsten CVD, using silane or hydrogen reduction is commonly used in the integrated-circuit industry. Fortunately, this same tool set can be applied to the selective silicon reduction process outlined here. The selective deposition of tungsten is accomplished through silicon reduction of WF_6 , which results in a self-limiting reaction [11, 12]. The self-limiting nature of this deposition process ensures consistent process control and results in a very conformal coating. The ability to selectively deposit W after the removal of the sacrificial oxide and the low temperature of the deposition (<450°C) eases process integration. Furthermore, since the deposited tungsten infiltrates under adhered silicon parts and the volume of W deposited is less than the amount of Si consumed, it appears to be possible to release adhered parts that are contacted over small areas such as dimples.

Tungsten has a number of attractive properties as a wear resistant coating. Endurance of the W coating is important, especially in applications where wear may occur due to rubbing or contacting surfaces. Unlike polymeric coatings, which only serve to reduce the coefficient of friction, W is hard. Also unlike the polymers, W is entirely compatible with the temperatures typically associated with packaging and is ultra high vacuum compatible.

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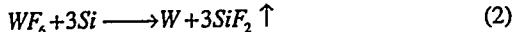
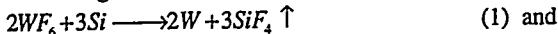
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COATING PROCESS

The chemical reactions for selective W deposition using chemical vapor deposition are given below:



When WF_6 encounters heated silicon, a reaction occurs in which SiF_4 or SiF_2 gas is formed and W is deposited on the silicon surface. Depending on the reaction temperature one reaction or the other dominates [10, 13]. Once a continuous film of W is formed (after $\sim 200\text{\AA}$ on our structural polysilicon) the WF_6 is shielded from the Si and the reaction slows or stops. This process is completely selective since the reaction does not occur on silicon dioxide or silicon nitride [14, 15].

The selective W deposited on polysilicon surfaces is extremely conformal as shown in a SEM micrograph in Figure 2. After cleaving, the polysilicon has been etched using $HNO_3:HF$ to delineate the extremely thin W coating around the polysilicon. The W on the top surface is continuous even after this aggressive etch, demonstrating the absence of pin holes which would have been enhanced during delineation.

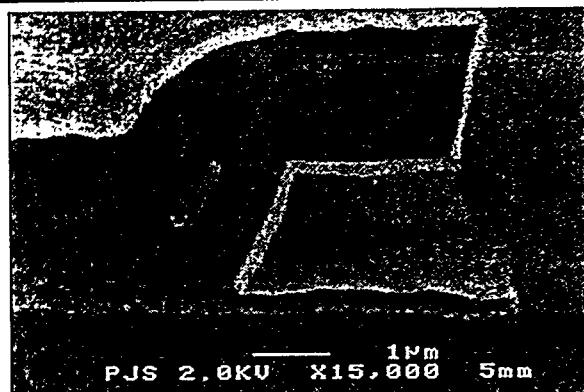


Figure 2. A scanning electron micrograph showing a thin $\sim 100\text{\AA}$ layer of silicon reduced W conformal around all sides of a section of structural polysilicon. The silicon has been recessed using a HF/HNO_3 mixture in order to delineate the W.

During the displacement reduction reaction of Si by WF_6 , the thickness of Si consumed is about twice the thickness of tungsten deposited. This inherent difference in the deposited film thickness and the reacted film thickness results in W depositing in very narrow spaces making this process desirable for MEMS devices. Figures 3a and 3b show undercutting in initially adhered Si members. The SEM image shows the underside of a cantilever beam where W has deposited. The structure was pulled using carbon-tape to image the underside. It appears that W has penetrated between layers of Si, which were initially adhered. If the contact area is kept small this may be a way to address the as fabricated stiction problem. This is also important in that it is a mechanism for the coating of portions of parts that are unavoidably in local contact at the end of the drying process. Standard deposition processes either do not coat these contacting regions or can act to further glue them together.

RELIABILITY

Microengines Functionality

MEMS devices were selectively coated with W using the selective silicon reduction process outlined above [15]. The

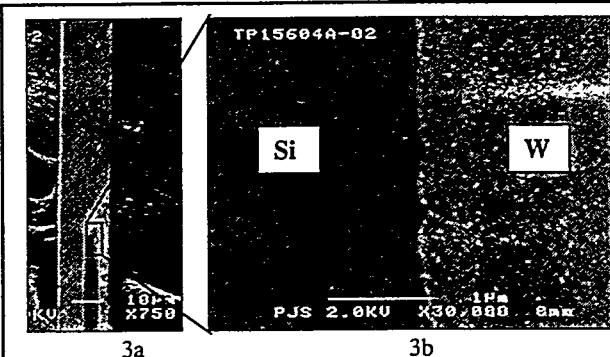


Figure 3 (a). Scanning electron micrograph of the backside of a cantilever beam showing W encroachment underneath the beam, (b) higher magnification of the same region as in (a) showing W and polysilicon regions.

microengine on the diagnostic module of the wafer was chosen for reliability and wear resistance assessment. It is of the same design as most of those tested in previous reliability experiments and is shown in Figure 4 [3-5, 16-18]. By applying the proper drive voltages, the linear displacement of the comb drives is transformed into circular motion. The X and Y linkage arms are connected to the gear via a pin joint. The gear rotates about a hub that is anchored to the substrate. The microengine has been the focus of much investigation for MEMS devices experiencing sliding friction [3-5].

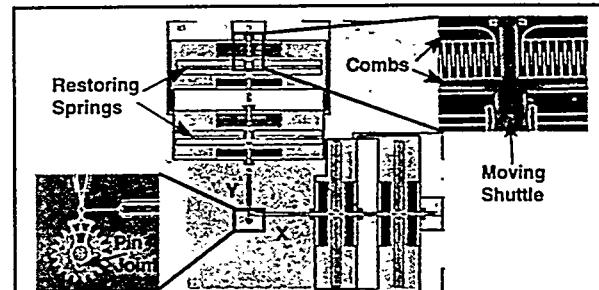


Figure 4. Sandia microengine with expanded views of the comb drive (top right) and the rotating gear (bottom left).

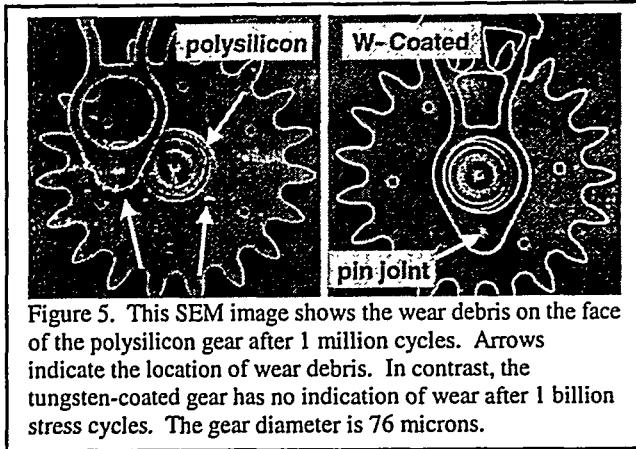
One of the many issues associated with assessing the reliability of microengines is the method used to operate them. Ideal equations (model-based), based on Newtonian physics, were derived to optimize the electrical signals [16]. In the typical optimization procedure, parameters were measured [17] which were then input into the equations. These equations are NOT simple sine waves, and it has been demonstrated that operation by square waves produced early failures [18].

To make a clean comparison to the polysilicon microengine, we decided to use the same drive parameters and frequency (1720 Hz) used in an earlier tests [3-5, 16-18]. All of the earlier tests were stressed with a large longitudinal force to accelerate the time to failure. The same was done with the tungsten-coated devices.

In our earlier tests without the coating, we observed a median time to failure of 4×10^5 accumulated cycles using a sample size of more than 20 microengines. This was performed in a controlled humidity environment of 39% RH. Using the same drive-signal parameters, but in ambient laboratory conditions (30-50%RH), we observed a dramatic increase in the time to failure for W coated parts. We saw no failures in 30 samples tested to 2 million cycles. Two microengines ran normally for 1,035,000,000 and 379,000,000 cycles respectively after which the test was stopped. One microengine was stressed using square waves and ran for

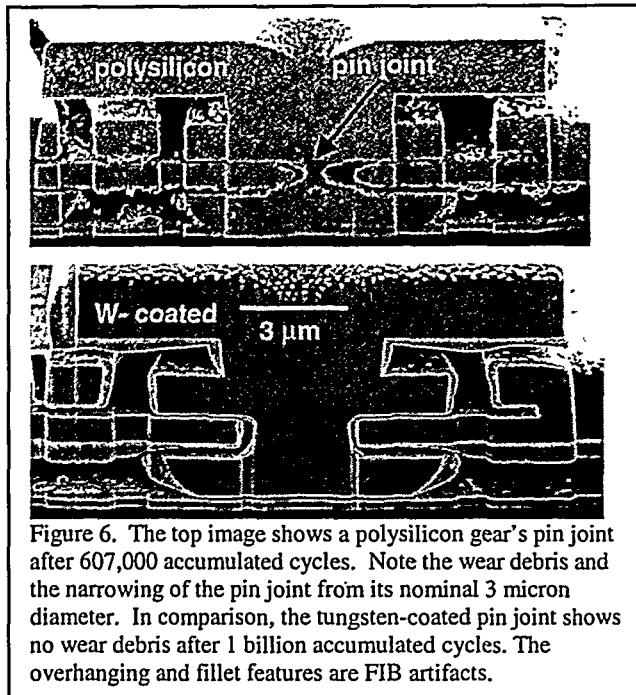
258,000,000 cycles without failure. We would have expected this to run less than 100,000 cycles without the W coating.

As shown in Figure 5, there is no apparent wear debris on the W coated devices. The polysilicon gear on the left failed after 1 million cycles with wear debris forming inside the hub and near the pin joint region. In comparison, on the right is a tungsten-coated microengine gear with no signs of wear debris after a billion cycles.

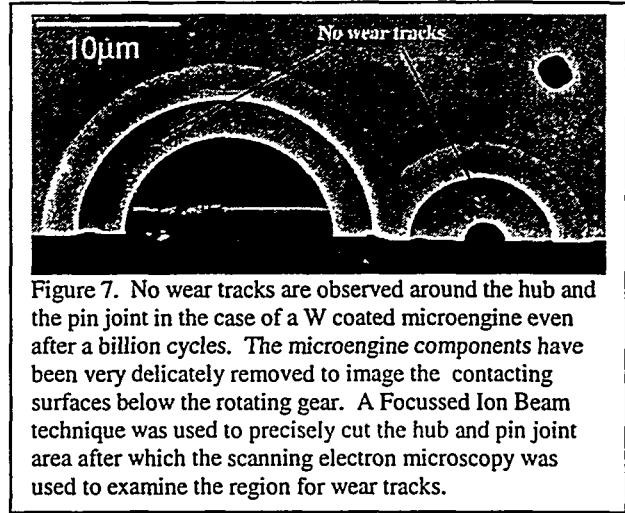


Further confirmation of excellent wear resistance is shown in Figure 6 by focused ion beam (FIB) cross sections of the pin joint region. The pin joint has a diameter of 3 microns. The uncoated polysilicon pin joint failed at 607,000 cycles and shows excessive wear with a significant narrowing of the diameter. Wear debris was present on all the rubbing surfaces. In contrast, the tungsten-coated pin joint shows no wear or wear debris anywhere near the rubbing surfaces.

Figure 7 shows the contacting surface below the revolving gear



in a W-coated microengine. There are no wear tracks even after an accumulation of a billion cycles under accelerated test conditions. Normal operation is at much lower frequencies, therefore the parts last much longer than observed in the accelerated test conditions, making these parts very attractive for applications requiring long term use.



Mechanical Behavior

Because it is relatively thin, we do not expect the ~15 nm W layer to strongly affect the basic mechanical properties (E , Young's modulus, $D=Er^3/12$ the bending stiffness, R the radius of curvature and σ_R the average residual stress through the thickness of the film) of the 2500 nm thick structural polysilicon/W composite. However, the mechanical properties and the density of W are significantly different from polysilicon, and the W is placed on the outside of the film where its impact on the effective bending stiffness is maximized. On the other hand, the overall film thickness is reduced about 30 nm when the W is formed, which would counteract the increase in the bending. When taking these factors into account, the combined expected effect on D is a 2% increase for the W-sandwich composite beam. It should also be considered that the W layer is highly tensile (~1 GPa) because of the associated volume reduction when the W formation takes place.

To assess the effect of the W coating on the mechanical properties, two types of testing were carried out. For out-of-plane properties, we measured beam bending by interferometry under increasing voltages before pull-in [24], and for in-plane properties we measured resonant frequency of comb resonators. The measurements were made on different wafers from the same lot. Because both of these types of measurements depend on the thickness or line width of the structure, wafer to wafer differences will also contribute to any differences found in the property measurements.

From the out-of-plane properties measurements, we first determined radii of curvature of $R = -0.5$ m and $R = 0.78$ m for the uncoated and coated polysilicon respectively. In both cases, R is large indicating that the average stress gradient through the thickness of the film is small. Next, D for the coated film was found to be less than 10% greater than for the uncoated film. The effective modulus E of the W-coated composite structure is within 5% of the uncoated structure. These numbers are in reasonable agreement with the expected effect calculated above. Finally, at less than 5 MPa, a small decrease in the average residual stress σ_R was found for the coated relative to the W-coated polysilicon. This is a small change, but the decrease rather than the increase of σ_R is unexpected.

For the in-plane measurements, as measured on 10 W-coated resonators, the average resonant frequency was 4.1 KHz, while typically it is ~4.3 KHz for our standard release process. The resonant frequency $v_R = \sqrt{k/m}$ is a combined measurement of a number of both the spring constant k as well as the mass m . The spring constant k is directly related to the bending stiffness D , which

we saw above is approximately the same for the coated compared to the uncoated material. However, the density of W is very high at 19 g/cm³, while the density of Si is 2.3 g/cm³. This would explain a decrease in ν_R of 4% (assuming a 15 nm thick W removing 30 nm of Si). Therefore, the observed reduction in ν_R is perhaps mainly due to mass differences.

In summary, the basic mechanical properties of the W-coated polysilicon are dominated by the polysilicon structural material, and the effect of W is small. More investigation is required to understand the differences in R and σ_R in detail. An effect on the fracture properties was more significant, as discussed in the next section.

Tensile or Mechanical Strength

During testing, the parts appeared to be more fragile, breaking relatively easily as compared to the uncoated parts. In order to quantify these qualitative observations we proceeded to test the strength of coated and uncoated parts. Miniature tensile samples have been tested to investigate the effect of tungsten coating on the strength of polysilicon. Each sample has a freely moving pivot and a pull-ring; the gage crosssection is 1.8 μm wide by 2.5 μm thick and varies in length from 15 to 1000 μm . The pull-ring is engaged by a 35 μm diameter flat tipped diamond using a nanoindenter. The normal force, lateral force and displacement are recorded. The normal force is maintained throughout the test to prevent the conical shaped diamond from being pushed upwards by the pull-ring engagement reaction. Once engaged, the tip continues to move laterally, which loads the thin polysilicon ligament in tension. Figure 8 shows two samples: the lower one has been tested to failure.

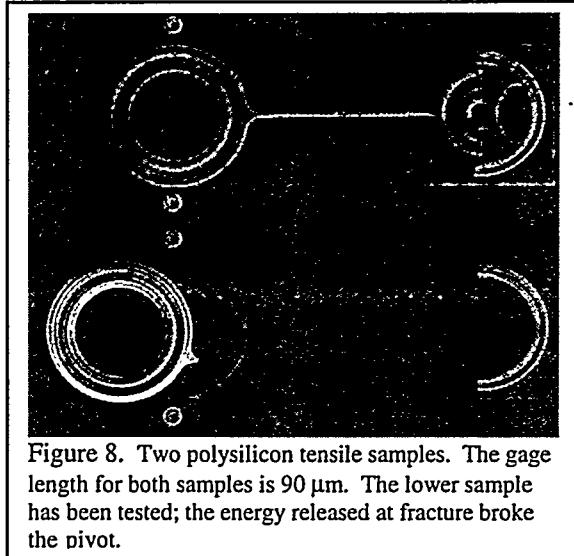


Figure 8. Two polysilicon tensile samples. The gage length for both samples is 90 μm . The lower sample has been tested; the energy released at fracture broke the pivot.

Ninety-eight conventionally released samples and fifty tungsten-coated samples were tested in tension. The conventionally treated samples were subject to two release process as released and dried by supercritical carbon dioxide or released and coated with a PFTS self assembling monolayer. No difference in strength was found between these samples. Figure 9 shows the strength the two cases as released and tungsten coated tensile samples. Weibull analysis was performed on the strength data for the conventionally released samples [19]. A probability estimator in the form of $(j-0.5)/n$ was chosen [20]. The Weibull modulus, m , was calculated to be 8.4 for the conventionally released samples and 5.3 for the W coated samples. Figure 10 shows plots of the strength data for the Weibull analysis.

Both strength and modulus for polysilicon must be evaluated in relation to fracture toughness and flaw size. The presence of defects can cause a drop in the modulus from that predicted from the elastic

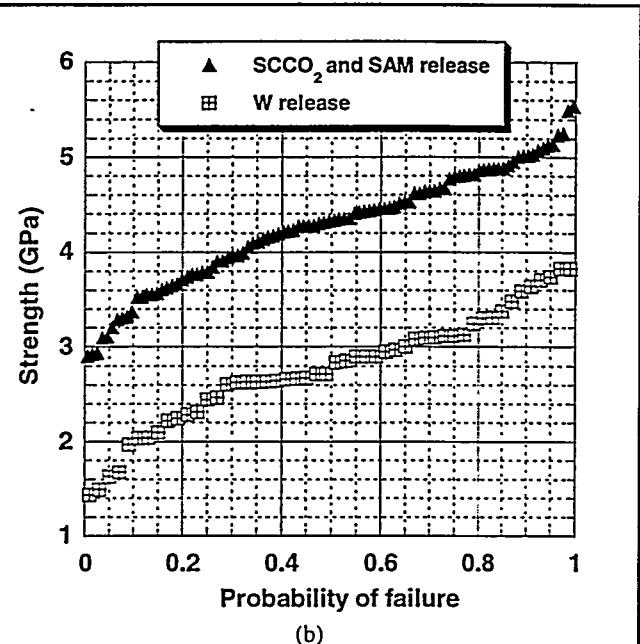


Figure 9. Strength of 98 conventionally released polysilicon samples and 50 tungsten coated samples
Probability of Failure

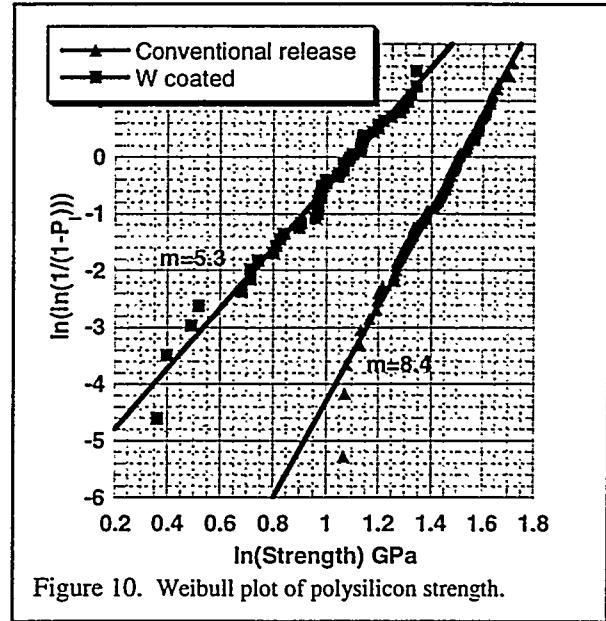


Figure 10. Weibull plot of polysilicon strength.

constants, however the volume fraction of such flaws has to be a recognizable percentage of the sample volume. Considering that this material has a fracture toughness on the order of 1-3 MPa $\sqrt{\text{m}}$, flaws approximately 5-20 nm in size are responsible for initiating fracture [21, 23], which is a very small percentage of the cross section. Efforts to measure the fracture toughness of polysilicon produced at Sandia are underway. Figure 11 shows a classical brittle fracture surface from a tensile sample along with a TEM image of particle intrusions found at the W-polysilicon interface. The size of these flaws is consistent with the equations in the literature. Even though

the grain size is approaching the size of critical features, the critical flaw size is much smaller than either of these dimensions, and it appears that there is no issue in scaling the fracture analysis.

Sidewall Friction Device

The sidewall friction device consists of two orthogonal

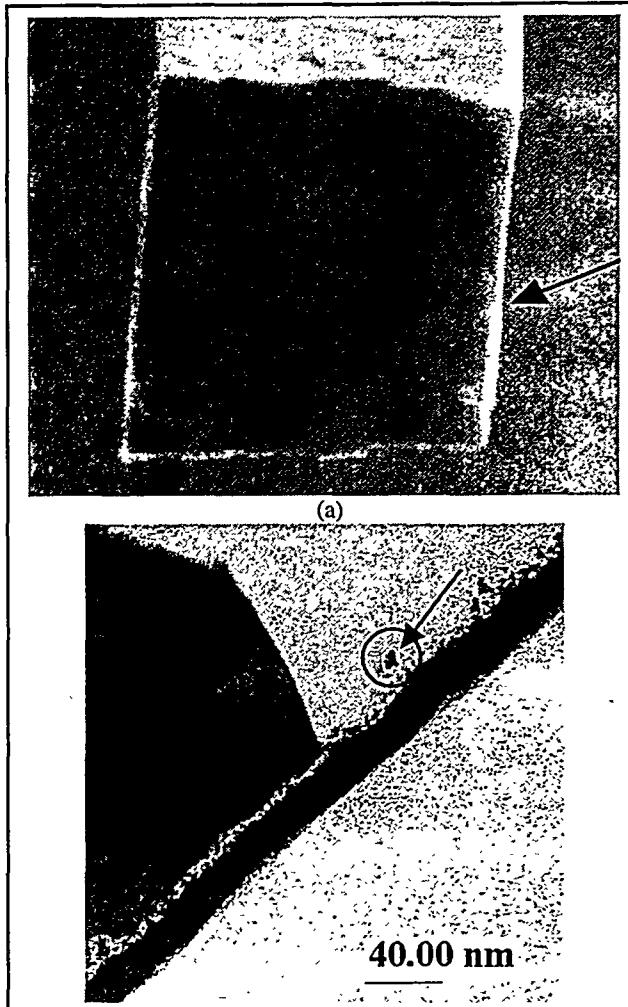


Figure 11. W coated polysilicon samples (a) fracture surface with crack initiation marked (b) TEM cross section showing particle intrusion at W-polysilicon interface.

electrostatic comb actuators connected to a movable beam. The beam is suspended above the substrate by the folded spring suspensions on the comb actuators. A post is formed by etching through sacrificial oxide layers so that a polysilicon deposition forms a structure, which is anchored to the substrate and has a cylindrical geometry facing the beam. The device is actuated by using a DC voltage on the loading comb to bring the beam into contact with the post, and then applying a waveform to the oscillation comb to slide the beam against the post.

The friction coefficient exhibited by the tungsten coated surfaces in laboratory air, measured using the sidewall friction tester, and is shown in Figure 12. The friction coefficient is initially near 0.07, and decreases to about 0.05 after several thousand cycles of operation. The small decrease in friction after the start of sliding is consistent with a minor amount of run-in, where the highest surface asperities are removed and improved conformality of the sliding surfaces is established. The kinetic friction coefficient value is comparable to that observed with fluorocarbon silane films on

polycrystalline silicon [25]. This value is lower than would be expected from self-mated tungsten surfaces in environments containing water vapor, from macroscopic measurements [26]. Even self-mated tungsten carbide surfaces, which would represent contact between covalently bound surface layers analogous to the natural oxide surfaces, would not be expected to exhibit a friction coefficient this low. Since the friction coefficient is much lower than that observed in macroscopic sliding experiments with tungsten, we believe that the friction behavior of the tungsten coated surface is governed by the presence of another surface film. This film may be a residual layer from the tungsten deposition process itself that effectively prevents metallic contact of the tungsten surfaces. Compositional analysis of worn surfaces is underway to test this hypothesis. In fact, the friction coefficient observed for the tungsten coated surface in micromachine contacts is in the range of values observed with metal dichalcogenide solid lubricant films such as MoS_2 , WS_2 or WSe_2 [27].

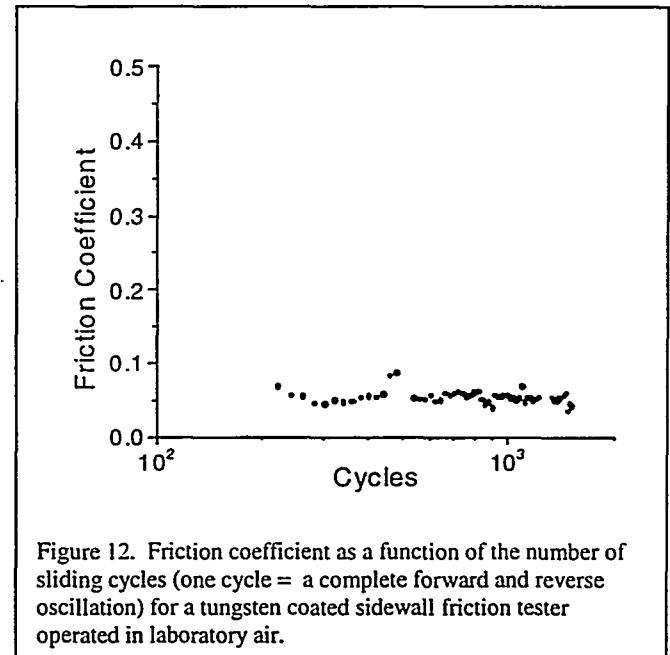


Figure 12. Friction coefficient as a function of the number of sliding cycles (one cycle = a complete forward and reverse oscillation) for a tungsten coated sidewall friction tester operated in laboratory air.

CONCLUSIONS

Selective tungsten coating using chemical vapor deposition has been used to deposit a thin, uniform, conformal coating to improve wear resistance. This W coating shows dramatic improvements in wear characteristics. Microengines coated with selective W show longer lifetimes than polysilicon microengines. The mean time to failure observed in polysilicon microengines was 4×10^5 using 20 microengines. In comparison, in the tungsten coated microengines no failure was observed in 30 microengines tested to 2 million cycles. The tungsten coating changes the mechanical behavior of the polysilicon structure, while satisfying the MEMS devices requirements and not influencing the device performance. Hence, this coating is well suited for MEMS applications.

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