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# **InGaAs MONOLITHIC INTERCONNECTED MODULES (MIM)**

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## **ABSTRACT**

A monolithic interconnected module (MIM) structure has been developed for thermophotovoltaic (TPV) applications. The MIM device consists of many individual InGaAs cells series-connected on a single semi-insulating (S.I.) InP substrate. An infrared (IR) back surface reflector (BSR), placed on the rear surface of the substrate, returns the unused portion of the TPV radiator output spectrum back to the emitter for recycling, thereby providing for high system efficiencies. Also, the use of a BSR obviates the need to use a separate filtering element. As a result, MIMs are exposed to the entire emitter output, thereby maximizing output power density. MIMs with an active area of 1x1-cm were comprised of 15 cells monolithically connected in series. Both lattice-matched and lattice-mismatched InGaAs/InP devices were produced, with bandgaps of 0.74 and 0.55 eV, respectively. The 0.74-eV modules demonstrated an open-circuit voltage (Voc) of 6.158 V and a fill factor of 74.2% at a short-circuit current (Jsc) of 842 mA/cm<sup>2</sup>, under flashlamp testing. The 0.55-eV modules demonstrated a Voc of 4.849 V and a fill factor of 57.8% at a Jsc of 3.87 A/cm<sup>2</sup>. IR reflectance measurements (i.e.,  $\lambda > 2 \mu\text{m}$ ) of these devices indicated a reflectivity of  $\geq 83\%$ . Latest electrical and optical performance results for our MIMs will be presented.

## **INTRODUCTION**

Most thermophotovoltaic (TPV) systems currently under consideration envision utilizing planar, one-junction low-bandgap (Eg) photovoltaic (PV) cells, such as InGaAs/InP, GaSb, and InGaSbAs/GaSb.<sup>1-4</sup> Under their operating conditions, these devices commonly exhibit current densities in the range of 1-10 A/cm<sup>2</sup>, and voltages in the range of 400-550 mV. In a TPV system, however, these cells are connected in series to create a high-voltage/low-current configuration: to provide a usable external bus voltage, and to keep  $I^2R$  resistive power losses to acceptable levels. Another approach to achieving a high-voltage/low-current configuration is to fabricate a device, where small area PV cells are connected in series monolithically. This results in the formation of a single high-voltage/low-current module. We have termed such a device, a monolithic interconnected module (MIM). Planar views of a MIM device are shown in Figure 1.

Another important advantage of a MIM, compared to a conventional PV cell, is its ability to efficiently recycle the incoming non-convertible infrared (IR) radiation. As shown in Figure 1, the fabrication of MIM requires the use of a semi-insulating (S.I.) InP substrate. S.I. InP is transparent to infrared (IR) radiation. Radiation with wavelengths ( $\lambda$ ) greater than the device bandedge wavelength (i.e.,  $\lambda \geq \sim 2 \mu\text{m}$ ) can be reflected back to the TPV radiator via a back

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surface reflector (BSR). The BSR also provides for the convertible in-band radiation to get a second pass through the active regions of the device, increasing the effective cell collection efficiency.<sup>5</sup>

*Figure 1 will be attached here.*

Figure 1.—Planar view of a MIM (top), and close-up view of an interconnect area (bottom).

There are other advantages to a MIM design. Since both the negative and positive electrical connections are made to the top side of the module, array design, fabrication, and thermal management are simplified. The completed device may be soldered directly onto the array substrate/heat sink without having to provide electrical isolation. Finally, the rear surface IR reflector eliminates the need for a separate filtering element. Without a filter, and its inherent transmission and re-radiation losses, the system design is simplified and the power density is increased.

We have fabricated and tested both lattice-matched and lattice-mismatched p/n InGaAs/InP MIMs, grown by organo-metallic vapor phase epitaxy (OMVPE). These structures had bandgaps of 0.74 and 0.55 eV, respectively. In what follows, we will discuss the design and fabrication of our MIMs, and present data regarding their electrical and optical performance.

### Device Design and Fabrication

The MIM structures were deposited in a horizontal, low-pressure organo-metallic vapor phase epitaxy (OMVPE) reactor described elsewhere.<sup>6</sup> The precursor materials were trimethyl indium (TMIn), trimethyl gallium (TMGa), arsine, phosphine, diethyl zinc (DEZn), and silane for p- and n-type doping, respectively. Conventional Au-Ge<sup>7</sup> and the more adherent Cr-Au<sup>8</sup> contact metallizations were deposited on the emitter and the lateral conduction layer (i.e., back contact) of the device simultaneously. A dielectric barrier layer, deposited by electron-beam evaporated Ta<sub>2</sub>O<sub>5</sub>, was used to prevent the interconnect metallization from shorting out the individual cells.

In our past works on MIMs, we had designed and fabricated 1x1-cm modules that had eight (8) series-connected cells.<sup>9</sup> The devices presented in this work are, however, composed of fifteen (15) p/n InGaAs cells monolithically interconnected in series. By minimizing the packing factor in the new 15-cell MIM design, we were able to actually reduce the total interconnect metal shadowing, relative to the older 8-cell MIM design. The packing factor for the 15-cell MIM device was 92%, but only slightly less than half of the interconnect area was optically opaque to the input radiation.

In every interconnect region, a trench via was etched down to the semi-insulating InP substrate. This via served to electrically isolate one individual cell from the adjacent cell. Near this via, another wider via was etched down to the lateral conduction layer, where the back contact metallization was later deposited. The interconnect region (or the packing factor) for the module is comprised of the trench via, the back contact metallization, and the interconnect

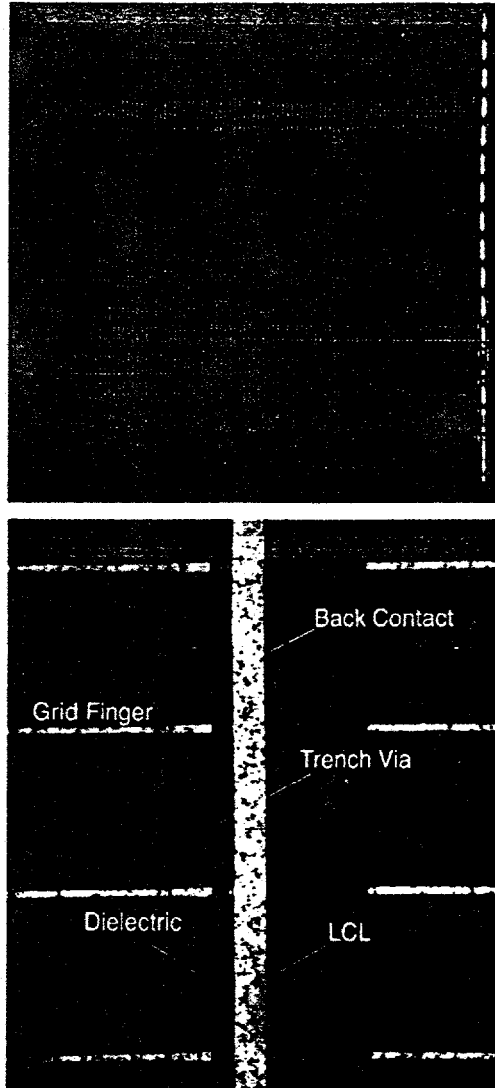


Figure 1

metallization areas. A glancing angle-cross sectional micrograph and a schematic view of the interconnect region are shown in Figure 2.

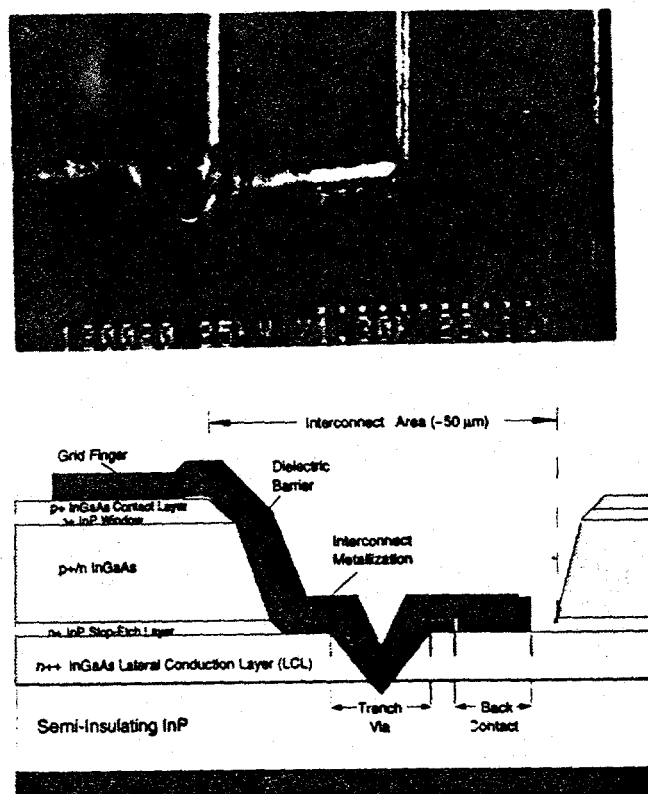


Figure 2.—Micrograph of an interconnect area (top) for a MIM with  $E_g=0.55$  eV, and cross-sectional schematic of the interconnect geometry (bottom).

To maximize the output power density of the module, we attempted to minimize the packing factor by narrowing the trench via (to  $\sim 7$  μm) and the back contact widths. As shown in Figure 2, the total interconnect region was  $\leq 50$  μm wide. To enhance the optical recycling within the module, instead of using a solid busbar, we made the interconnect metallization to be a simple extension of the front contact grid metallization. (See Figure. 2). The gridded interconnects also reduced the probable shunting effects of the metallization residing on top of an imperfect dielectric barrier, by decreasing the metal area over the dielectric to about 5%, as compared to a solid bus metallization. This enabled us to use a non-optimized electron-beam deposited  $Ta_2O_5$  dielectric film, without degrading the electrical performance of the modules.

We selected a p/n configuration for the individual cell structures, mainly because the MIM design requires a relatively thick ( $\sim 1$  μm) rear lateral conduction layer (LCL) to conduct current the length of the device, to reach the back contact. (See Figure. 2). Since the n-InGaAs mobility is higher than p-InGaAs by a factor of  $\sim 25$ , resistive losses are drastically reduced if LCL is an  $n^{++}$  InGaAs. Also, a shift in the optical bandgap of the  $n^{++}$  LCL to a lower wavelength (to  $\sim 1.2$  μm), allows the use of a relatively thin base region ( $< 2$  μm).<sup>5</sup> Bandedge photons that are not absorbed in the thin base region are able to pass through the LCL without

being absorbed. These photons then reflect off the BSR and pass through the base region a second time.

It is also important to minimize the thickness of all the doped layers in the device structure. This is because thinner (and lower doped) layers exhibit less free-carrier absorption (FCA). Lowering FCA is crucial to enhancing the optical recycling efficiency of the MIM. We have also found that for comparably-doped layers of the same thickness, FCA was significantly lower for n-InGaAs, as compared to p-InGaAs.<sup>9</sup>

## Electrical and Optical Results

High-intensity illumination current versus voltage (I-V), quantum yield (QY), and reflectivity ( $R(\lambda)$ ) measurements were performed to characterize the MIMs. We present data for both lattice-matched and lattice-mismatched p/n InGaAs/InP structures, with bandgaps of 0.74 and 0.55 eV, respectively. As compared to our more optimized lattice-matched MIM structures, the data for the non-optimized lattice-mismatched devices are only preliminary.

### *Current-voltage (I-V) data*

Historically, nearly all the reports on the growth and characterization of low bandgap InGaAs cells have been focused on the n/p configuration.<sup>1-3</sup> As a result, to verify the basic material quality, we fabricated base-line conventional planar p/n InGaAs cells ( $E_g=0.74$  eV), prior to fabricating MIM devices. These cells exhibited record efficiencies under air-mass zero (AM0) illumination conditions.<sup>9</sup> We then proceeded to grow, fabricate, and test the MIM devices.

These MIM devices were tested under high-intensity illumination, using a large-area pulsed solar simulator (LAPSS), to assess their performance under simulated operating conditions. The results for the variation in the open-circuit voltage ( $V_{oc}$ ) and fill factor (FF) with the short-circuit current ( $I_{sc}$ ), of a 15-cell MIM ( $E_g=0.74$  eV) are given in Figures 3 and 4. As seen in the figures, the diode ideality factor ( $A$ ) for the device was unity. Also, the FF values remained above 70% in the  $I_{sc}$  range of interest. This result indicates that resistive losses are acceptably low in these devices at the anticipated operating TPV output current levels.



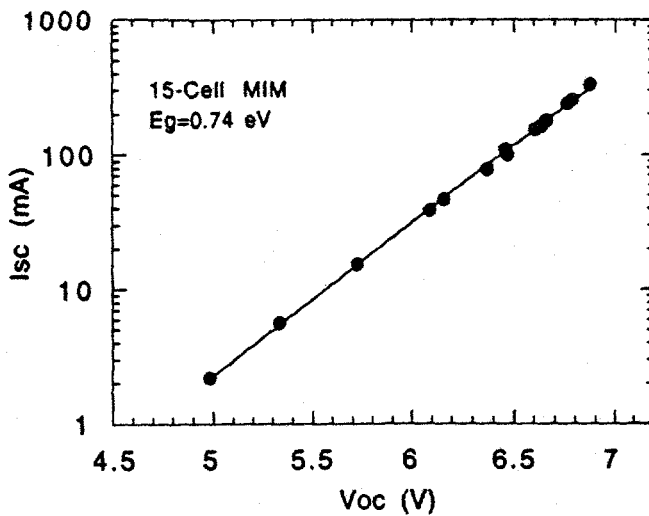


Figure 3.—Variation of the open-circuit voltage ( $V_{oc}$ ) with the short-circuit current ( $I_{sc}$ ) for a MIM with  $E_g=0.74$  eV.

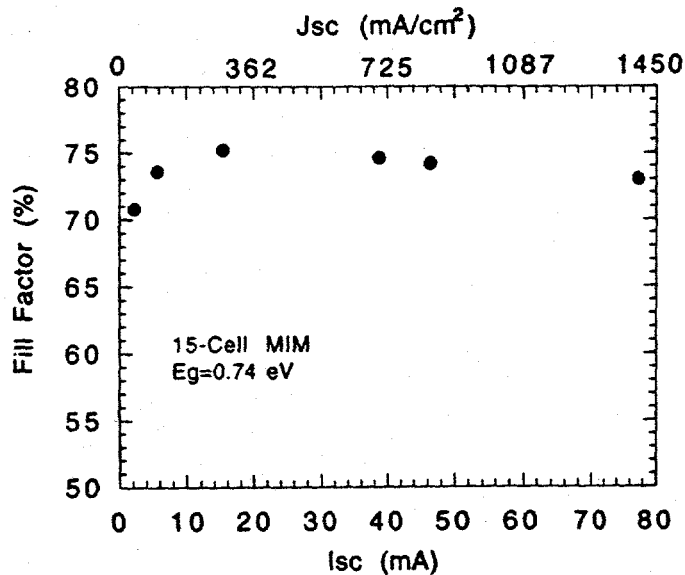


Figure 4.—Variation of the fill factor (FF) with the short-circuit current ( $I_{sc}$ ) and the short-circuit current density ( $J_{sc}$ ) for a MIM with  $E_g=0.74$  eV.

The expected  $J_{sc}$ , for an ideal lattice-matched InGaAs/InP cell (i.e., quantum yield of unity) operating in a TPV system with a greybody emitter temperature of 1200 K, is about 870 mA/cm<sup>2</sup>. In Figure 5, an I-V curve measured under high-intensity light is shown, with the module output  $J_{sc}$  of 842 mA/cm<sup>2</sup>. As shown, this module produced high  $V_{oc}$  (6.158 V, or 410 mV per cell) and FF (74.2%) values near the  $J_{sc}$  design point.

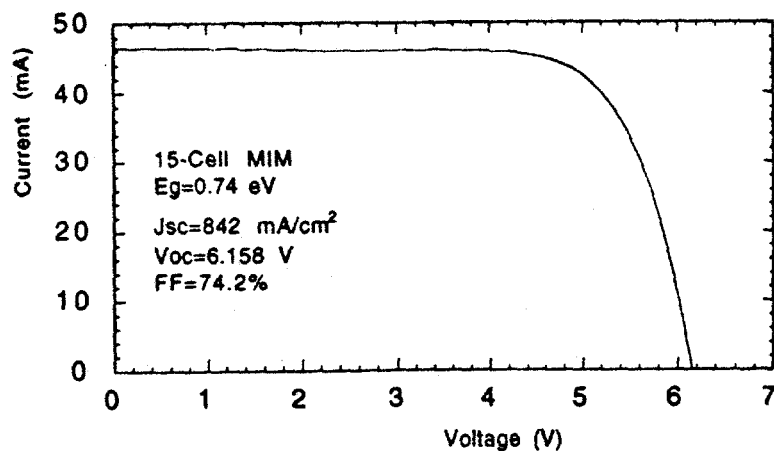


Figure 5.—Current-voltage (I-V) characteristics of a MIM, with  $E_g=0.74$  eV, measured under high-intensity illumination.

We also performed high-intensity illumination measurements on our non-optimized lattice-mismatched MIMs ( $E_g=0.55$  eV). In Figures 6 and 7, data is presented for variations of  $V_{oc}$  and FF as a function of  $I_{sc}$ , respectively. The diode ideality factor for these device was only slightly above unity. The  $V_{oc}$  values for these devices were, however, somewhat lower than the values we have obtained with our conventional planar cells in the past. The FF values, on the other hand, remained close to the 60% mark in the  $I_{sc}$  range of interest.

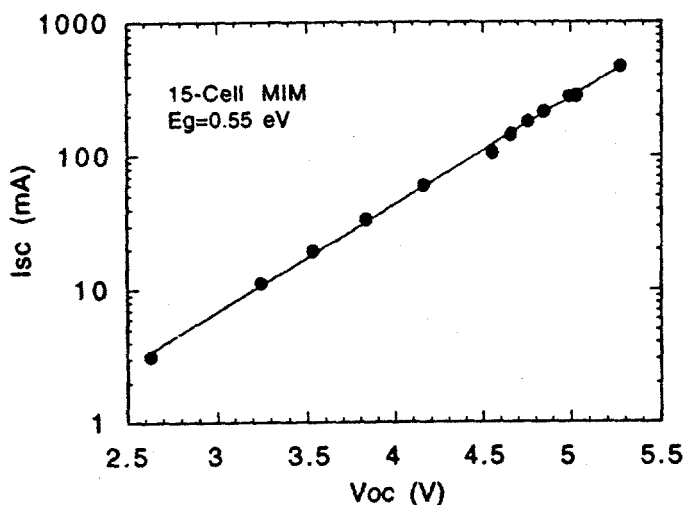


Figure 6.—Variation of the open-circuit voltage ( $V_{oc}$ ) with the short-circuit current ( $I_{sc}$ ) for a MIM with  $E_g=0.55$  eV.

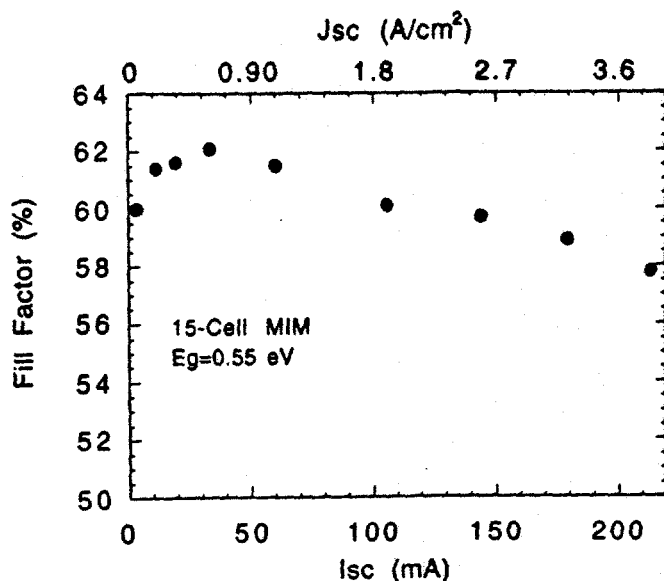


Figure 7.—Variation of the fill factor (FF) with the short-circuit current ( $I_{sc}$ ) and the short-circuit current density ( $J_{sc}$ ) for a MIM with  $E_g=0.55$  eV.

For a cell with  $E_g=0.55$  eV (and a quantum yield of unity), operating under a greybody emitter ( $T=1200$  K), the expected  $J_{sc}$  is about  $3.72$  A/cm<sup>2</sup>. An I-V curve for a module with  $J_{sc}$  of  $3.87$  A/cm<sup>2</sup> is shown Figure 8. The  $V_{oc}$  and FF values were  $4.849$  V ( $323$  mV per cell) and  $57.8\%$ , respectively.

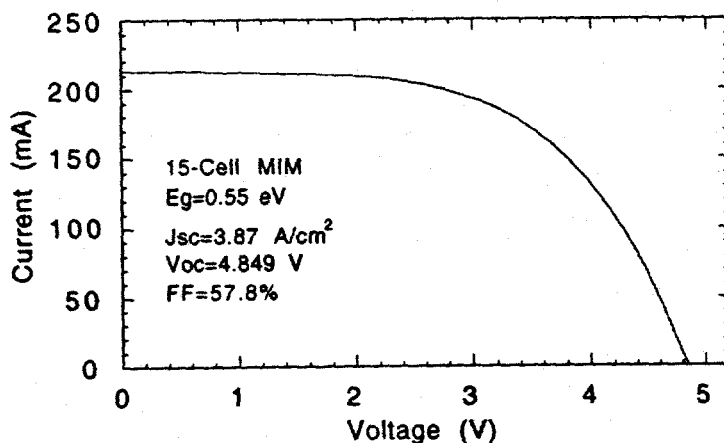


Figure 8.—Current-voltage (I-V) characteristics of a MIM, with  $E_g=0.55$  eV, measured under high-intensity illumination.

#### Quantum yield (QY) data

The quantum yield (QY) data for our lattice-matched MIMs ( $E_g=0.74$  eV) showed extremely good collection efficiency. This data is given in Figure 9. Two external and one internal QY curves are shown in the figure. The external QY curves illustrate the effectiveness of the BSR in improving the current collection. As expected, the increase in current collection due

to the BSR is mainly seen near the bandedge. It should be noted that this device had no anti-reflection (AR) coating. The internal QY was calculated by measuring the reflectivity,  $R(\lambda)$ , of the device (prior to processing), and dividing the external QY by  $[1-R(\lambda)]$ . As shown in the figure, the internal QY for this module was near or at unity for most of the wavelength range of interest.

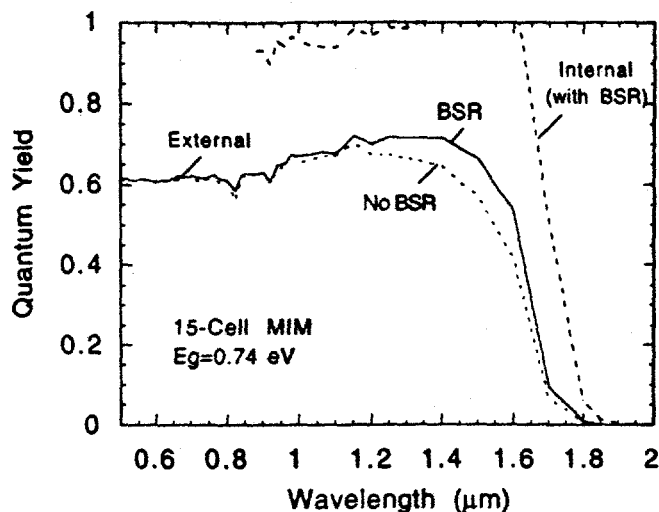


Figure 9.—Quantum yield data for a MIM with  $E_g=0.74$  eV.

A QY curve for our lattice-mismatched device ( $E_g=0.55$  eV) is shown in Figure 10. Similar data to that shown in Figure 9 is presented. This QY data was very encouraging because the calculated internal QY was over 80% over all the wavelengths of interest.

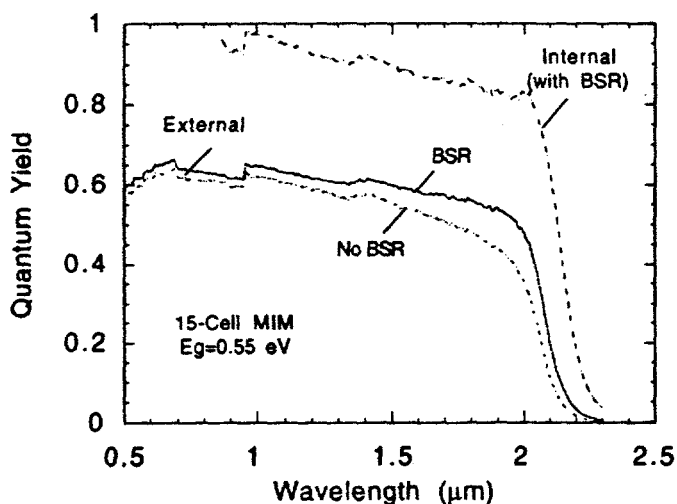


Figure 10.—Quantum yield data for a MIM with  $E_g=0.55$  eV.

### Reflectivity data

To maximize optical recycling efficiency, the reflectivity of the MIMs (with a BSR), beyond the bandedge wavelength must be as high as possible. This allows for the majority of the non-convertible IR photons to be reflected back to the TPV emitter for recycling. Therefore, we performed reflectivity measurements of the modules with bandgaps of 0.74 and 0.55 eV, before they were processed into complete devices. Both structures had thick ( $\sim 2 \mu\text{m}$ ) gold BSRs. The results are shown in Figures 11 and 12.

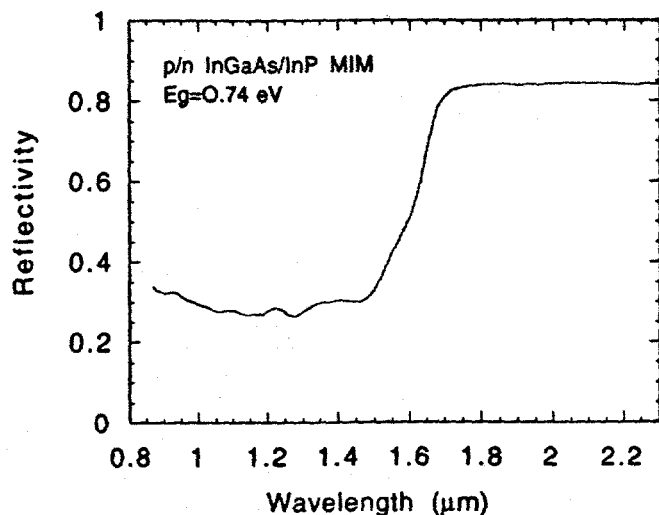


Figure 11.—Reflectivity data for a MIM structure, with  $E_g = 0.74 \text{ eV}$ , without anti-reflection coating.

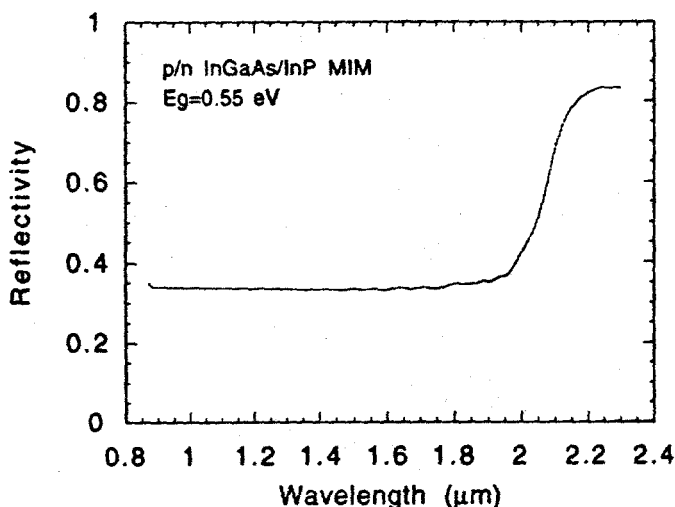


Figure 12.—Reflectivity data for a MIM structure, with  $E_g = 0.55 \text{ eV}$ , without anti-reflection coating.

As seen in the figures, the out-of-band IR reflectivity ( $\lambda > \sim 2 \mu\text{m}$ ) for these structures was 84 and 83% for bandgaps of 0.74 and 0.55 eV, respectively. The gold BSR IR reflectivity, as measured through a semi-insulating InP substrate, is greater than 95%. The somewhat lower reflectivities measured with the MIM structures were, therefore, due to the free-carrier

absorption within the doped layers of the devices. By modifying the module design, we expect to increase this reflectivity to near 90%.

## Summary

The results presented in this work are summarized below:

1. We fabricated and tested both lattice-matched and lattice-mismatched InGaAs/InP monolithic interconnected modules (MIM), with bandgaps of 0.74 and 0.55 eV, respectively. These devices had an active area of 1x1 cm and were comprised of 15 cells interconnected in series. They had a gold back surface reflector (BSR) layer to allow for efficient radiation recycling, when used in a TPV system.
2. MIM devices were tested under high-intensity illumination. The I-V results for the structures with a bandgap of 0.74 eV were:  $V_{oc}=6.158$  V and fill factor=74.2% for  $J_{sc}=842$  mA/cm<sup>2</sup>. The I-V results for the structures with a bandgap of 0.55 eV were:  $V_{oc}=4.849$  V and fill factor=57.8% for  $J_{sc}=3.87$  A/cm<sup>2</sup>.
3. The internal quantum yield for the MIM devices were determined to be at or near unity for the module with  $E_g=0.74$  eV, and over 80% for the module with  $E_g=0.55$  eV, over the wavelength range of interest.
4. The reflectivity as a function of wavelength for both lattice-matched and lattice-mismatched structures was measured to be  $\geq 83\%$  in the infrared (i.e.,  $\lambda > \sim 2\mu\text{m}$ ) region of the spectrum.

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