

# Efficient experimental verification of continuously-parameterized gate sets and analog quantum simulators

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We introduce the **randomized analog verification (RAV)** protocol for verification of quantum computers with continuously-parameterized gate sets, as well as for verification of analog quantum simulators. We show that **RAV requires fewer circuit repetitions** than cross-entropy benchmarking (XEB) to produce an equivalently precise estimate of the error rate. We demonstrate this **efficiency advantage** numerically and experimentally.



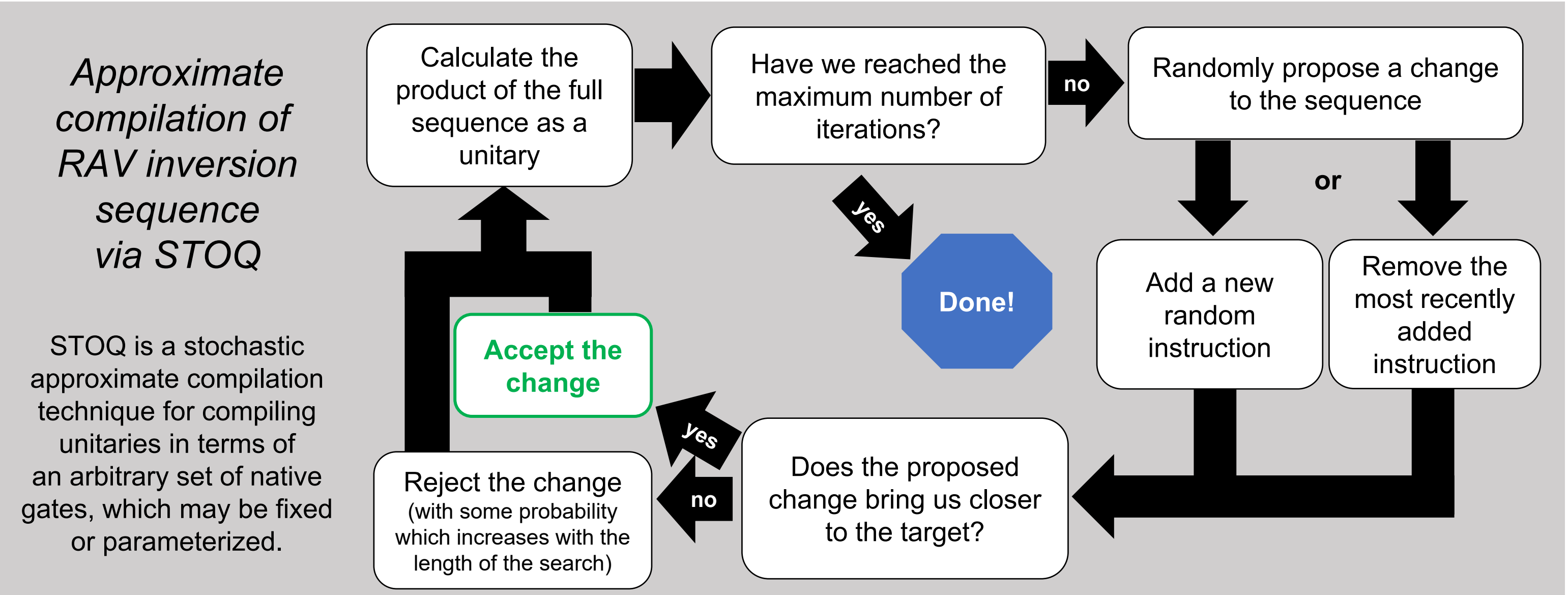
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Verification of gate-based devices	Building blocks	Sequence generation	Measurement efficiency	Scalability	Parameterized gate support
<b>Randomized benchmarking (RB)</b> $ 0\rangle \rightarrow \text{Random } n\text{-qubit Clifford} \rightarrow \text{Random } n\text{-qubit Clifford} \rightarrow \dots \rightarrow \text{Random } n\text{-qubit Clifford} \rightarrow \text{Inversion } n\text{-qubit Clifford} \rightarrow  0\rangle$	Random $n$ -qubit Cliffords	Efficient	Efficient	Infeasible for $n \gg 2$	No
<b>Cycle benchmarking (CB)</b> $ 0\rangle \rightarrow \text{Pauli layer} \rightarrow \text{Native } n\text{-qubit Clifford} \rightarrow \text{Pauli twirl} \rightarrow \text{Native } n\text{-qubit Clifford} \rightarrow \text{Pauli twirl} \rightarrow \text{Inversion layer of Paulis} \rightarrow  0\rangle$	Native $n$ -qubit Cliffords, random single-qubit Cliffords	Efficient	Efficient	Scalable to large $n$ (for $n$ -qubit native gate)	No
<b>Mirror RB</b> $ 0\rangle \rightarrow \text{Random 1-, 2-qubit Clifford layer} \rightarrow \dots \rightarrow \text{Random 1-, 2-qubit Clifford layer} \rightarrow \text{Twirled inverse} \rightarrow \dots \rightarrow \text{Twirled inverse} \rightarrow  0\rangle$	Random native single-qubit and two-qubit Cliffords	Efficient	Efficient	Scalable to large $n$	No
<b>Cross-entropy benchmarking (XEB)</b> $ 0\rangle \rightarrow \text{Random native gate layer} \rightarrow \text{Random native gate layer} \rightarrow \dots \rightarrow \text{Random native gate layer} \rightarrow  \varphi\rangle$	Random native gates	Efficient	Inefficient	Infeasible for $n \gg 50$	Yes
<b>Randomized analog verification (RAV)</b> $ 0\rangle \rightarrow \text{Random native gate layer} \rightarrow \text{Random native gate layer} \rightarrow \dots \rightarrow \text{Random native gate layer} \rightarrow \text{Approx. inversion sequence} \rightarrow  0\rangle \text{ (approx.)}$	Random native gates	Inefficient	Efficient	Infeasible for $n \gg 10$	Yes

Verification of analog simulators	Error sensitivity	Hardware requirements	Scalability
<b>Time-reversal benchmarking</b> $ 0\rangle \xrightarrow{e^{-iH\tau}}  \psi\rangle \xleftarrow{e^{+iH\tau}}  0\rangle$	Incoherent noise	Implement time reversal of Hamiltonian	Scalable to large $n$
<b>Cross-entropy benchmarking (XEB)</b> $ 0\rangle \rightarrow \dots \rightarrow n \text{ random steps} \rightarrow  \varphi\rangle$	Coherent errors, incoherent noise	Implement programmable Hamiltonian terms	Infeasible for $n \gg 50$
<b>Randomized analog verification (RAV)</b> $ 0\rangle \rightarrow \dots \rightarrow n \text{ random steps} \rightarrow \text{inversion steps} \rightarrow  \varphi\rangle$	Coherent errors, incoherent noise	Implement programmable Hamiltonian terms	Infeasible for $n \gg 10$



## Variance of RAV and XEB fidelity estimates

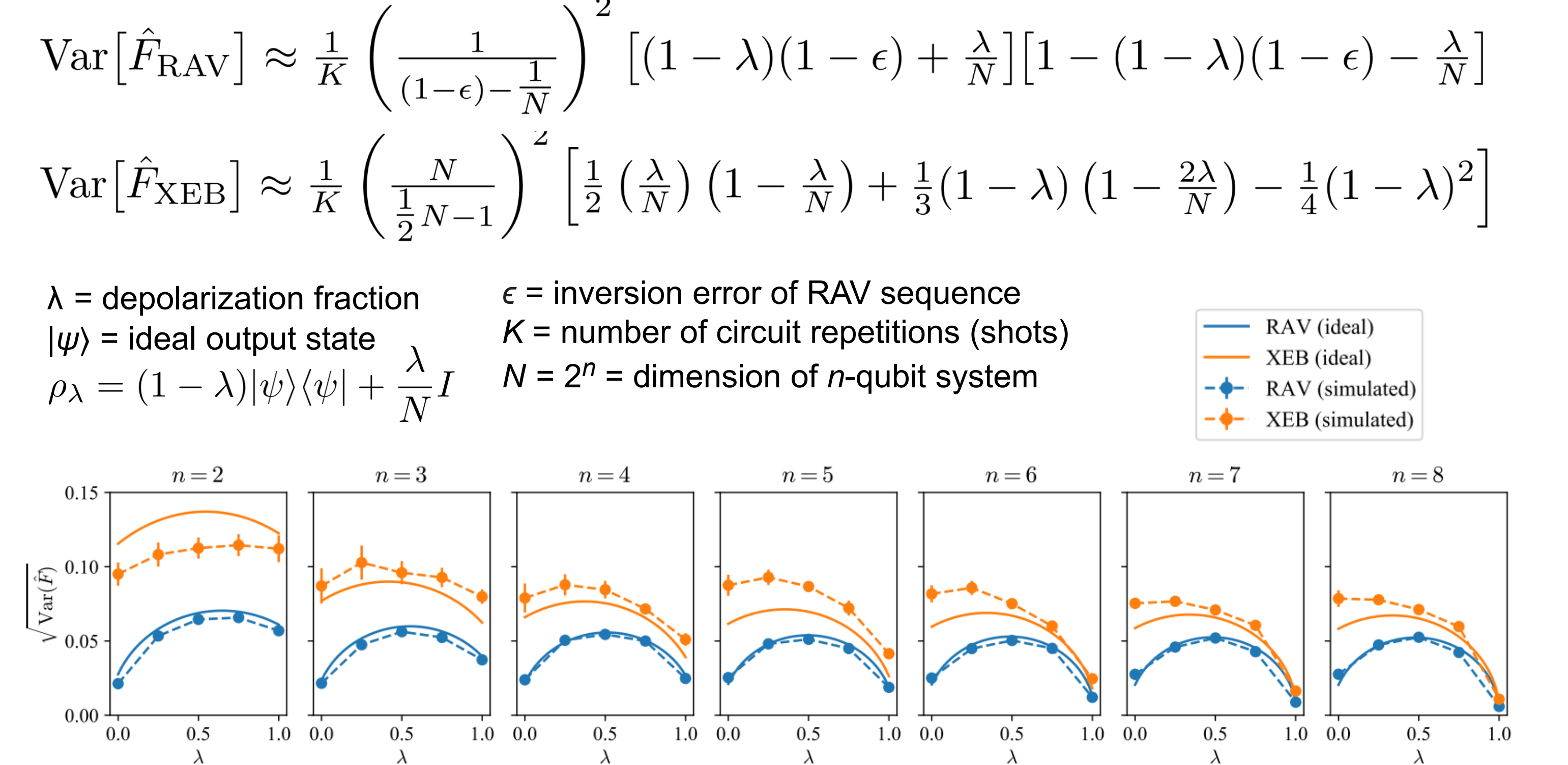
Fidelity estimates for a single circuit execution:

$$\hat{F}_{\text{XEB}} = \frac{\sum_x P(x)Q(x) - \frac{1}{N}}{\sum_x P(x)^2 - \frac{1}{N}} \quad \hat{F}_{\text{RAV}} = \frac{Q(x_0) - \frac{1}{N}}{P(x_0) - \frac{1}{N}}$$

$P(x)$  = ideal probability of measuring  $x$   
 $Q(x)$  = observed probability of measuring  $x$   
 $x_0$  = expected RAV output state  
 $N = 2^n$  = dimension of  $n$ -qubit system

Measurement of RAV fidelity estimates is more efficient than XEB fidelity estimates, since only one output probability must be measured.

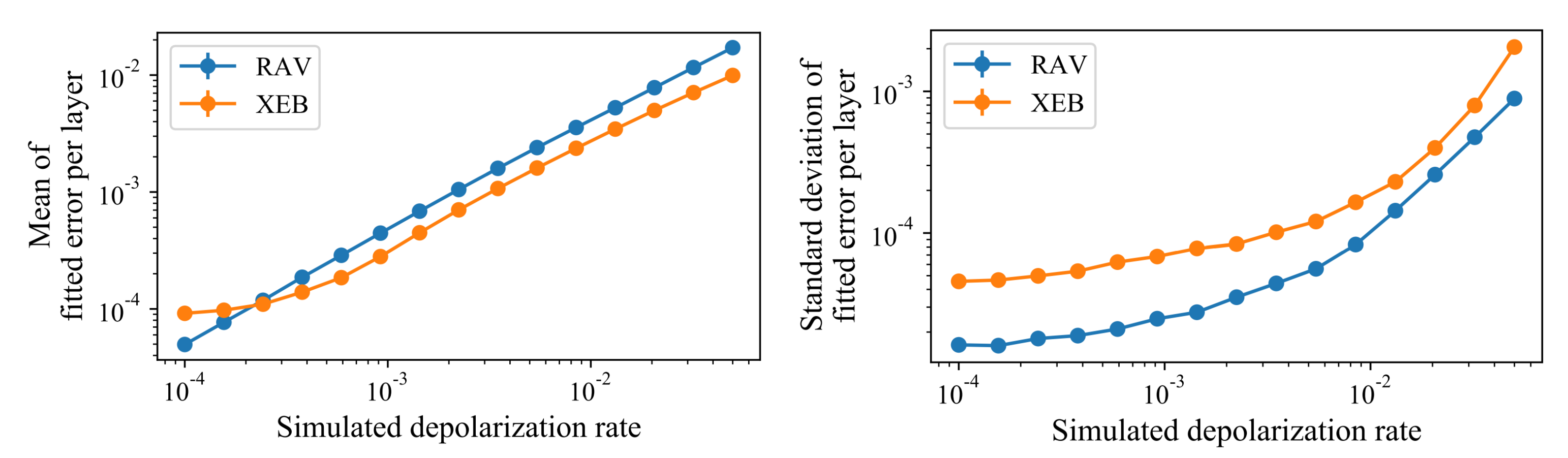
We demonstrate this by calculating the variance of the RAV and XEB fidelity estimates under varying amounts of depolarization:



We observe that RAV fidelity estimates have a **smaller standard deviation** than XEB fidelity estimates in all cases, and especially for smaller qubit count  $n$  and smaller depolarization fraction  $\lambda$ . This means that for a fixed number of shots  $K$ , RAV circuits will provide a fidelity estimate with **lower uncertainty** than XEB circuits.

## Numerical demonstration

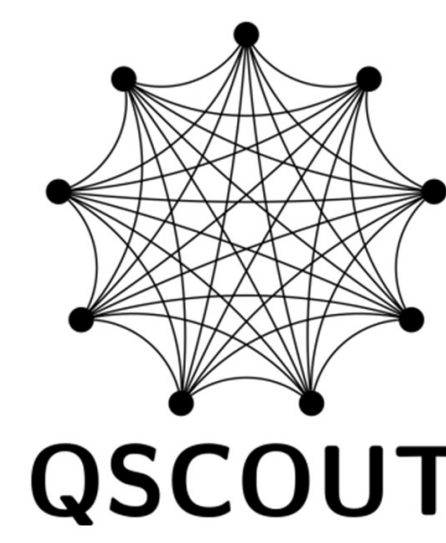
We simulated 50 RAV and 50 XEB sequences for a five-qubit system under varying depolarization rates. Statistics of the error estimates:



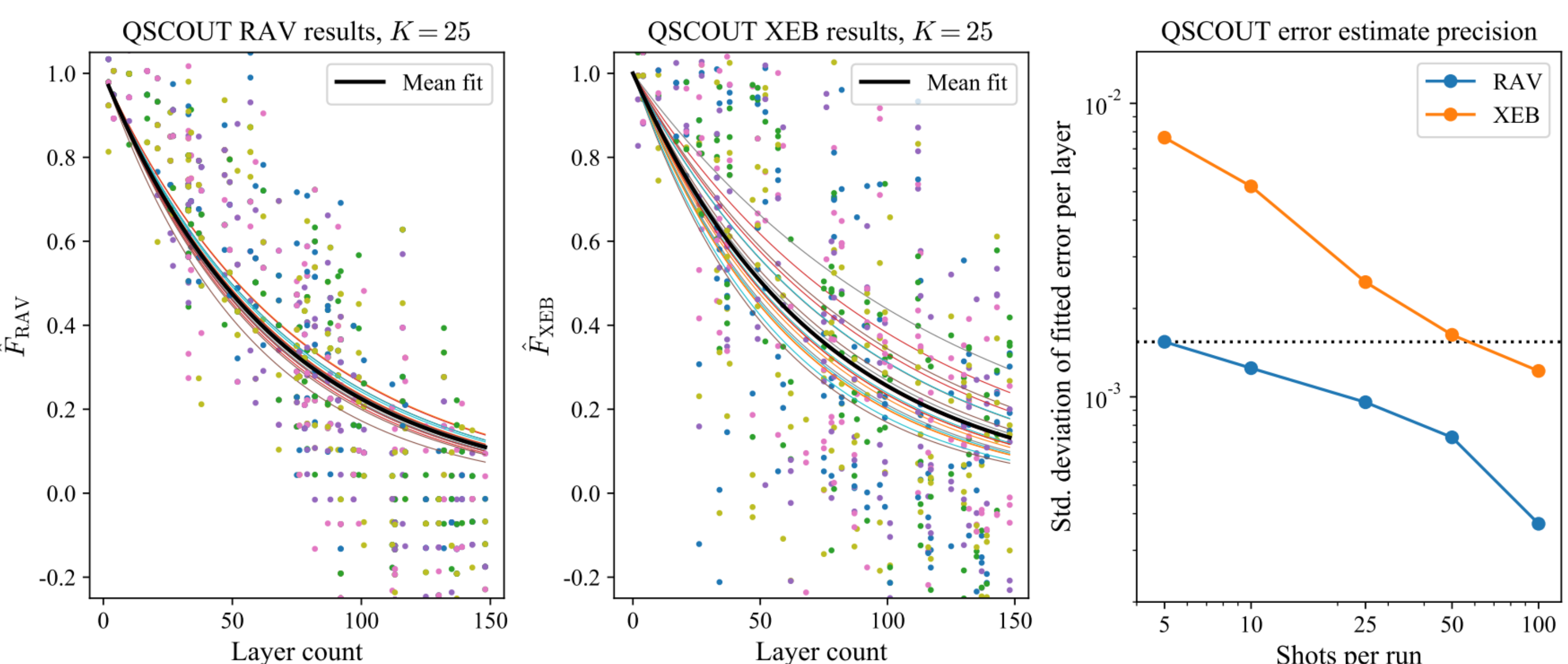
For this simulated five-qubit system, we observe that RAV error estimates have **significantly smaller (by a factor of 2 to 3)** standard deviation than XEB error estimates given the same number of circuit repetitions.

This suggests that RAV would require **4x-9x fewer shots than XEB** to obtain error estimates to some desired precision.

## Experimental demonstration



We tested 50 RAV and 50 XEB two-qubit sequences on a trapped-ion quantum processor at the Quantum Scientific Computing Open User Testbed (QSCOUT) operated by Sandia National Laboratories.



The RAV runs on QSCOUT produce error estimates with significantly smaller standard deviation (by a factor of 2.5 to 5) than those obtained from XEB runs. This suggests that RAV would require **6x-25x fewer shots than XEB** to produce an equivalently-precise error estimate.