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# From naïve to smart: leveraging offloaded capabilities to enable intelligent NICs

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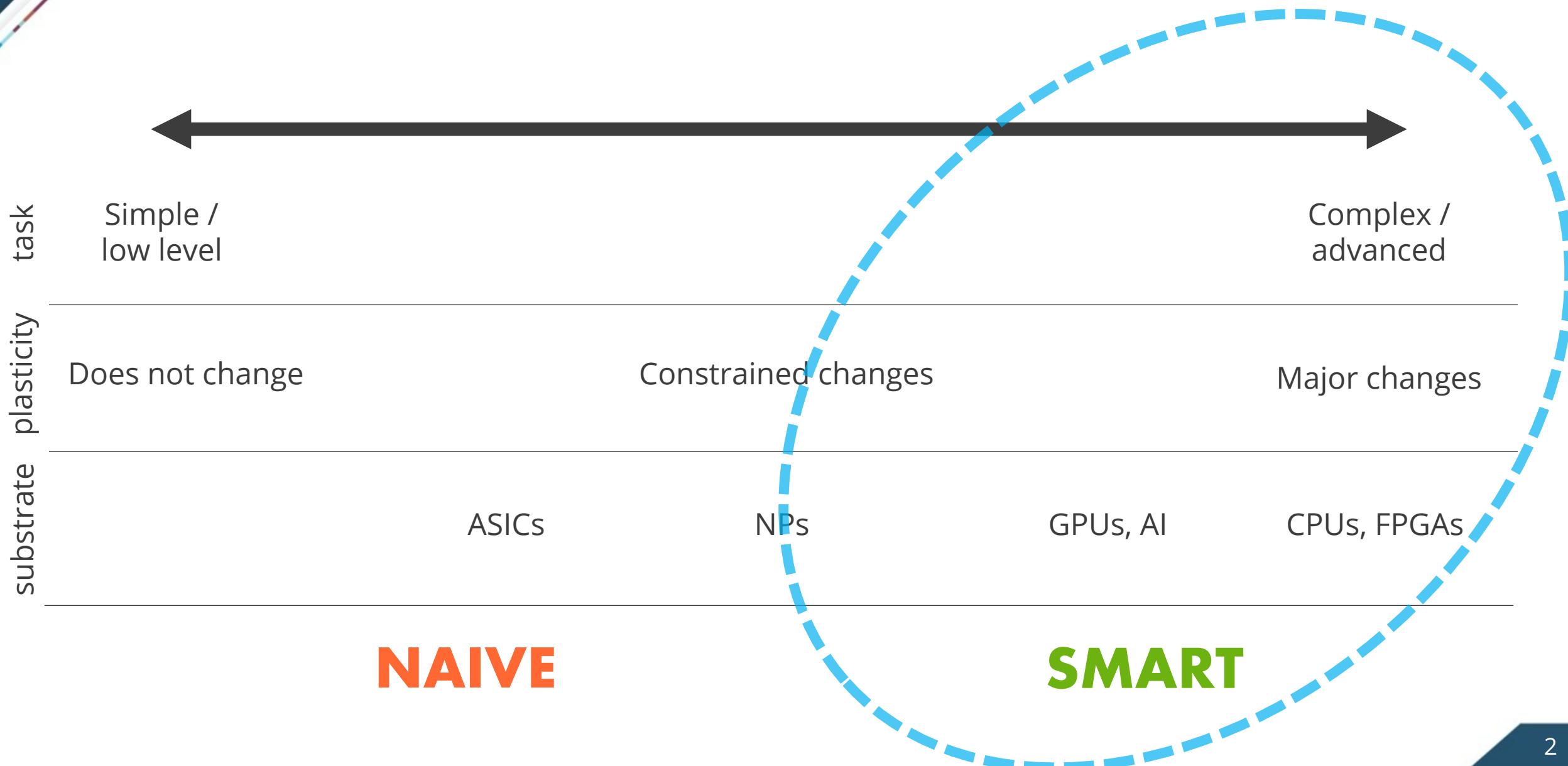
2021-12-02

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# What is a SmartNIC?

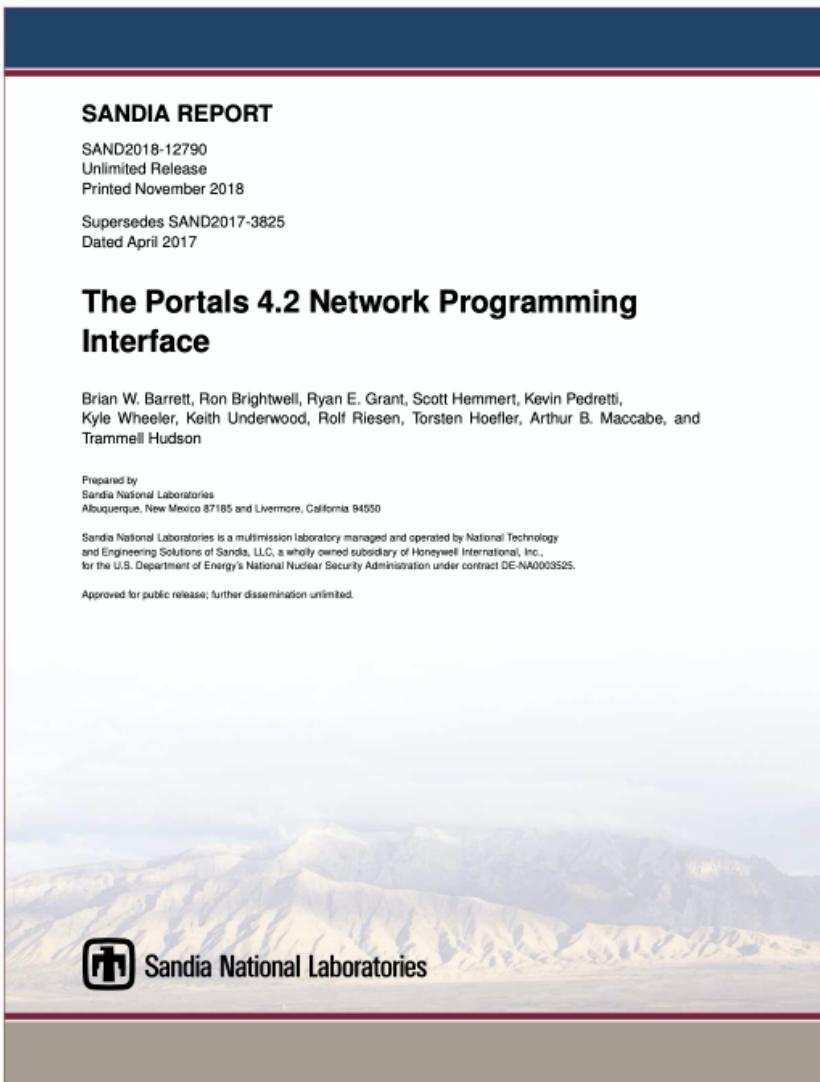




# What is a SmartNIC?

- A NIC that can be configured to perform arbitrary tasks.
- Coordination of heterogeneous task-specific accelerators (GPUs, AI engines, etc.)

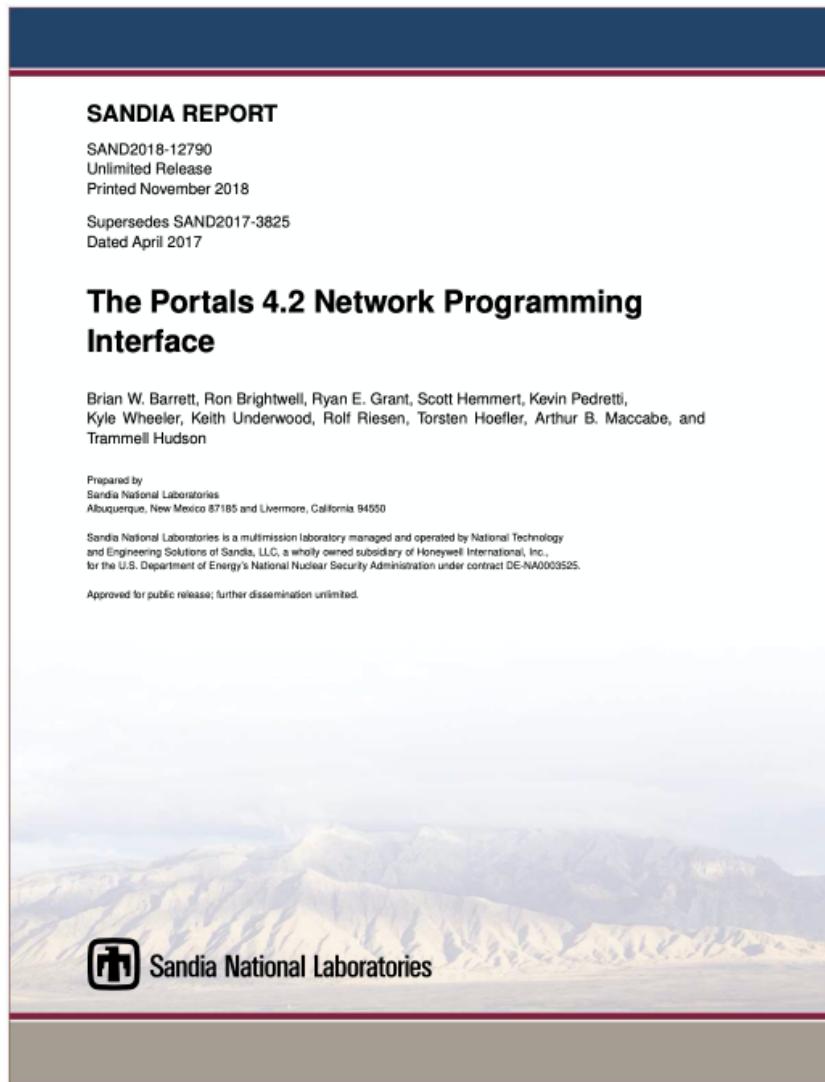
# Portals Network API



- Network programming API
- Hardware oriented



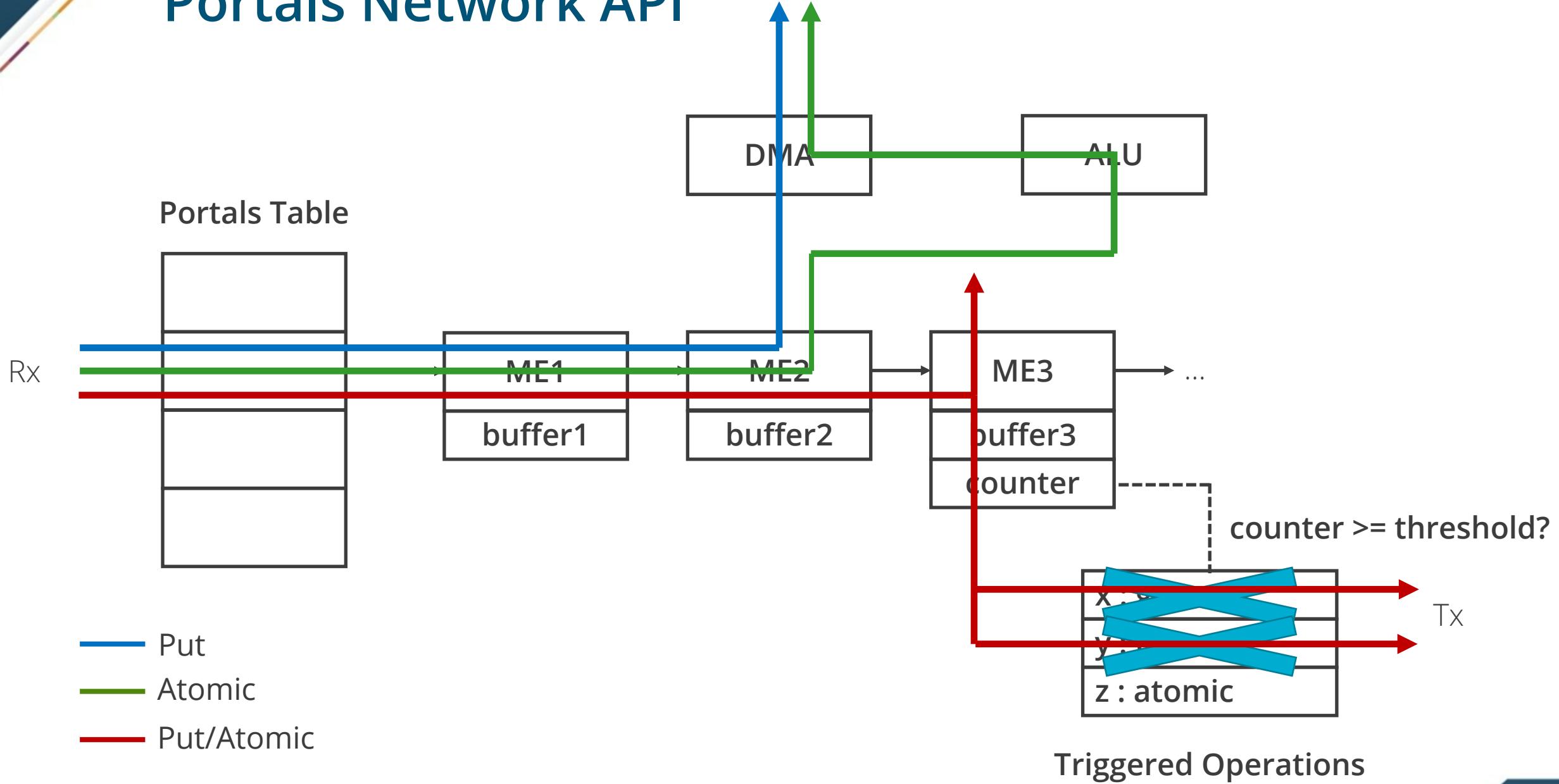
# Portals Network API



- How can a Portals NIC support 'intelligent' offloads?



# Portals Network API



# Extending Portals

## Current (4.2)

Target-side resource management:  
buffers, offsets



+ ME-specified operations

Transient triggered operations



+ persistent triggered operations

Triggering conditions:  $\geq$



+ strict indexing (==)

Counter updates: bytes, writes



+ conditional counter updates

## Proposed

# Extending Portals

+ ME-specified operation:  
*counter* = (*cond* ? *val1* : *val2*)

```
if [buffer] <= 0:  
    counter = x  
else:  
    counter++
```

# Extending Portals

## Current (4.2)

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## Proposed

# Extending Portals

Rank 0

Put( Rank 1, x )

ME1

MUL  
[buf] = [buf]\*x

Rank 1

Triggered Operations

1 : TriggeredPut( Rank 1, -- )

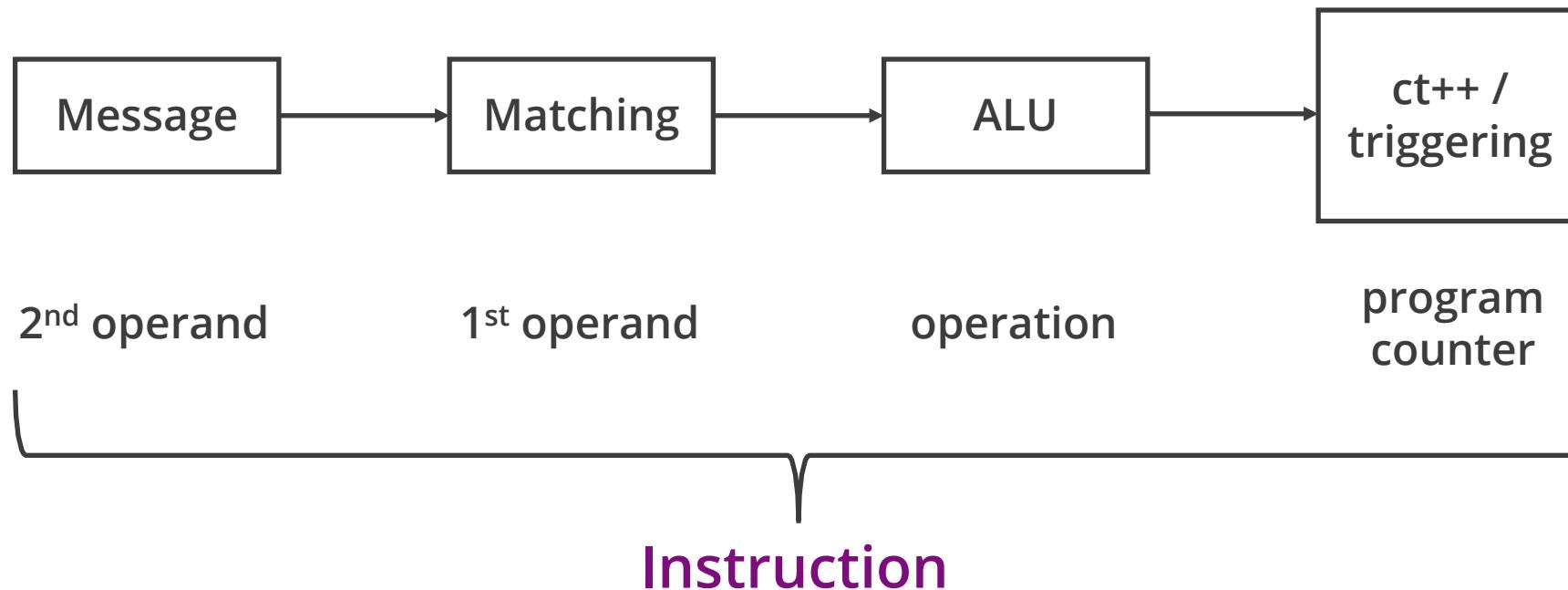
ME2

COND CT  
UPDATE  
If [buf] <= 0:  
counter = 10  
else:  
counter = 2

2 : TriggeredPut( Rank 0, 0 )

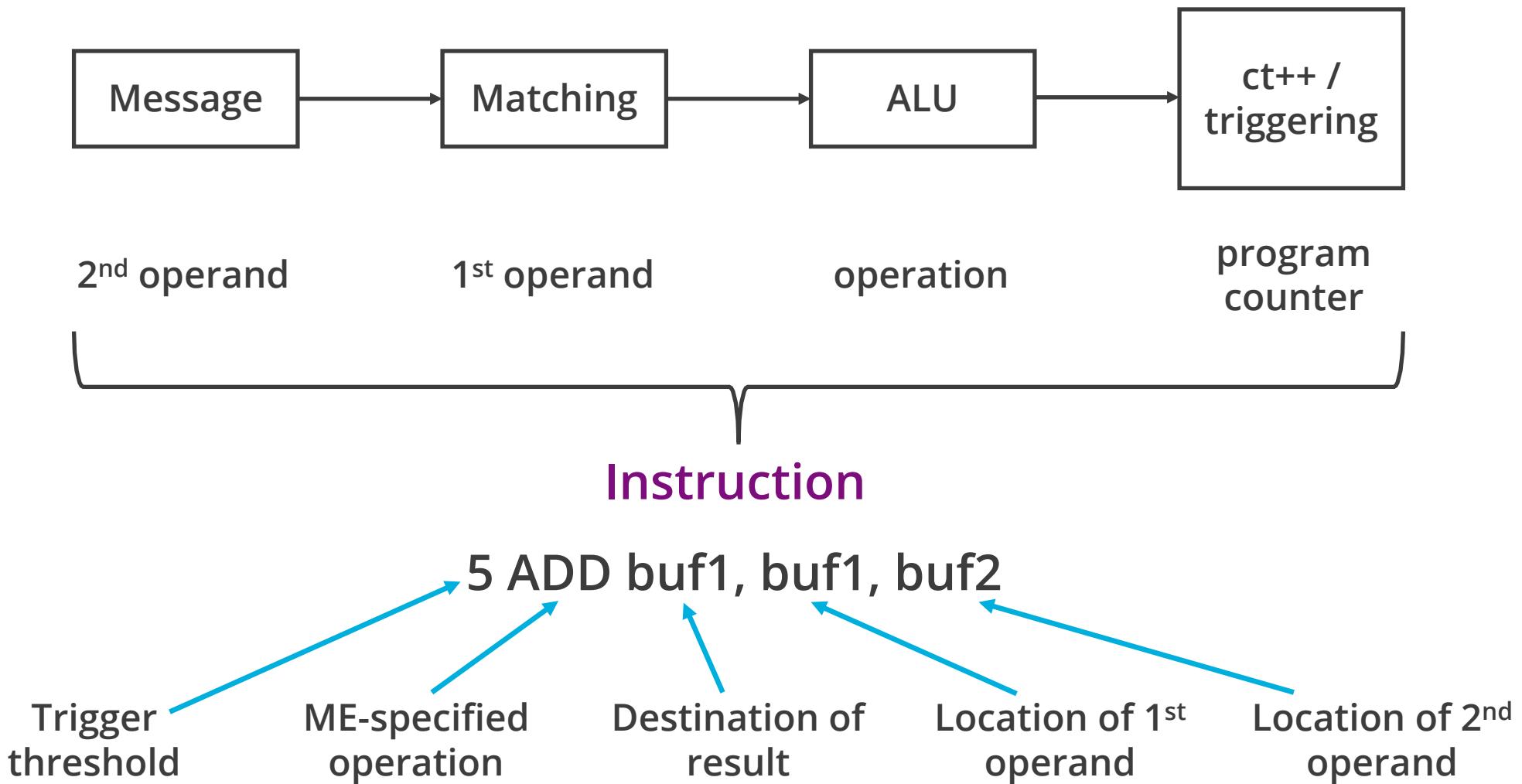
10 : TriggeredPut( Rank 0, 1111 )

# Enabling Intelligence



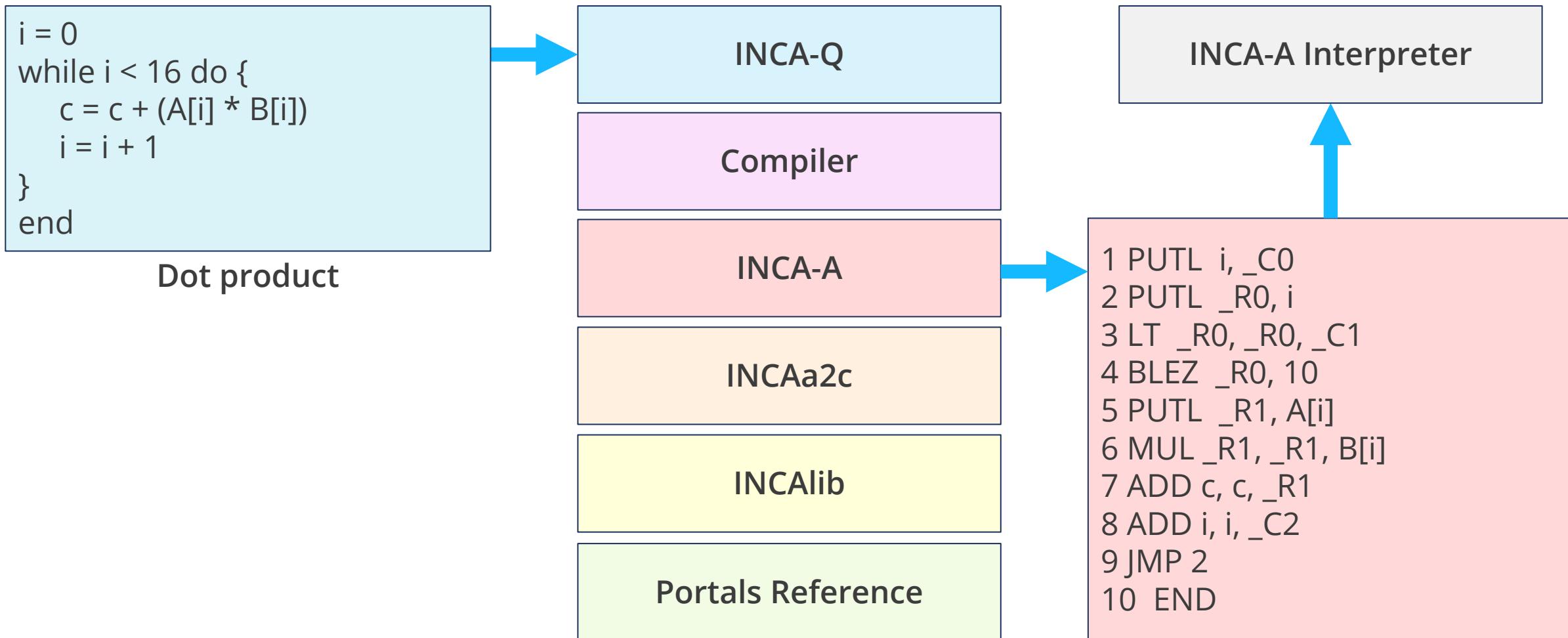
Program: Sequence of instructions sharing the same counter  
Turing Equivalent

# INCA: In-Network Compute Assistance





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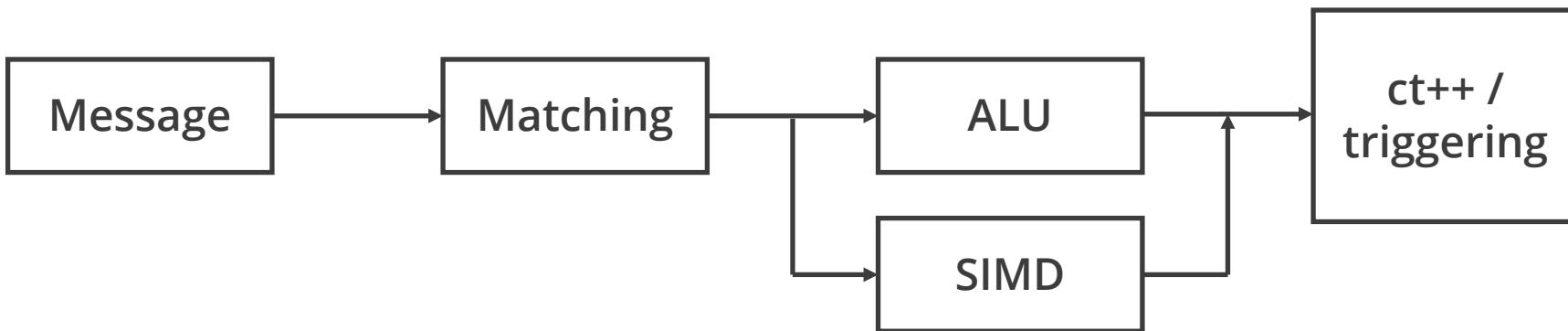
# INCA: In-Network Compute Assistance

Kernel	usecs
convolution	190.49
dot-product	31.76
hadamard-product	31.81
linear-interpolation	212.85
matrix-multiplication	1051.20
matrix-transpose	23.62
unpack	59.93

3.2 GHz Haswell:  
139.5 -- 10.5 usecs

400 MMsgs/s, 1ns scratchpad  
8KiB payloads

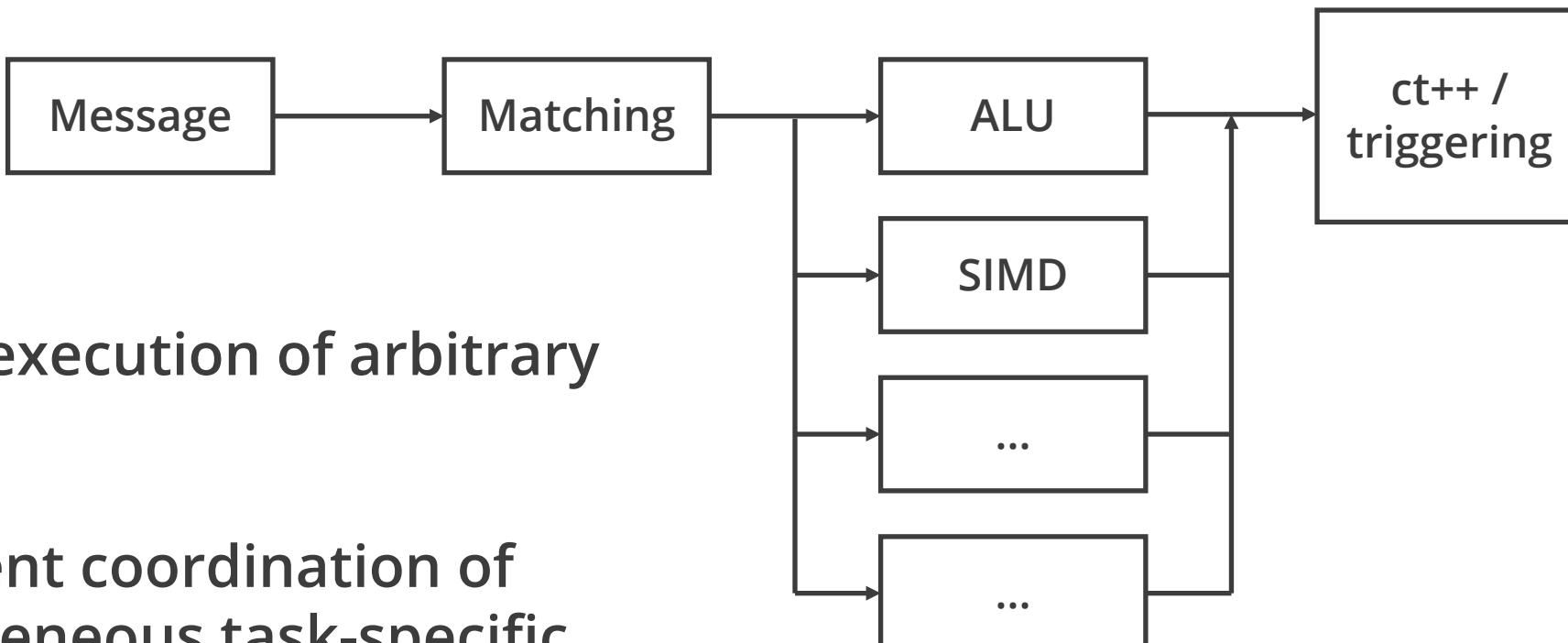
# Enabling Intelligence



2 MULM A[\_Z0], A[\_Z0], B[\_Z0], 256

Kernel	usecs	usecs
dot-product	31.76	23.74
hadamard-product-pc	31.81	0.176
matrix-multiplication-p	1051.20	819.61
matrix-multiplication-pc	1051.20	153.60

# Enabling Intelligence



- Enable execution of arbitrary tasks
- Intelligent coordination of heterogeneous task-specific accelerators



# Thank You

- Whit Schonbein  
[wwschon@sandia.gov](mailto:wwschon@sandia.gov)
- Portals specification  
<https://cs.sandia.gov/Portals/> [BROKEN; but see researchgate]
- Portals reference implementation  
<https://github.com/Portals4>