EM.421
MIT SDM Capstone Project
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(U) MIT Capstone Project Assignment 2- Proposed System Design

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Disclaimer: This report analyzes a hypothetical scenario using systems engineering principles for a graduate course assignment. The opinions are those of the authors and do not necessarily represent the opinions of LLNL, LLNS, DOE, NNSA or the US government.

Introduction and Background

"First Production Unit (FPU) in 5": The National Nuclear Security Administration (NNSA) and upper management at Lawrence Livermore National Lab (LLNL) have set a long-term goal of decreasing the development cycle of new systems to five years or less. The driver is to be able to respond to emerging situations which require new capabilities more quickly. There are numerous approaches to solve the problem, and any single improvement is likely insufficient. We analyzed a variety of possible solutions at varying levels of specificity as shown in Figure 1. We chose to focus our scope on the optimization of existing documentation processes because it is the least likely to add additional risk and can be accomplished with lower investment than other potential solutions.

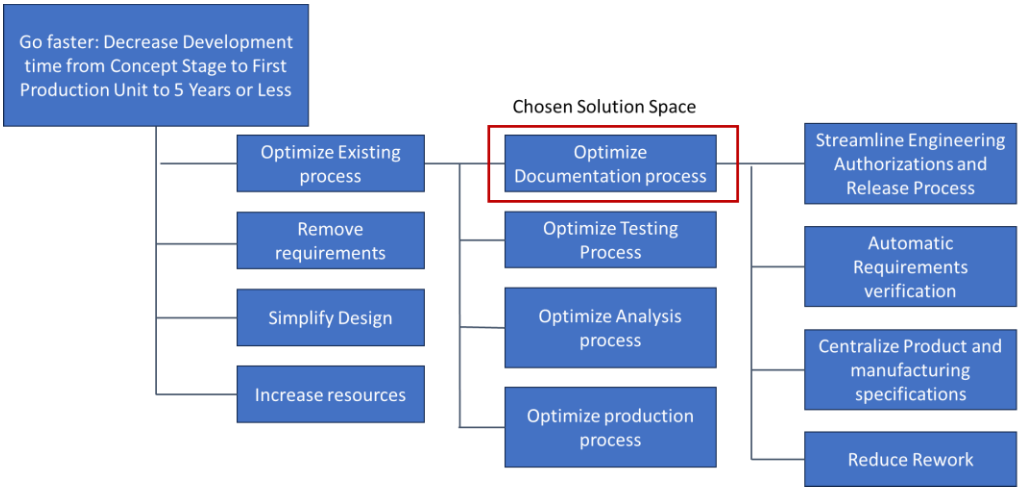
Go faster: Decrease Development time from Concept Stage to First Production Unit to 5 Years or Less
Chosen Solution Space
Optimize Existing process
Remove requirements
Simplify Design
Increase resources
Optimize Documentation process
Optimize Testing Process
Optimize Analysis process
Optimize production process
Streamline Engineering Authorizations and Release Process
Automatic Requirements verification
Centralize Product and manufacturing specifications
Reduce Rework

Figure 1 - Possible solution space for decreasing the development cycle from concept to first production unit.

Our capstone project focuses on modernizing the design development processes, production development processes, and communication between design agencies (DAs) and production agencies (PAs) to decrease the timeline from conceptual study to first production unit. To focus our scope, we are looking only at interactions between the DA, Lawrence Livermore National Lab (LLNL), and the PA, Kansas City National Security Campus (KCNSC). Our project's system problem statement (SPS) is below:

> ***TO*** *decrease the time from conceptual study to first production unit* ***BY*** *centralizing design development, production development, and information exchange* ***USING*** *digital engineering techniques.*

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Figure 2 illustrates a functional decomposition of the current design qualification process and defines the boundaries for our proposed system. The formal links to these functions are mostly software programs; the type of software used will vary by product, activity, and organization. However, some software is shared across sites to communicate information. For example, PRIME is a requirement tracking software used for archiving engineering releases, design definitions, and specifications and is the only software that is managed across sites.

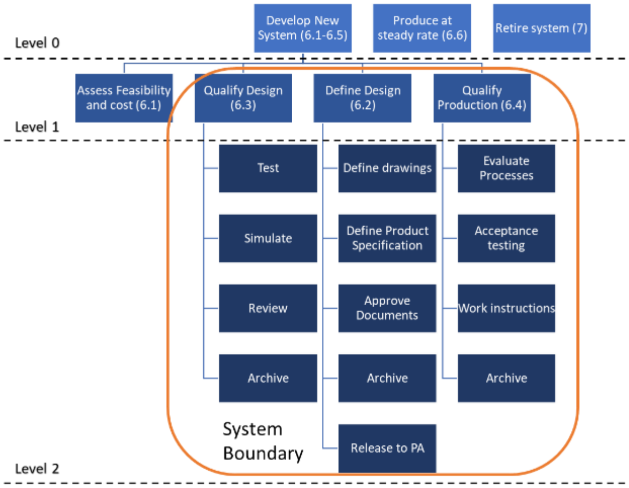

Figure 2 - Functional decomposition of existing system with system boundary defined.

Stakeholder Analysis

Stakeholder Network: The stakeholders and their needs for this system were compiled into the diagram below (Figure 3). The number of stakeholders and the presence of multiple value loops made determining the level of stakeholder importance a challenge. We chose to focus on the main groups using and benefiting from the system, and those whose buy-in is needed to successfully implement the system.

Metrics: Of the needs of the groups identified, we chose two key metrics to evaluate the system by: schedule reduction and implementation cost. The system needs to reduce the time taken for the DA and PA to settle on a final design and keep implementation costs low to improve adoption rates.

Table 1 - Key stakeholder needs defined and ranked

Need	Need Weight	Need Provided by the System	Performance Metric (units)	Stakeholders Interested in Metrics
1	0.75	**Reduce coordination time between the DA and PA**, therefore decreasing the time to first production unit	Delta in communication and approval effort **(engineer hours/year)**	DOE, DA Engineers, PA Engineers
2	0.25	**Keep implementation costs low** to improve adoption rate	Amortized cost of system over 5 year ROI **($)**	DOE

There are several high importance needs and stakeholders identified for our system, but most relate to cost or schedule, so the metrics listed above are sufficient to fulfill most of these needs. A high importance stakeholder who's need is not covered by the metrics is the Nuclear Enterprise Assurance (NEA) with its regulations on security. Our assumption, stated in the next section, is that our system will meet all security regulations, and so it was not factored into the metrics by which we rank the architectural decisions.

We acknowledge that there will be several other needs that are not be met by the system within the current scope of what we have set out. One example is that the system misses out on is scalability and adoption at other sites. Due to the focused nature of this analysis, solely looking at the LLNL and KCNSC interactions, an analysis for the other 12 NNSA agencies was not performed. Our intent is to use

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LLNL and KCNSC as a case study for implementation across the DOE enterprise. Traditionally, each site has their own slightly different way of doing things, so we expect this to be a challenge to overcome in the future.

Another limitation of our stakeholder analysis is the non-functional requirement that the system be easy to use. The users are high importance stakeholders, the DA and PA Engineers and the Database owners, and if they are not able to use the system easily then there is an increased likelihood that implementation will fail to achieve the desired results. Care should be taken to ensure that "Ease of Use and Maintainability" is an emergent property of the system.

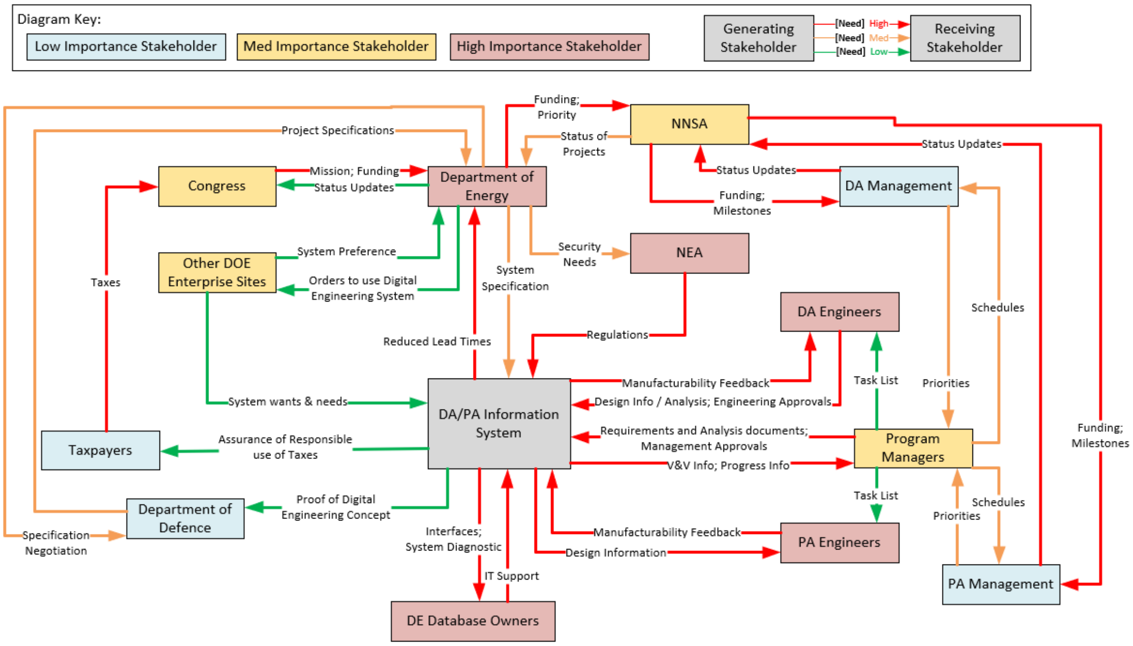

Figure 3 - Stakeholder network for the system

System Architecture Analysis

We designed a simplified model to assess the impact of our chosen architectural decisions on the time to FPU. Some key assumptions in our modeling approach are:

- Proposed architecture can meet all quality requirements.
- Proposed architecture can meet all security requirements.

We identified 11 work activities that impact the development process and estimated a duration as well as the number of repetitions of the activity that must occur during the development cycle. Currently, these are treated as serial processes. Dependencies and parallel work should be evaluated in future iterations with higher fidelity estimates. For each of our architectural decision options, we assign an impact factor on the duration of each activity with a scale of 0 – 1 where 1 is 100% of the original time and 0 is 0% of the original time. We also assign an estimated implementation cost for each decision option. Our tradespace model then calculates the time to FPU by summing the durations multiplied by the repetitions and the impact factor. In the current implementation, we assume that all activities occur

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serially; future iterations may factor in the effect of dependencies and shred resources. The total cost is the sum of the implementation cost for each decision. A visualization of our model is shown in Figure 4.

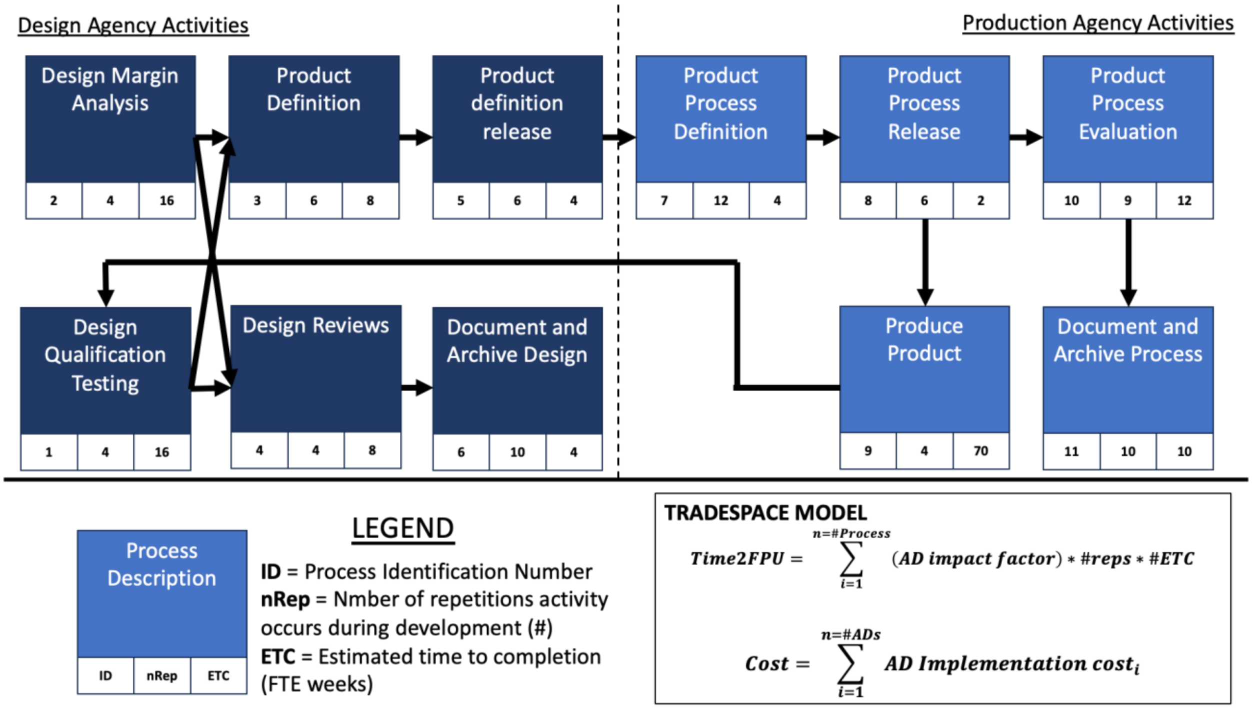
Design Agency Activities
Production Agency Activities
Design Margin Analysis
2 4 16
Product Definition
3 6 8
Product definition release
5 6 4
Product Process Definition
7 12 4
Product Process Release
8 6 2
Product Process Evaluation
10 9 12
Design Qualification Testing
1 4 16
Design Reviews
4 4 8
Document and Archive Design
6 10 4
Produce Product
9 4 70
Document and Archive Process
11 10 10
LEGEND
Process Description
ID nRep ETC
ID = Process Identification Number
nRep = Nmber of repetitions activity occurs during development (#)
ETC = Estimated time to completion (FTE weeks)
TRADESPACE MODEL
Time2FPU = $\sum_{i=1}^{n=\#Process}$ (AD impact factor) * #reps * #ETC
Cost = $\sum_{i=1}^{n=\#ADs}$ AD Implementation cost$_i$

Figure 4 - Simple model for development cycle to FPU

Our architectural decisions and estimated time impact factors and cost are shown in Table 2. For each decision option, the values in the parentheses correspond to (Time Impact factor 0-1, cost in $k). The time factors vary between 0.6 and 1.2. We propose that time is saved with various features by reducing rework, eliminating unnecessary steps, and consolidating disjointed data repositories. The only architectural decision with a negative impact (increased time) is the ownership of the software; we assume sharing responsibility or assigning it to a higher governing body increases the complexity and latency in maintaining a streamlined software.

The 5,182 distinct architectures were simulated and the implementation cost and time to FPU were calculated as described in Figure 4. The results are plotted in Figure 5. As anticipated, there are no architectures that will bring the FPU time below the target level; the proposed digital engineering implementation is one of many improvements that will be required to reach the target. The decision that had the largest impact to time was decision ID=7, which was the decision related to the ownership of the software. Our reference architecture corresponds to option 1 of each architectural decision which results in the cheapest option closest to the existing implementation. We use this reference as a baseline comparison for our preferred architecture.

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Table 2 - Architectural decisions and tradespace model inputs (Impact Factor, Implementation Cost) with processes affected as described in Figure 5. Decision options corresponding to our preferred architecture are highlighted in bold..

ID	Decision	Option 1	Option 2	Option 3	Option 4	Processes Impacted
1	Engineering management software used	COTS (0.9,2000)	**COTS + modification** **(0.8,3000)**	Custom Build (0.85,5000)		3,4,5,6,7,8,11
2	Security Level of information exchange	**Low** **(0.8,500)**	High (0.9,1000)	Both (0.8,1500)		8,5,7,8,6,11
3	Automatic requirements verification	No (1.0,0)	**Yes** **(0.8,1000)**			8,5,7,8
4	Cross-Site Standardization	No (1.0,0)	**Yes** **(0.95,1000)**			5,7,8
5	Type of information exchanged cross-sites	2D Drawing (1.0,0)	3D CAD (0.8,1000)	**3D CAD, notes, tolerances, etc.** **(0.75,1500)**		5,7,8
6	Level of Granularity of Digital Engineering Model	System (0.9,500)	Sub-system (0.8,2000)	**Component** **(0.7,4000)**		1,5,7,8
7	Who owns authority of Management Software	**DA** **(1.0,1000)**	PA (1.0,1000)	Both (1.1,2000)	DOE (1.2,2000)	1,2,3,4,5,6,7,8,9,10,11
8	Built-in Design Analysis Software	No (1.0,0)	**Yes** **(0.8,1000)**			2,3
9	Approval process for information change/release	Serial (1.0,0)	**Parallel** **(0.6,1000)**			4,2

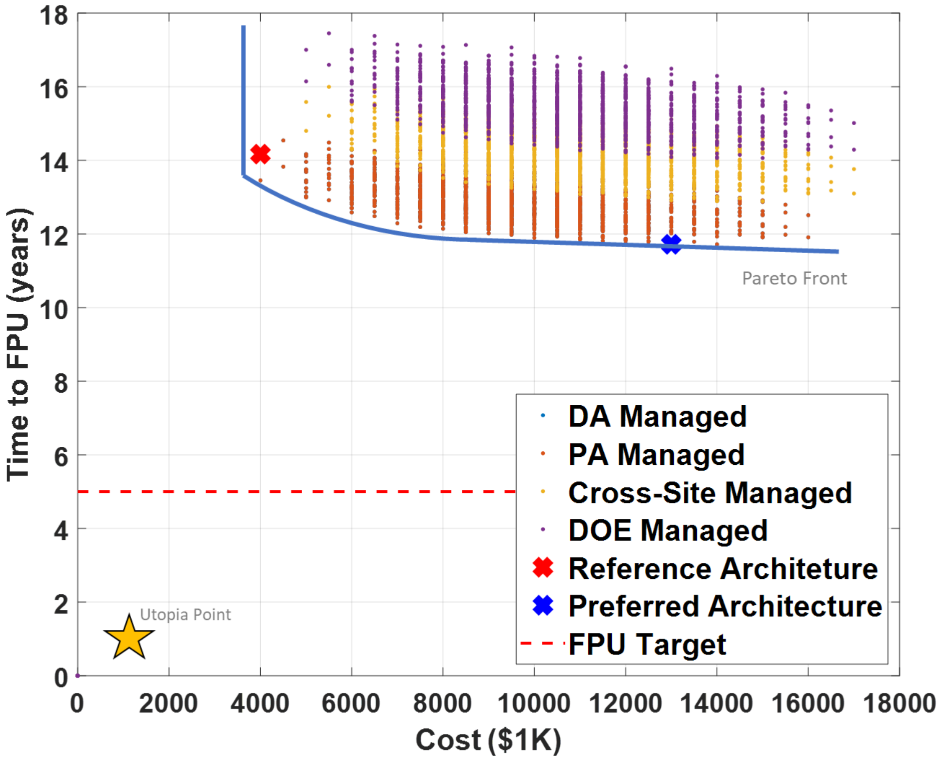

Figure 5 - Architecture Tradespace

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Proposed System Architecture

Our preferred architecture was selected by finding the minimum time to FPU of all the architectures. Our model predicts a decrease in time to FPU by 2.4 years compared to the reference architecture. The decision options corresponding to our preferred architecture are in bold in Table 2. It is worth noting that most architectural decisions for our proposed system do not impact the time it takes to produce parts, which is by far the largest contributor to the overall time, as seen in the Figure 7 in the Appendix. For this project, production improvements are outside of the scope. We recommend using our findings to motivate further study into the impact of reducing the production lead time.

Due to the uncertainties in the current estimates, more work is needed to confirm that this architecture is distinct from other options close in cost and in time. Current model inputs are only estimates and not based on real data or case studies. Factoring in uncertainty, there are likely multiple architectures that have a similar impact magnitude as our preferred architecture. We intend to continue to refine our tradespace model and fully expect our preferred architecture to change as we iterate.

In addition to the tradespace, we assessed complexity using methods described in the Appendix. The preferred architecture has a complexity score that is 49% lower than the baseline score. The benefits of consolidating separate software programs and databases will improve efficiency in the development process in the long term.

Conclusion

Although our proposed architecture does not meet the long-term goal of FPU in 5, we demonstrated that incremental process optimizations can have a substantial impact on the length of the development cycle. Of the design options evaluated, an 18% time savings may be realized by switching to a centralized digital engineering platform, decreasing the FPU time from approximately 14.1 years to 11.7 years. Adding architectural decisions may increase this estimate. We acknowledge that, as of now, our uncertainty is high; we will continue to iterate on our tradespace model and factor in more realistic cost and time estimates by looking at previous case studies in the same or similar industries. In addition to our tradespace analysis in cost and time space, we performed a complexity analysis on the formal architecture of our proposed solution compared to the current implementation and found that the preferred architecture has a lower complexity primarily due to the consolidation of different software programs and databases. Prior to deciding on a final architecture, we propose continuing to iterate on our defined approach by allocating one full-time system engineer for three months.

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Appendix

The flowchart below is a detailed description of the product definition process covered at a high level under Define Design (6.2) in Figure 6. The process covers only the design agency side of the product release process.

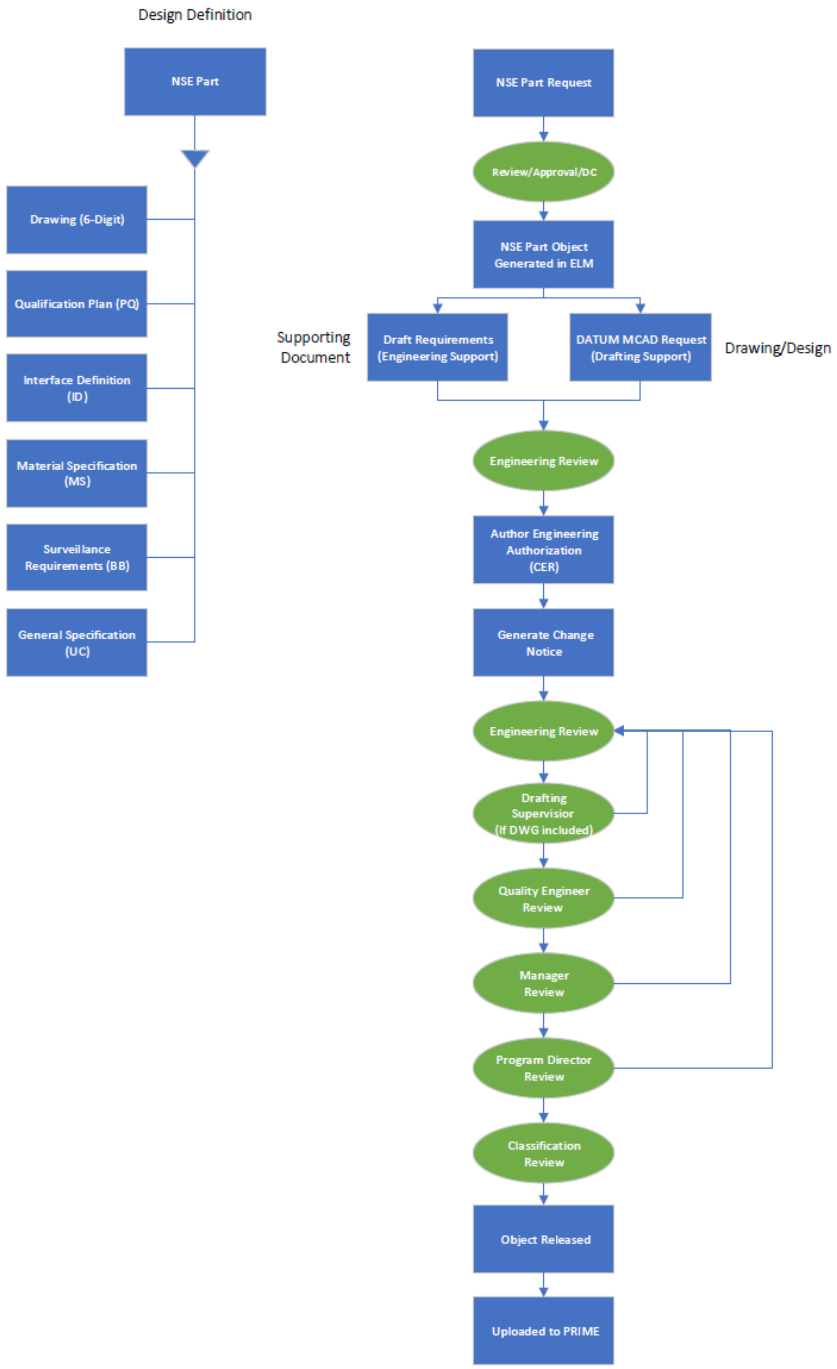

Figure 6

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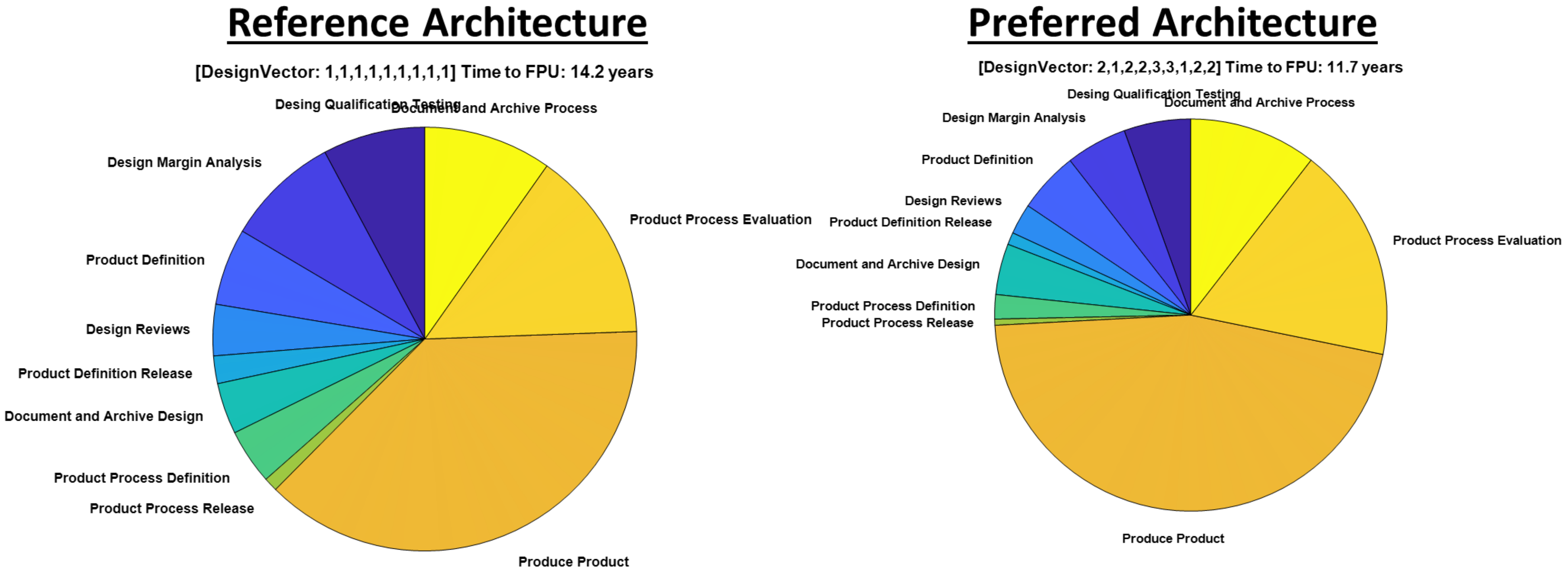

Figure 7 – Distribution of time spent progressing to FPU

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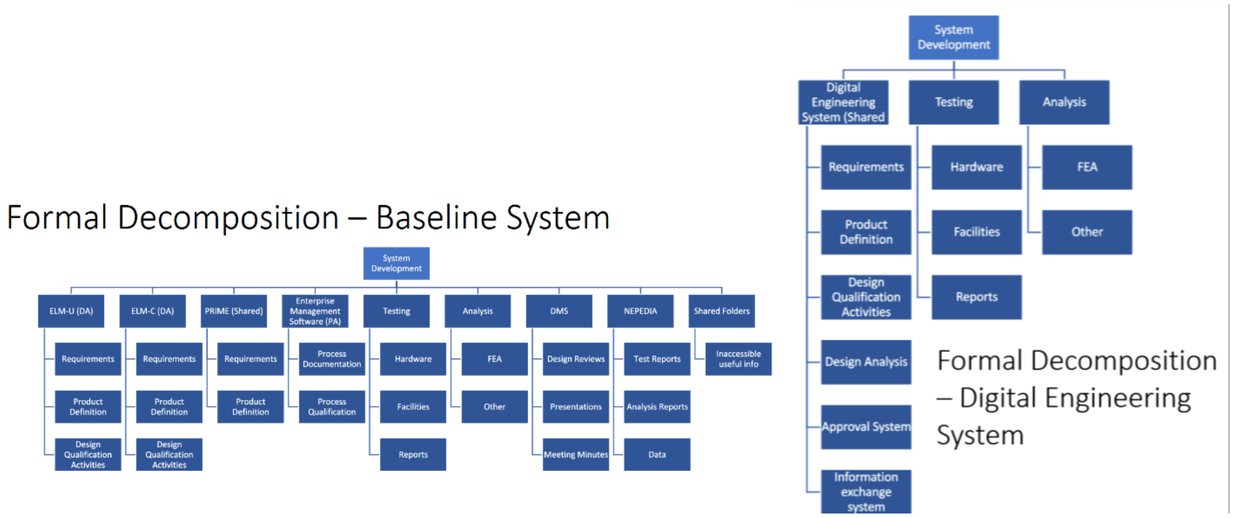

Figure 8 Systems Formal Decomposition

The complexity analysis was performed by taking the formal decomposition of the baseline (current) system and the proposed digital engineering system and converting them into Design Structure Matrixes (DSM). The DSM were then processed using two major equations and an evaluation of the resulting node diagram to determine the overall Structural Complexity. Structural Complexity **C** is derived by calculating the Component Complexity C_1 and adding it to the product of the Pair-wise component interactions C_2 and the Topological complexity C_3. Thus $C = C_1 + C_2 * C_3$

C_1 is composed of $\alpha+\beta*(\gamma/\alpha)$ where alpha (α) is the number of items within the decomposition, beta (β) is number of interactions between the items, and gamma (γ) is the sum of the singular value decomposition (SVD) values from the DSM. This determines the component complexity of the system.

C_2 is determined by taking the greater of the number of component-component interactions or the average magnitude of said interactions. In this case since the DSM is already a unit matrix the average magnitude is less than 1 so the number of component-component interactions is used.

C_3 is able to be calculated using system graph energy; however, as per the presentation Rebentisch, Eric, (March 1, 2023) Foundations of System Design and Management III, System Design and Management, MIT. It is possible to substitute the calculation with an rough order constant based upon the systems visual complexity utilizing a Node Diagram as in Figure 9. As a Hierarchical structure the systems are assigned a value of 1.5 for topological complexity.

These numbers are summarized in Table 3. The baseline design generates a complexity score of 232, which the proposed design generates a score of 114. This allows us to conclude that the proposed design is approximately half as complex as the original design. As complexity is inversely proportional to speed and accuracy within an organization by reformatting the information exchange between the DA and PA there is potential for significant savings.

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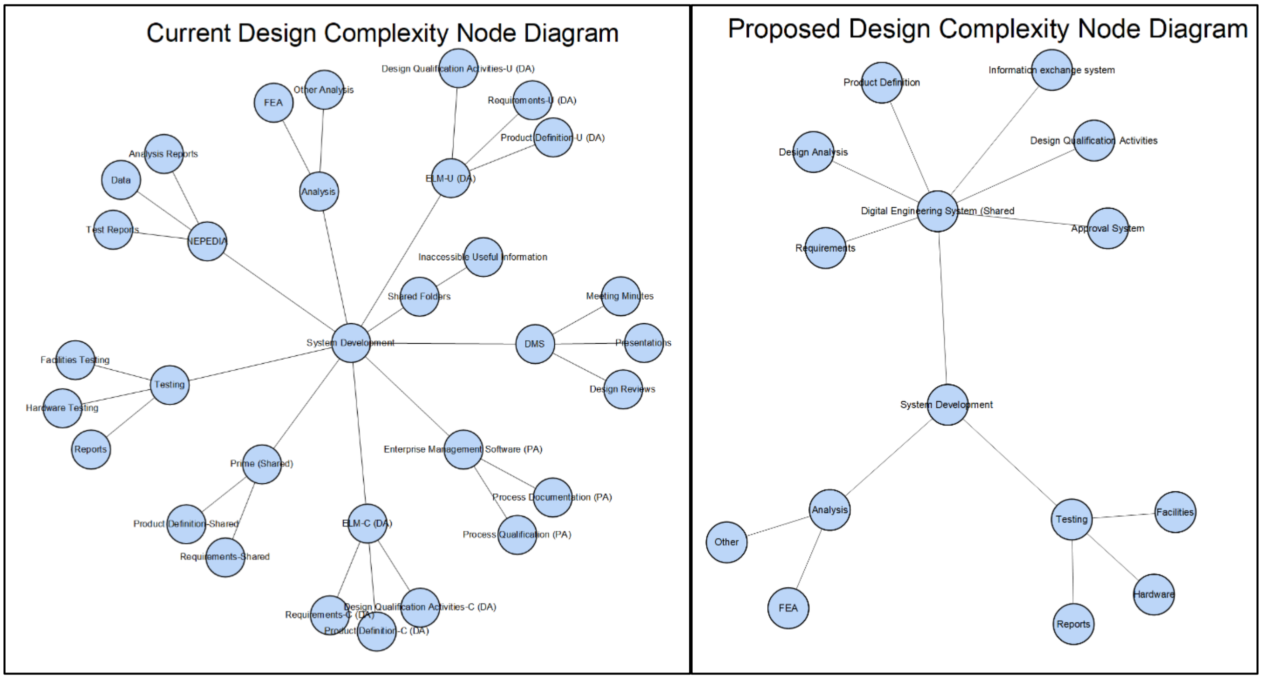
Current Design Complexity Node Diagram
Design Qualification Activities-U (DA)
Other Analysis
FEA
Requirements-U (DA)
Product Definition-U (DA)
Analysis Reports
Data
Analysis
ELM-U (DA)
Test Reports
NEPEDIA
Inaccessible Useful Information
Shared Folders
Meeting Minutes
System Development
DMS
Presentations
Facilities Testing
Testing
Design Reviews
Hardware Testing
Reports
Enterprise Management Software (PA)
Prime (Shared)
Process Documentation (PA)
Product Definition-Shared
ELM-C (DA)
Process Qualification (PA)
Requirements-Shared
Design Qualification Activities-C (DA)
Requirements-C (DA)
Product Definition-C (DA)
Proposed Design Complexity Node Diagram
Product Definition
Information exchange system
Design Qualification Activities
Design Analysis
Digital Engineering System (Shared
Approval System
Requirements
System Development
Analysis
Testing
Facilities
Other
Hardware
FEA
Reports

Figure 9 Current vs Proposed Design complexity node diagram

Table 3 Design complexity comparison

	Component Complexity C1	Interaction Complexity C2	Topological Complexity C3		Structural Complexity Total C
Baseline	91.11	94	1.5		232
Design 1	49.88	43	1.5		114

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