



LAWRENCE
LIVERMORE
NATIONAL
LABORATORY

LLNL-JRNL-825588

Metall: A Memory Allocator For Persistent Data Centric Data Analytics

K. Iwabuchi, K. Youssef, K. Velusamy, R. A. Pearce, M. B. Gokhale

August 9, 2021

Parallel Computing

Disclaimer

This document was prepared as an account of work sponsored by an agency of the United States government. Neither the United States government nor Lawrence Livermore National Security, LLC, nor any of their employees makes any warranty, expressed or implied, or assumes any legal liability or responsibility for the accuracy, completeness, or usefulness of any information, apparatus, product, or process disclosed, or represents that its use would not infringe privately owned rights. Reference herein to any specific commercial product, process, or service by trade name, trademark, manufacturer, or otherwise does not necessarily constitute or imply its endorsement, recommendation, or favoring by the United States government or Lawrence Livermore National Security, LLC. The views and opinions of authors expressed herein do not necessarily state or reflect those of the United States government or Lawrence Livermore National Security, LLC, and shall not be used for advertising or product endorsement purposes.

Metall: A Persistent Memory Allocator For Data-Centric Analytics

Keita Iwabuchi^{a,*}, Karim Youssef^{a,b}, Kaushik Velusamy^c, Maya Gokhale^a, Roger Pearce^a

^aCenter for Applied Scientific Computing, Lawrence Livermore National Laboratory, 7000 East Avenue, Livermore, CA 94550, United States

^bDepartment of Computer Science, Virginia Polytechnic Institute and State University, Blacksburg, VA 24061, United States

^cDepartment of Computer Science, University of Maryland, Baltimore County, 1000 Hilltop Cir, Baltimore, MD 21250, United States

Abstract

Data analytics applications transform raw input data into analytics-specific data structures before performing analytics. Unfortunately, such data ingestion steps are often more expensive than analytics. In addition, various types of NVRAM devices are already used in many HPC systems today. Such devices will be useful for storing and reusing data structures beyond a single process life cycle.

We developed Metall, a persistent memory allocator built on top of the memory-mapped file mechanism. Metall enables applications to transparently allocate custom C++ data structures into various types of persistent memories. Metall incorporates a concise and high-performance memory management algorithm inspired by Supermalloc and the rich C++ interface developed by Boost.Interprocess library.

On a dynamic graph construction workload, Metall achieved up to 11.7x and 48.3x performance improvements over Boost.Interprocess and memkind (PMEM kind), respectively. We also demonstrate Metall's high adaptability by integrating Metall into a graph processing framework, GraphBLAS Template Library. This study's outcomes indicate that Metall will be a strong tool for accelerating future large-scale data analytics by allowing applications to leverage persistent memory efficiently.

Keywords: Persistent Memory, Memory Allocator, Graph Processing

1. Introduction

Data science has become a rapidly evolving field. It plays an increasingly important role in science and security domains. High volume data analytics is one of the key domains in 5 exascale computing [1] [2]. Such data analytics applications usually perform data ingestion tasks, which index and partition data with analytics-specific data structures before performing the targeted analytics. However, the ingestion step is often more expensive than the analytics itself due to unstructured 10 write-intensive operations on large volumes of data. In addition, the same or derived data is re-ingested frequently in 15 real situations – for example, running multiple analytics to the same data with different parameters or developing/debugging a data analytics program. An often overlooked but common 20 theme among the variety of data analytics platforms is the need to persist data beyond a single process lifecycle.

There have been significant performance improvements and 25 cost reductions in both software and hardware technologies of non-volatile random-access memory (NVRAM). These devices offer cost-effective ways of persistently storing large datasets with efficient means of accessing the data for processing.

We anticipate that *persistent data-centric analytics* will be 30 a powerful model for accelerating next-generation large-scale

data analytics. In the model, applications use NVRAM as *persistent memory*, i.e., applications can access data transparently using standard memory operations while the data can live beyond a single process lifecycle.

To enable the persistent data-centric analytics, we 35 developed a persistent memory allocator, Metall¹. Metall is built on top of the memory-mapped file mechanism (mmap(2)) to allow applications to allocate and access data in persistent memory transparently. Metall employs the rich C++ interface developed by Boost.Interprocess [3] so that applications can 40 allocate custom C++ data structures in persistent memories with a small code migration cost. Metall provides persistent memory snapshotting (versioning) capabilities. As for the internal architecture, Metall incorporates a concise and high-performance memory management algorithm that is based on a heap memory allocator, SuperMalloc [4]. We also 45 developed a user-level mmap technique, *batch synchronized mmap* (*bs-mmap*), to improve sparse data update performance on network-attached file systems.

The rest of this paper is structured as follows. Section 2 50 introduces preliminary knowledge of this work. Section 3, Section 4, and Section 5 introduce Metall, Metall internal architecture, and bs-mmap, respectively. Section 6 shows the performance of Metall and bs-mmap on dynamic graph construction workloads. Section 7 demonstrates Metall's high adaptability and impact by integrating Metall into a graph

*Corresponding author

Email addresses: kiwabuchi@llnl.gov (Keita Iwabuchi), karimy@vt.edu (Karim Youssef), kaushik2@umbc.edu (Kaushik Velusamy), gokhale2@llnl.gov (Maya Gokhale), rpearce@llnl.gov (Roger Pearce)

Table 1: Performance comparison of memory devices

Device	Latency (read/write)	Bandwidth (read/write)	Source
DDR4 DRAM	100/100 ns	100/37 GB/s	[8]
NVDIMM (Intel Optane)	370/400 ns	38/3 GB/s	[8]
PCIe NVMe SSD	10 us	2.5/2.2 GB/s	[9, 10]

50 processing framework, GBTL [5]. Section 8 contains related works. Finally, Section 9 offers our conclusions.

In summary, our main contributions are as follows:

- We demonstrate the benefit of the persistent data-centric analytics model and developed a persistent memory allocator, Metall;⁹⁵
- Metall is designed to allow applications to transparently allocate memory into various persistent memory devices¹⁰⁵ with a reasonable code migration cost;
- Metall exhibits up to 11.7x and 48.3x performance improvements over two state-of-the-art memory allocators: Boost.Interprocess [3] and memkind (PMEM kind)¹¹⁵ [6], respectively, on a dynamic graph construction workload with node-local conventional NVMe SSD and emerging byte-addressable persistent memory (Section 6);
- We present techniques to improve sparse data¹²⁰ update performance on network-attached file systems (Section 5);
- We show Metall’s high adaptability and impact on a real graph processing workload using a graph processing¹²⁵ framework, GBTL [5] (Section 7).

2. Preliminary

2.1. Persistent Memory

75 There have been substantial performance improvements and cost reductions in non-volatile memory (NVRAM) technology.¹³⁰ For example, emerging non-volatile dual in-line memory module (NVDIMM), which is installed in the same DIMM¹³⁵ slot as DRAM and can provide byte-addressable accesses, is expected to play a role between DRAM and conventional NVRAM (Table 1). Furthermore, high-performance computing (HPC) systems have various types of NVRAM devices in production systems today, such as locally-attached devices and¹⁴⁰ network-attached distributed file systems [7].

80 To store data into NVRAM, utilizing file systems is highly beneficial since we can support various NVRAM devices transparently and leverage existing powerful technologies to manage and move large-scale data with high reliability.¹⁴⁵ Therefore, we design Metall to work on top of a file system.

85 For the purposes of this paper, we use the term *persistent memory* to represent a storage device/system that works with a filesystem — including NVDIMM, NVMe SSD, and distributed file systems.

2.2. Memory-mapped File

95 Data serialization is a common technique to store data into files; however, dismantling and assembling large complex data structures is expensive in terms of performance and programming cost [11].

To avoid the cost, we leverage the memory-mapped file mechanism. *mmap(2)* is a system call that can map a file into a process’s virtual memory (VM) space and provide applications with transparent access to the region — applications can access the mapping area as if it were regular memory.

100 We show an example code block of mapping a file using *mmap()* in Code 1. A file is created and extended to 4096 bytes at lines 1–3. In line 5, the file is mapped into the process’s VM space with the read/write mode. If a non-NULL address is passed to the first argument of *mmap()*, the kernel uses it as a hint about where to map the file. After line 5, one can use the memory space as if it were allocated by normal memory allocation functions such as *malloc()*. In line 9, *msync(2)* flushes dirty pages back to the filesystem and waits for the I/O to complete. The mapping is closed at line 10 by *munmap(2)*.

105 Actual I/O is conducted with the *demand paging* mechanism — operating systems perform I/O on-demand by page granularity and keep page cache in DRAM. I/O could happen at any point in lines 5–10 in Code 1. Thanks to the demand paging, an application can also map a file bigger than the DRAM capacity.

110 *mmap()* plays an essential role in memory management and is highly useful. However, calling it directly for each memory allocation will cause significant overhead and is not practical because A) *mmap()* works with at least a page granularity (e.g., 4 KB) and B) each allocation requires a new backing-file. To provide fine-grained memories for applications, one can mitigate the overheads by building another memory allocation management layer on top of a memory-mapping region.

Code 1: Example of using *mmap(2)*

```

1 int fd = open("/mnt/ssd/file", O_RDWR | O_CREAT);
2 int size = 4096;
3 ftruncate(fd, size);
4
5 char* array = (char*)mmap(NULL, size, PROT_READ |
6 PROT_WRITE, MAP_SHARED, fd, 0);
6 close(fd);
7 array[0] = 'a';
8
9 msync(array, size, MS_SYNC);
10 munmap(array, size);

```

3. Metall

115 To take advantage of the memory-mapped file mechanism while minimizing the overheads of *mmap()* system call, we propose a persistent memory allocator, called *Metall*, built on top of a memory-mapped region.

120 In this section, we describe the key features of Metall. We first briefly introduce Metall, followed by its API, persistence policy, snapshot capability, design choice for pointers in persistent memory, and backend data store.

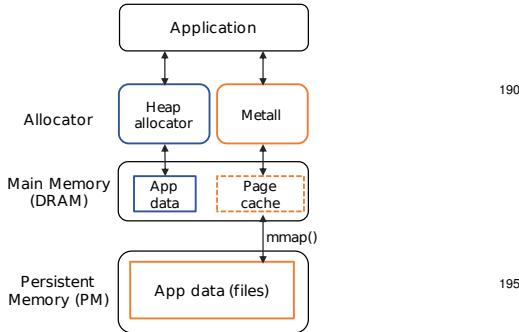


Figure 1: Data Analytics Leveraging Persistent Memory (PM)

3.1. Overview of Metall

Metall works on various memory devices with file system support and enables applications to allocate heap-based objects into persistent memory. As described in Figure 1, Metall looks like a regular (heap) memory allocator from applications; however, it allocates memory into persistent memory using the memory-mapped file mechanism (`mmap()` system call). Metall stores its internal memory allocation management data into persistent memory to resume memory allocation work in the subsequent execution. Besides basic memory allocation features, Metall employs *snapshot* capabilities. Metall supports multi-threads; however, it is not designed to be shared by multiple processes, i.e., there is no interprocess synchronization support.

3.2. Memory Allocation using Metall

Here we describe Metall’s principal APIs, followed by two examples that allocate objects using Metall.

3.2.1. Principal API

Metall has C++ interfaces designed by *Boost.Interprocess* (BIP) [3]. Although BIP has been developed as an interprocess communication library, it has a collection of APIs useful for persistent memory allocators. The APIs allow applications to allocate not only contiguous memory regions like `malloc(3)` but also complex custom data structures, including the C++ STL containers, in persistent memory.

We implemented those APIs in *manager* class under *metall* namespace (principal APIs are listed in Table 2). `allocate()` and `deallocate()` work like `malloc(3)` and `free(3)`. `construct <T>(char* name)` allocates `sizeof(T)` bytes and stores the address into an internal key-value store with the key “name”. This `construct()` function returns a proxy object whose “`()` operator” takes arguments and constructs an object of `T` on the allocated memory (i.e., uses the placement `new`) with the passed arguments. Thus, `construct(char* name)(Args... args)` performs the multiple steps above in one line. `find()` and `destroy()` are used to retrieve and destroy previously allocated objects by `construct()`. `get_allocator()` returns an object of the Standard Template Library (STL) style allocator to work with STL containers.

The manager class contains approximately fifty functions to accomplish high usability, including those with slightly different signatures².

3.2.2. Example of Memory Allocation using Metall

An example of storing and reattaching an `int` object using APIs listed in Table 2 is shown in Code 2.

In line 2, a Metall manager object is constructed; a backend datastore (directories and files) is created under “/ssd/mydata” directory, and an initial backing file is mapped to the process’s virtual memory space. In line 3, an `int` object is allocated and initialized with 10 (10 is passed to the constructor of the `int` object). Additionally, the object’s address and key (“data”) are inserted inside the key-value store in the manager object. When the manager object is destructed (line 6), it synchronizes the allocated data with the backing files and stores its internal management data to the backing store.

In line 9, Metall opens the backing datastore created in line 2. Metall also has a read-only open mode (`metall::open_read_only`), which protects against unintended writes — trying to write data will cause a segmentation fault. In line 10, Metall searches the address of the `int` object allocated in line 3 from its key-value store using the key “data”.

Finally, in line 11, the object is deconstructed and deallocated; the corresponding entry is also removed from the internal key-value store.

Code 2: Example of allocating an `int` object using Metall

```

1  {
2     metall::manager mgr(metall::create_only,
3                          "/ssd/mydata");
4     int* n = metall::mgr.construct<int>("data")(10);
5     std::cout << *n; // show '10'
6     *n = 20;
7     // -- Exit the program and reattach the data -- //
8     {
9         metall::manager mgr(metall::open_only, "/ssd/mydata");
10        int* n = mgr.find<int>("data").first;
11        std::cout << *n; // show '20'
12        mgr.destroy<int>("data");
13    }

```

3.2.3. Metall with STL Container

An STL container holds an allocator object to allocate memory storage for its elements. Metall provides an STL-compatible allocator. Applications can store an STL container into persistent memory by conducting the following two steps: 1) Allocate a container using Metall; 2) Pass a Metall STL allocator object to the constructor of the container object.

We show an example of storing and reattaching an STL container in Code 3. Overall, this example is almost the same as the previous one (Code 2). In line 1, the STL-compatible allocator in Metall is passed to the vector container as the second template argument. In line 5, an object of the vector container is allocated and constructed, receiving a Metall STL allocator object as a constructor argument.

²Metall API documentation: <https://software.llnl.gov/metall/api/>

Table 2: Metall’s Principal APIs (all of them are provided by metall::manager class)

Signature	Description
void* allocate(size_t n)	Allocates n bytes.
void deallocate(void *addr)	Deallocates the allocated memory.
T* construct<T, Args>(char* name)(Args... args)	Allocates and constructs an object of T with arguments $args$. Also internally stores the allocated memory address with key $name$.
T* find(T)(char* name)	Finds the already constructed object associated with key $name$.
bool destroy(char *name)	Destructs and deallocates the object associated with key $name$.
metall_stl_allocator<T> get_allocator(T)()	Returns an STL allocator object for type T .

After lines 5 and 11, as written in there, the vector object can be used transparently — even its capacity can be changed since it holds a Metall STL allocator object internally. 245

Code 3: Example of using a STL container with Metall

```

1  using vector_t = vector<int , metall::allocator<int>>;
2
3  metall::manager mgr(metall::create_only ,
4      "/ssd/mydata");
5  auto* pvec = 295
6  mgr.construct<vector_t>("vec")(mgr.get_allocator<int>());
7  pvec->push_back(5);
8  // -- Exit the program and reattach the data -- //
9
10 metall::manager mgr(metall::open_only , "/ssd/mydata");
11 auto* pvec = mgr.find<vector_t>("vec").first; 300
12 pvec->push_back(1);
13 }
```

3.3. Persistence Policy

Metall employs snapshot consistency, an explicit coarse-grained persistence policy in which persistence is guaranteed only when the heap is saved in a “snapshot” to the backing store. The snapshot is created when the destructor or a snapshot method in Metall is invoked. Those methods flush the application data and the internal management data in Metall to the backing store (backing files). If an application 305 crashes before Metall’s destructor finishes successfully, there is a possibility of inconsistency between the memory mapping and the backing files. To protect application data from this hazard, the application must duplicate the backing files before reattaching the data by using either the snapshot method or a 310 copy command in the system. 275

In contrast, libpmemobj in the Persistent Memory Development Kit (PMDK) [12] builds on Direct Access (DAX) and is designed to provide fine-grained persistence. Fine-grained persistence is highly useful (or almost necessary) 320 to implement transactional object stores, leveraging new byte-addressable persistent memory fully, e.g., Intel Optane DC Persistent Memory. However, fine-grained persistence requires fine-grained cache-line flushes to the persistent media, which can incur an unnecessary overhead for applications that do 325 not require such fine-grained consistency [13]. It is also not possible to efficiently support such fine-grained consistency on more traditional NVMe devices. 285

3.4. Snapshot

In addition to the allocation APIs, Metall provides a snapshot feature that stores only the difference from the previous snapshot point instead of duplicating the entire persistent heap by leveraging reflink [14].

With reflink, a copied file shares the same data blocks with the existing file; data blocks are copied only when they are modified (copy-on-write). Because reflink is relatively new, not all filesystems support it. The filesystems that implement reflink include *XFS*, *ZFS*, *Btrfs*, and *Apple File System (APFS)* — we expect that more filesystems will support this feature in the future. In case reflink is not supported by the underlying filesystem, Metall automatically falls back to a standard copy operation.

3.5. Pointers in Persistent Memory

Applications have to take care of some restrictions regarding pointers to store objects in persistent memory. Applications cannot use raw pointers for data members in data structures stored in persistent memory because there is no guarantee that backing files are mapped to the same virtual memory addresses every time. Therefore, the *offset pointer* has to be used instead of the raw pointer. An offset pointer holds a relative offset between the address pointing at and itself so that it can always point to the same location regardless of the VM address to which it is mapped.

Metall inherits the *offset_pointer* implemented in Boost.Interprocess library. The pointer type in the STL allocator in Metall uses the *offset_pointer*. STL containers are designed to use the pointer types declared in the allocators. Unfortunately, some implementations of containers do not work with Metall because raw pointer types are hardcoded. Containers in Boost.Container is compatible with Metall.

Additionally, references, virtual functions, and virtual base classes have to be removed since those mechanisms also use raw pointers internally.

Other persistent memory allocators also ask applications to replace raw pointers with similar designs of offset pointers (for example, libpmemobj library in PMDK [12] and Ralloc [15]). Furthermore, the concept of the non-raw pointer is being integrated into C++ (e.g., smart pointers). We believe that offset pointer is one of the most realistic solutions for random memory placement. To help application developers, developing a program that assesses the compatibility of an existing data structure with Metall would be interesting future work. 330

3.6. Metall Data Store

In addition to application heap data, Metall management data also have to be stored in persistent memory to resume memory allocation work. When a Metall manager object is constructed with the create mode, it creates a root directory at the specified path; then, the manager creates files and directories on-demand under the root directory to store its management data and application data allocated through itself. 335 Hereafter, we call the directory as *Metall datastore* for convenience. 340

As all files related to a single manager are located in the same directory, one can easily duplicate or delete a Metall 385 datastore, even using normal file copy or remove commands.

In addition, Metall is not designed for multi-process data 345 sharing; however, multiple processes can still open the same datastore with the read-only mode.

Metall uses multiple files to store application data. We 350 found that breaking application data into multiple backing files increases parallel I/O performance in many situations. When we performed a preliminary evaluation by running multi-threaded out-of-core sort, we achieved 4.8X performance improvement by dividing the original array into 512 files (we 390 used 96 threads and PCIe NVMe SSD). To efficiently use persistent memory resources, Metall creates and maps new files 355 on demand. By default, Metall creates each file with 256 MB. This value can be changed by defining the corresponding macro at compile time. 400

4. Metall Internal Architecture

In this section, we explain the internal architecture of 360 Metall. To efficiently manage memory allocations without a complex architecture, Metall exploits Supermalloc's main 405 design philosophy [4] – virtual memory (VM) space on a 64-bit machine is relatively cheap, while physical memory remains dear. More specifically, we take advantage of *demand paging* mechanism, that is, physical memory space both in DRAM and persistent memory is not consumed until the corresponding 410 pages are accessed.

4.1. Application Data Segment and Chunk

Metall reserves a large contiguous virtual memory (VM) 370 space to map backing file(s) when its manager class is 415 constructed. Applications can set the VM reservation size when creating a new Metall datastore (Metall manager's constructor takes the value, the default size is a few TB). Metall automatically detects the necessary VM size when 375 opening an existing datastore. Metall divides the reserved VM space into *chunks* (2 MB by default, configurable via Metall 420 manager's template parameter). A chunk can hold multiple *small objects* of the same allocation size (from 8B to the half chunk size). Objects larger than the half chunk size (*large 425 objects*) use a single chunk or multiple contiguous chunks. Metall frees DRAM and file space by chunk, that is, small object deallocations do not free physical memory immediately, whereas large object deallocations do.

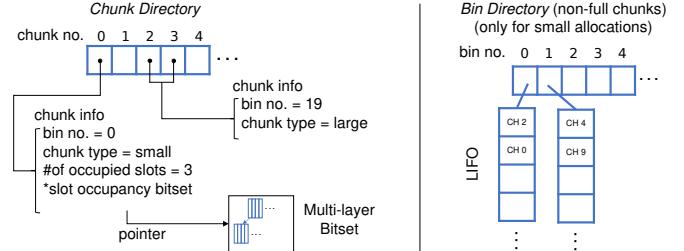


Figure 2: Metall Memory Allocation Management Data

4.2. Internal Allocation Size

Same as other major heap memory allocators, Metall rounds up a small object to the nearest internal allocation size. Metall uses allocation sizes proposed by Supermalloc [4] and jemalloc [16]. Thanks to this approach, Metall can keep internal fragmentations equal to or less than 25 % and convert a small object size to the corresponding internal allocation size quickly. Metall also assigns a *bin number* for each internal allocation size. The allocation size techniques enable Metall to compute a bin number from an internal allocation size efficiently.

On the other hand, a large object (larger than 1 MB by default) is rounded up to the nearest power of 2 — although this strategy wastes VM space, it does not waste physical memory thanks to the demand paging mechanism. In the worst case, 1.6% of physical memory is wasted when (1 M + 1) bytes of allocation is requested on a 4 KB page size system. On a 64 KB page size system, 6.3% is wasted for a (1 M + 1) bytes of allocation.

4.3. Management Data

Metall uses three types of management data directories to 365 manage memory allocation. Because updating the management data causes fine-grained random memory accesses, Metall constructs them in DRAM to increase data locality — consequently, Metall rarely touches persistent memory when allocating memory. Metall deserializes/serializes the 405 management data from/to files when its constructor/destructor is called. The cost of the process is often negligible since the management data is much smaller than the application data.

The three data directories are allocated for each Metall 375 manager object so that multiple Metall manager objects can coexist with one another in the same program. Here, we 415 describe the details of the three management data directories.

4.3.1. Chunk Directory

The chunk directory is an array of blocks (the left figure in Figure 2). The *i*-th block holds the status of the *i*-th chunk of the application data segment, such as bin number (internal allocation size id), chunk type (represents small or large allocation), and a pointer to a bit set for small allocation. The size of a single block is 14 bytes.

Metall utilizes a compact multi-layer bitset table and built-in bit operation functions to manage available slots in a chunk used for a small size. It can manage up to $64^3 (= 2^{18})$ slots

430 using a three-layer structure, which is equal to the maximum number of slots if the minimum allocation size is 8B and the chunk size is 2 MB ($2^{21}/2^3 = 2^{18}$). Therefore, Metall calls a₄₈₀ built-in bit operation function at most three times to find an available slot in a chunk.

435 Metall sequentially probes the array when it needs to find empty chunk(s). Although we have not seen a performance bottleneck during the step, it will be straightforward to implement an additional index structure to increase the performance.⁴⁸⁵

4.3.2. Bin Directory

440 The bin directory is an array of *bins* (the right figure in Figure 2). A bin holds IDs of non-full chunks of the same internal allocation size. A bin operates in a LIFO (last in, first out) manner. Metall first checks this directory to find available chunks for small allocations. If a bin is empty, Metall accesses the chunk directory to find an empty chunk. Metall uses this directory only for small allocations since large allocations do not share chunks.⁴⁹⁰

4.3.3. Name Directory

450 The name directory is a simple key-value table. When an object is constructed by `construct()` function in Metall manager (Table2), some attributes (e.g., key string and address) of the object are stored here.⁵⁰⁰

4.4. Management Data and STL Allocator

455 An STL container object holds an allocator object internally so that it can change its capacity dynamically. To perform memory allocation work, an object of the STL allocator in Metall needs to know the address of the corresponding management data allocated in DRAM. However, Metall cannot embed the address of the corresponding management data into an STL allocator object since the management data will not be allocated to the same VM addresses always. In order to address this issue, a Metall STL allocator object holds the offset to the head of the application data segment using the offset pointer.⁵⁰⁵ When a Metall manager object is constructed, it writes the address of its management data at the head of the corresponding application data segment. Consequently, Metall STL containers can access the management data always.⁵¹⁵

4.5. Multi-thread Support

470 Metall works with multiple threads. Here, we describe how the multi-thread support is implemented in Metall.⁵²⁰

4.5.1. Mutex in Global Management Data

475 Metall allocates a single mutex object for the chunk directory and the name directory each.

Metall also arranges a mutex object per bin in the bin directory. As Metall does not mix different allocation sizes within a chunk, it can handle different small size allocation/deallocation requests concurrently except the following two situations:⁵²⁵

- There is no entry (non-full chunk ID of the allocation size) in the bin directory during an allocation operation; thus, Metall needs to find an empty chunk in the chunk directory.

- The last slot of a chunk has been freed. Metall needs to update the metadata of the chunk directory.

4.5.2. Local Object Cache

To increase multi-thread performance, memory allocators often employ local object caches. An object cache holds recently deallocated objects. Object caches are allocated at, for example, thread level, CPU core level, and/or CPU socket level.

Since Metall is designed to deal with larger data than existing memory allocators, we decided to employ free-object caches at the CPU core level only to simplify its implementation.

5. Batch Synchronized mmap (bs-mmap)

When an application maps a file using `mmap(2)`, *shared mapping* (MAP_SHARED option) is usually used. The file-backed shared mapping writes back updates (dirty pages) with page granularity into the underlying file system on demand. This feature is necessary for mapping data larger than the DRAM capacity of the system (out-of-core processing). On the other hand, network file systems such as Lustre [17] are not designed to handle small and random I/Os with low concurrency [18]. Therefore, if applications do not need out-of-core processing with network file systems, such on-demand I/O patterns will cause unnecessary performance degradation.

A naive solution for the problem would be *data staging*. Specifically, 1) an application copies all files into a local memory device; 2) maps the files and performs analytics; 3) copies back to the original storage after the analytics. However, this data staging approach could be wasteful if applications want to update data sparsely.

Another technique to mitigate the performance degradation is tuning up the behavior of the page cache by writing values to some files in `/proc/sys/vm`. However, it causes 1) system-wide changes; 2) requires privilege access, which is unavailable in many large-scale clusters.

Considering these options, we designed *bs-mmap*, batch synchronized mmap. *bs-mmap* is a user-space file-backed memory mapping mechanism that efficiently writes back dirty pages to the backing file only when it is invoked by the application explicitly.

5.1. *bs-mmap* Implementation

bs-mmap calls `mmap()` with the MAP_PRIVATE option. MAP_PRIVATE creates a private copy-on-write mapping where updates are not written back to the backing file by the operating system.

`msync(2)` is used with the shared mapping to flush dirty pages into the backing file explicitly. However, `msync()` does not work with private mapping. Therefore, we implemented

530 a user-level `msync()` that works with the private mapping. To detect dirty pages, we used the information provided by the /proc file system on Linux systems. The /proc file system provides an interface called *pagemap*, which contains page table information about every page in a process’s virtual memory space. This information is stored in a file named /proc/self/pagemap, which contains a 64-bit value for each page that belongs to the process [19]. In the case of a private mapping, a page is no longer file-backed once it becomes dirty; however, its status is either *present* or *swapped*. Hence, a dirty page of a MAP_PRIVATE region can be identified by checking if bit number 61 of its pagemap entry is zero and the logical OR of bits 62 and 63 equals one. By querying these values, our `msync` (write-back) method can identify dirty pages without making any change in system calls or kernels.

545 5.2. Bandwidth and Parallelism Utilization

550 We implemented two optimizations to efficiently utilize the bandwidth and parallelism on parallel file systems. First, `bs-mmap` writes back dirty pages in consecutive chunks when possible rather than page-by-page. Second, `bs-mmap` writes back dirty pages in parallel. As described in Section 3.6, Metall 590 uses multiple backing files for the application data segment. When `bs-mmap` flushes dirty pages using its `msync()` function, it assigns a thread per file to perform parallel I/O.

6. Evaluation

555 To evaluate the memory allocation performance, we perform a multi-threaded dynamic graph construction benchmark. We also demonstrate the impact of the batch synchronized mmap technique (`bs-mmap`) described in Section 5. The benchmark inserts edges into a graph data structure allocated in persistent memory.

6.1. Graph Data Structure

560 To construct graph data with multiple threads on a shared-memory system, we used a multi-bank *adjacency list* (Figure 3). The adjacency list is one of the de facto standard graph data structures and consists of a vertex table and an edge list per vertex in the graph. Each element in the vertex table 605 contains the ID of a vertex and an edge list. An edge list contains the IDs of all neighbor vertices of a vertex. To quickly locate a specific vertex by its ID, we used the `unordered_map` (hash table) container for the vertex table. As for the edge list, we used the `vector` (dynamic array) container. We used 64 bits to represent a vertex ID.

570 To support multi-thread graph construction, we used m banks, where m is greater than the number of threads ($m = 1024$ in this experiment). A bank is a pair of an adjacency list and a mutex object. We constructed a graph by repeatedly inserting edges. Each edge is a pair of source and neighbor vertex IDs, and we acquired the mutex of the bank associated with the source vertex when ingesting an edge.

580 To make a data structure that works with a custom STL allocator, we followed the C++ standard procedure of

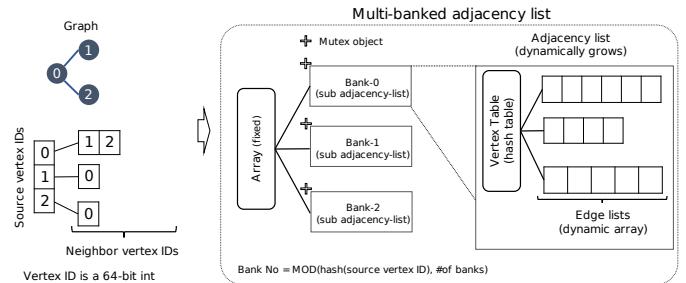


Figure 3: Banked Adjacency List Data Structure

developing an allocator-aware class. Precisely, we customized the multi-bank adjacency list data structure to take an allocator type in its template and an allocator object in its constructor.

6.2. Machine Configuration

We used three single node machines at Lawrence Livermore National Laboratory. We show the specification of the machines in Table 3.

EPYC. EPYC has a PCIe NVMe SSD. We tuned the behavior of the page cache by writing values to some files in /proc/sys/vm to reduce the number of forced write-backs to the SSD device. Specifically, we set `dirty_ratio` to 90, `dirty_background_ratio` to 80, and `dirty_expire_centisecs` to a large number so that dirty pages are not evicted due to long stays in the cache. When we performed a preliminary evaluation, we achieved significant performance improvement (up to 7X) on the graph construction benchmark.

Optane. A single Intel Optane DC Persistent Memory device is installed in its DIMM slots (one side of a NUMA node) and configured with App Direct Mode. In the App Direct Mode, the device shows up in the system as if it were a conventional block device; we set up the device with ext4 filesystem DAX mode to bypass the page cache layer and to enable fine-grained I/O rather than page granularity.

Corona. We also use one of the nodes of the Corona cluster, which consists of over 200 compute nodes. Corona is connected to two parallel file systems: Lustre [17] and VAST [20]. In our environment, Lustre is suitable for large-chunk I/O and possesses higher bandwidth over VAST. On the other hand, VAST shows better performance for fine-grained I/O.

6.3. Dynamic Graph Construction

We ran the dynamic graph construction benchmark on EPYC and Optane machines, which have node-local persistent memory, varying the sizes of input data.

6.3.1. Implementations For Performance Comparison

For performance comparison, we used three allocators as below:

Table 3: Machine Configurations

(a) EPYC: NVMe SSD

CPU	AMD EPYC 7401 48 cores, 96 threads
DRAM	256 GB
Storage	PCIe NVMe SSD 3 TB Ultrastar SN200 HH-HL add-in card XFS filesystem
Kernel	Linux Kernel v5.6

(b) Optane: NVDIMM

CPU	Intel Xeon Platinum 8260L 48 cores, 96 threads
DRAM	192 GB
Storage	Intel Optane DC Persistent Memory 1.5 TB, App Direct Mode, ext4 DAX
Kernel	Linux Kernel v5.9

(c) Corona: NVMe SSD and parallel file system (PFS)

CPU	AMD EPYC 7401 48 cores, 96 threads
DRAM	251 GB
Local Storage	PCIe NVMe SSD 1.6 TB XFS filesystem
PFS	Lustre
PFS	VAST, connecting via Ethernet 4 × 20 Gbps links
Kernel	Linux Kernel v3.10

Boost.Interprocess (BIP). Even though Boost.Interprocess [3] has been developed as an interprocess communication library; its managed mapped file version can work as a persistent memory allocator. This allocator does not free space in files. We used Boost libraries v1.75.0.

PMEM kind. memkind library [6] provides a file-backed memory allocator (called *PMEM kind*) built on top of a state-of-the-art heap allocator, jemalloc [16]. Although PMEM kind allocates memory into a file, it uses persistent memory as volatile memory — i.e., it cannot reattach data or resume memory allocation beyond a single process lifecycle. We used memkind v1.11.0.

On the Optane machine, we made a small change to this allocator because we noticed vital performance degradation due to frequently calling `madvise(2)` system call with `MADV_REMOVE` flag to free space in both DRAM and file system. Thus, we switched to use `MADV_DONTNEED` flag to free only pages in DRAM. Metall also uses the system call with `MADV_REMOVE` for the same purpose; however, we did not make any change to Metall because Metall is designed to call the system call less frequently.

libvmmem is a similar memory allocator library included in Persistent Memory Development Kit (PMDK) [12]; however, we used PMEM kind as PMDK recommends it over libvmmem.

Ralloc. Ralloc [15] is a persistent lock-free allocator designed and optimized for byte-addressable NVRAM (e.g., Intel Optane DC Persistent Memory). Ralloc showed notably better performance over `libpmemobj` in PMDK [15]. As Ralloc is targeted for byte-addressable NVRAM, we used it only on the Optane machine. We used the version that was available at the time of writing. For the purpose of this evaluation, we wrote an STL-compatible allocator class that uses Ralloc internally.

6.3.2. Dataset

We used an R-MAT [21] generator with the settings used in the Graph500 to generate synthetic scale-free graphs of different sizes. We generated SCALE 24–30 graphs, scrambling the vertex IDs in order to remove unexpected localities. The number of vertices and undirected edges in a SCALE s graph are 2^s and $2^s \times 16$, respectively. We treat generated edges as undirected ones; hence, the number of actually inserted edges is $(2^s) \times 16 \times 2$. At each iteration of the benchmark, the benchmark program generates a chunk of edges into DRAM first and inserts the edges into a graph data structure. We exclude the edge generation time from reports.

6.3.3. Results

We show results on the EPYC machine and the Optane machine in Figure 4b and Figure 4a, respectively.

On EPYC machine. Metall showed up to 7.4–10.9x and 2.2–2.8x improvements over Boost.Interprocess (BIP) and PMEM kind, respectively, at SCALE 25–29. At SCALE 30, we observed performance drops in all implementations because the adjacently-list objects exceeded the DRAM capacity. At the SCALE, Metall achieved 11.7x and 48.3x better performance over BIP and PMEM kind, respectively.

On Optane machine. Metall achieved 2.1–2.3x better performance over BIP. Ralloc did not finish at SCALE 30 because it ran out of the persistent memory space. Metall showed similar performance to Ralloc and the modified version of PMEM kind; specifically, PMEM kind and Ralloc were up to 10% and 14% better than Metall, respectively.

Summary. We attribute the low performance of Boost.Interprocess to its internal architecture — it employs a single tree with a single lock for governing memory allocation, which will not scale well with multiple threads. Although Metall employs a simpler internal design than PMEM kind (which is based on jemalloc), it was able to achieve comparable memory allocation performance on Optane machines thanks to the design strategies proposed by Supermalloc (Section 4), such as leveraging the demand page mechanism. Metall showed 1) the best performance on EPYC (conventional NVRAM) and 2) the compatible performance against PMEM kind and Ralloc on Optane (emerging byte-addressable NVRAM). This evaluation demonstrated Metall’s high portability.

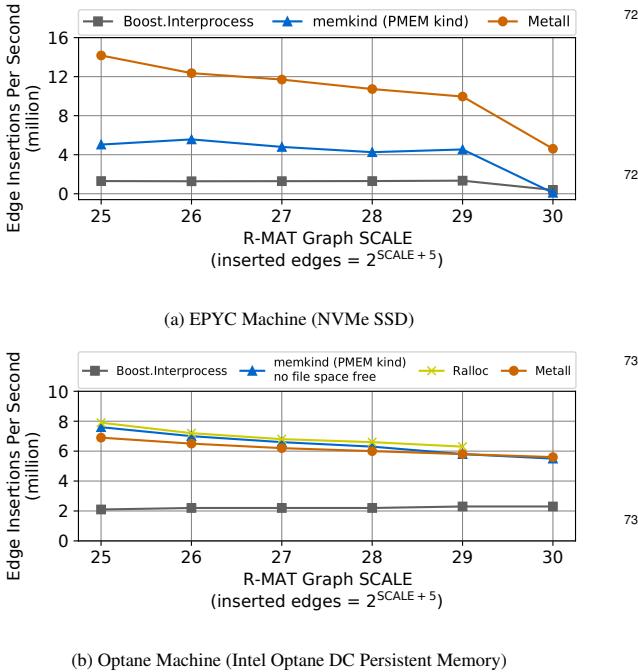


Figure 4: The results of the multi-threaded shared-memory dynamic graph construction benchmark on two persistent memory devices. We did not run Ralloc₇₄₀ on EPYC machine because it is designed for byte-addressable memory.

6.4. *bs-mmap* on Network File Systems

We evaluated the performance of Metall with our *bs-mmap* (batch synchronized mmap) described in Section 5, using an incremental graph construction benchmark.

6.4.1. Benchmark Workload: Incremental Graph Construction

The incremental benchmark uses Metall to construct a persistent graph data incrementally — the actual data structure is the multi-bank adjacency list described in Section 6.1. We ran our experiments on one compute node of the Corona cluster (table 3c). We evaluated the performance of constructing graphs on two different network file systems, Lustre and VAST.

We used real temporal graph datasets extracted from Wikipedia and Reddit (see details in Section 6.4.2). We sorted the edges of the Wikipedia and the Reddit datasets by timestamp to simulate a real-world incremental graph growing. We partitioned each of the sorted datasets by month to iteratively and incrementally constructed a persistent banked adjacency list using Metall. The incremental benchmark's first iteration creates a new Metall datastore, adds the first chunk of edges, flushes data back to the backing store, then closes the Metall data store. Each subsequent iteration opens the existing datastore, appends the next chunk of edges, flushes, then closes the Metall datastore. We measured the total time per iteration, i.e., adding a monthly chunk of edges. We broke down the measured time into ingestion time and flush time.

6.4.2. Datasets

We used two real-world datasets in our experiments: Wikipedia page reference graph and Reddit author-author graph:

Wikipedia page reference graph. We curated the Wikipedia dataset by extracting hyperlinks between all pages in the English Wikipedia dump³ as of July 1st, 2017. The dump data contains the entire edit history of the pages in English Wikipedia from January 15th, 2001, which is the date English Wikipedia was founded. The graph contains hyperlinks not only between article pages but also other types of pages such as author (user) pages and Category pages. Specifically, it contains 1.8 billion hyperlink (edge) insertions.

Reddit author-author graph. Reddit⁴ is one of the largest social news websites in the world. On Reddit, users can comment on other comments. We extracted the user activities to construct an author-author comment graph. For example, if *Alice* posts a comment to *Bob*'s comment, we represent it as an edge from *Alice* to *Bob*. This dataset contains 4.4 billion comment activities (edges).

6.4.3. Implementations

We compared the performance of our *bs-mmap* to that of two models that use the standard file-backed mmap (shared mapping with system *msync*) as follows:

direct-mmap. The first configuration consisted of mapping files directly from Lustre or VAST into Metall's virtual memory space. We considered this method as our baseline for performance comparison.

staging-mmap. The second configuration brings a Metall datastore into *tmpfs* and maps files from there into the virtual memory of the process in order to increase data locality during the graph construction. *tmpfs* is temporary file storage configured on top of DRAM. The staging step copies a datastore from Lustre or VAST into *tmpfs* at the beginning of each iteration, then copies it back at the end of each iteration. We implemented parallel file copy-in and copy-out operations to maximize resources utilization.

bs-mmap. We configured *bs-mmap* to read the mapped file ahead into virtual memory using *mmap*'s *MAP_POPULATE* flag since this showed to be significantly faster than on-demand paging on both Lustre and VAST. Finally, we disabled the feature of freeing file space in Metall since our preliminary experiments showed that it was an expensive operation on Lustre. While it did not cause significant performance degradation on VAST, we disabled it in order to compare the performance across both file systems under similar conditions.

6.4.4. Results

Figure 5 contains the cumulative execution time after each iteration of constructing the Wikipedia and the Reddit graphs on Lustre and VAST. Figure 6 shows the time breakdown into ingestion time and flush time for each configuration. We added

³<https://dumps.wikimedia.org/enwiki/>

⁴<https://www.reddit.com/>

staging-mmap’s copy-in time to the ingestion time and copy-out time to the flush time.

First, the direct-mmap did not complete within a reasonable time except for the Wikipedia graph on VAST. As direct-mmap needed to issue a lot of fine-grained write-backs on the fly for evacuating dirty pages to the file systems over the networks, it showed the notable slow performance, especially on Lustre.

Second, on Lustre, staging-mmap showed the best performance for both graphs. Compared with bs-mmap, it was 1.3X and 1.5X faster for the Wikipedia graph and the Reddit graph, respectively. As the Lustre system has high bandwidth, staging-mmap was able to conduct the staging of the whole data store to/from the local memory (tmpfs) efficiently. We also attribute the slower ingest times of bs-mmap to the cost of accessing the file metadata of the Lustre system. Consequently, staging-mmap was the best on the Lustre.

Third, on the VAST, bs-mmap yielded the best performance out of all three configurations. It showed 1.6X and 2.4X better performance than direct-mmap and staging-mmap for the Wikipedia graph, respectively. bs-mmap was also 1.5X better than staging-mmap for the Reddit graph. staging-mmap suffered from the low bandwidth of the filesystem and took considerably long times for staging out the data stores, which are included in flush time in Figure 6. On the other hand, bs-mmap was able to finish the flush step quickly as it writes back only dirty pages.

7. Application Case Study: GraphBLAS Template Library (GBTL)

In this section, we demonstrate the high adaptability of Metall by integrating it into GraphBLAS Template Library (GBTL) [5]. In this work, we present GraphBLAS as a real application use case to demonstrate Metall persistent memory allocator benefits. We show an example of how storing and reattaching graph containers using Metall, eliminates the need for graph reconstruction at a one-time cost of reattaching to Metall data store.

7.1. Graph Analytics and GraphBLAS

Graph analytics enables us to develop new data processing capabilities. One of the main problems in graph analytics is the need to persist the data beyond the scope of a single execution. Graph construction, indexing, and regular updates are often more expensive than the analytics itself. This has been observed in, for instance, large genome assembly [22] and kNN graphs [23]. With persistent memory, data structures, once constructed, can be re-analyzed and updated beyond the lifetime of a single execution. GraphBLAS specifies a set of building blocks for computing on graphs and graph-structured data, expressed in the language of linear algebra [24]. This approach represents graphs as sparse matrices and operations using an extended algebra of semirings. An almost unlimited variety of operators and types are supported for creating a wide range of graph algorithms. The GraphBLAS Template Library (GBTL) is a C++ reference implementation of the GraphBLAS specification [5].

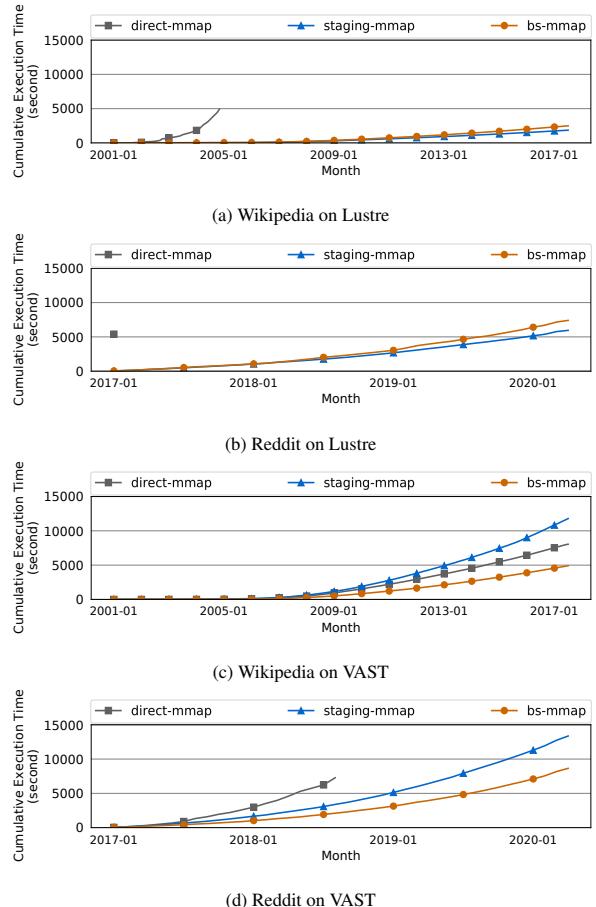


Figure 5: Cumulative execution time after each iteration (month) of constructing the Wikipedia page reference graph and the Reddit author-author graph on Lustre and VAST file systems. direct-mmap completed in only one case (Wikipedia on VAST).

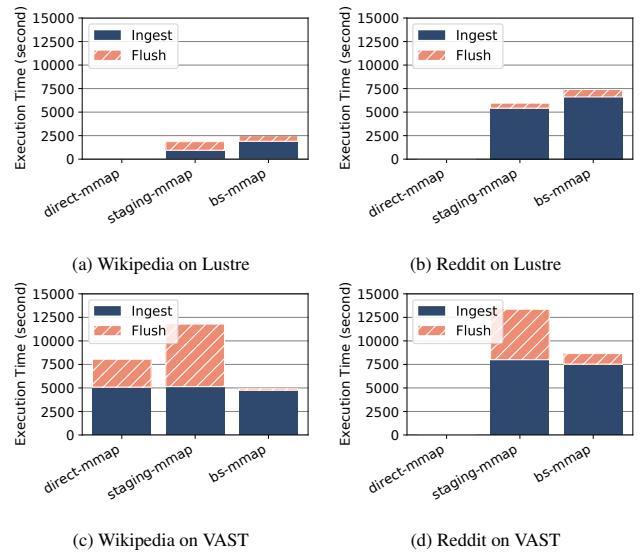


Figure 6: Total time for incrementally constructing the Wikipedia page reference graph and the Reddit author-author graph on Lustre and VAST file systems. The time is broken down into total ingestion time and total flush time. direct-mmap completed in only one case (Wikipedia on VAST).

7.2. Graph Analytics using GBTL

We show a typical workflow of GBTL in Code 4.

- First, read edge list data stored in text files, counting the numbers of vertices and edges in the edge list; 875
- Second, allocate a graph object using the information about the numbers of vertices and edges; 825
- Third, ingest the input edge list into the graph object; 880
- Finally, run graph algorithm(s). 880

As GBTL uses a normal/transient memory allocator, one has to repeat the whole graph construction step every time when running a graph algorithm. 830

7.3. Integrating Metall into GBTL

Here, we describe the work we performed for integrating Metall into GBTL. 885

7.3.1. Graph Data Structure

GBTL employs an adjacency list data structure to store the vertex and edge lists. Its adjacency list uses vector containers internally. We adapted the data structures to take a custom STL allocator instead of using the default one in the vector container. 890
 840 We were able to complete the adaption just following the C++ standard style of implementing an allocator-aware class. In fact, the modified data structures do not contain any code that depends on Metall. 895

7.3.2. Graph Algorithm Implementation

GBTL has a set of high-level graph algorithms built on top of GraphBLAS. We selected five algorithms implementations to investigate the necessary change for integrating Metall into GBTL: breadth-first search, page rank, single-source-shortest-paths, triangle counting, and K-Truss. 905

It turned out that the only additional requirement in 'metallizing' GBTL was modifying the template parameters of the graph algorithm functions so that the functions can take a graph type with a custom allocator — no changes were made inside the graph algorithm functions. 910

In addition, we found that GBTL implementations use temporary graph containers to store intermediate results in computing graph algorithms. Specifically, we found lines like "Graph_t tmp_g;" (Graph_t is a graph type) in multiple graph analytics functions. Such temporary graphs need not be allocated in the persistent store and can be left as a non-persistent data structure in DRAM. 915

To make this convenient, we implemented another STL-compatible allocator, called *fallback allocator adaptor*. The fallback allocator adaptor *fallbacks* to a normal memory allocator (e.g., `malloc()`) if its default constructor is called. Metall's STL-compatible allocator (not the fallback adaptor) has to be constructed with a parameter so that it can communicate with a Metall manager object. Thus, fallback allocator adaptor knows that the application wants to allocate the object into DRAM rather than persistent memory 930 860

if no argument is passed to its constructor. The purpose of this adaptor is to provide a way to quickly integrate Metall into an application that occasionally wants to allocate 'metallized' classes as non-persistent data structures in DRAM. By introducing the adaptor, we were able to support Metall with only the changes in the graph data structures and a few helper functions.

7.3.3. Graph Analytics with Metall and GBTL

Finally, we show how the original graph analytics code (Code 4) should be changed to use Metall (Code 5). Specifically, we applied the following changes:

- Use Metall to allocate the graph at Step 2 (line 9–10). 880
- Add a graph reattach mode (line 16–17). 880

As shown in Code 5, Metall scopes provide a way to exit the program and reattach to the previously created data, avoiding construction time. This would be helpful to many graph analytics applications where the data structure reconstruction can be completely avoided.

Code 4: Workflow of a graph analytics with GBTL

```

1 void main() {
2   // Step 1. Read edgelist
3   vector<pair<int, int>> edges;
4   int nv; // #of vertices
5   int ne; // #of edges
6   read_edge_list("./edge_list.txt", &edges, &nv, &ne);
7
8   // Step 2. Allocate graph object
9   auto* g = new Graph(nv, ne);
10
11  // Step 3. Ingest edgelist to build graph
12  g->build(edges);
13
14  // Step 4. Run graph analytics
15  run_analytics(*g);
16
17  delete g;
18

```

Code 5: Workflow of a graph analytics with GBTL and Metall. This code is based on Code 4.

```

1 using Graph_t = Graph<metall::allocator<std::byte>>;
2 void main() {
3   Graph_t *g;
4   metall::manager *mgr;
5   if (create_new) { // Create a new graph
6     // 1) Read edgelist, no change (code is omitted)
7
8     // 2) Allocate graph object
9     mgr = new metall::manager(metall::create_only,
10                               "/ssd/graph");
11    g = mgr.construct<Graph_t>("g")(nv, ne,
12                                   mgr.get_allocator());
13
14    // 3) Ingest edgelist to build graph, no change
15    g->build(edges);
16  } else {
17    // Re-attach graph
18    mgr = new metall::manager(metall::open_read_only,
19                               "/ssd/graph");
20    g = mgr.find<Graph_t>("g").first;
21
22    // 4) Run graph analytics, no change
23    run_analytics(*g);
24
25    delete mgr;
26

```

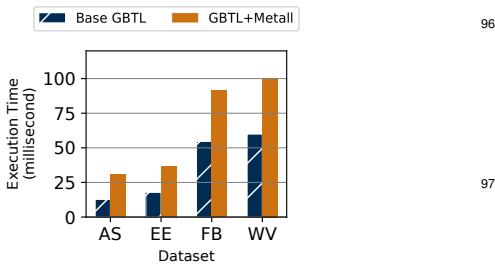


Figure 7: Graph construction time on the EPYC machine with four graph datasets. Base GBTL ran on DRAM. GBTL+Metall ran on the NVMe SSD. 965

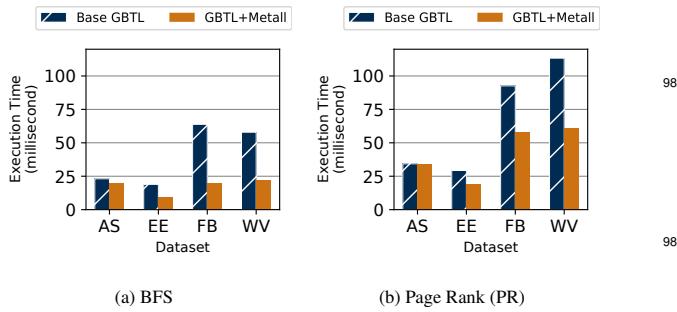


Figure 8: Graph analytic time on the EPYC machine using four graph datasets. Base GBTL ran on DRAM and includes a graph construction time. GBTL+Metall ran on the NVMe SSD and includes time of reattaching a previously constructed graph. 975

7.4. Demonstration

We use the breadth-first search (BFS) and page rank (PR) implemented in GBTL as a real application use case to demonstrate Metall persistent memory allocator benefits. BFS is a traversal algorithm that starts at a given source vertex and produces a list of vertices that are reachable from the source vertex by traversing the edges of the graph. In each iteration, only the vertices adjacent to a newly discovered vertex are processed. Page rank is a variant of the eigenvector centrality algorithm, which measures the influence of a node in a network. It gives a rough estimate of measuring the importance of 990 website pages.

We used four datasets from SNAP [25]: as-733 (AS) [26], email-Eu-core (EE) [27], ego-Facebook (FB) [28], and wiki-Vote (WV) [29]. Those graphs contain 1K–7K vertices and 14K–104K edges. 1005

We first show the graph construction time of the original GBTL (*Base GBTL*) and our 'metallized' GBTL (*GBTL+Metall*) in Figure 7. Base GBTL constructs a graph on DRAM, whereas GBTL+Metall does that on the NVMe SSD so that it can reuse the graph data later. GBTL+Metall was 1010 roughly 2X slower than Base GBTL. This is mainly due to the graph being constructed on the SSD device.

Next, we performed BFS and page rank using the two GBTL implementations (Figure 8). The Base GBTL cases include graph construction time as Base GBTL needs to 1015 construct a graph from scratch every time launching a graph

analytics program. On the other hand, GBTL+Metall avoids the reconstruction time by simply reattaching a previously constructed graph. Thus, the GBTL+Metall cases include only graph reattaching time (graphs were stored in the NVMe SSD). GBTL+Metall achieved 3.5X better BFS execution time over Base GBTL (Figure 8a). Metall's real benefit comes into the picture when multiple analytics run on the same previously constructed graph data, completely avoiding the graph reconstruction time. Similar results were observed from the page rank analytic time in Figure 8b.

By integrating Metall in GBTL, we were able to avoid the heavy graph construction time. This capability will be helpful to many graph analytics applications where the data structure reconstruction can be completely avoided.

Memory-mapped persistent pre-built data structures are helpful in enabling interactive real-time data science applications with large persistent data structures in unprecedented scales of data without going through traditional serialization and data structure reconstruction. Application developers can create custom complex persistent and consistent data structures. This ability to attach and detach from previously created datasets in a lightweight manner gives a powerful workflow software productivity benefit. 980

8. Related Work

8.1. Large-scale Graph Processing with Persistent Memory

Many studies have been conducted for large-scale graph processing on persistent memory using mmap and showed notable performance [30, 31], including evaluating large-scale graph processing on Intel Optane DC Persistent Memory (e.g., [32, 33]). Metall is aiming at helping application developers with respect to memory allocation and data management.

8.2. Memory Allocator

Boost.Interprocess (BIP) [3] offers higher-level allocator mechanisms on top of a file-backed mmap mechanism. However, as it is not designed intentionally as a persistent allocator, there are some drawbacks. For example, 1) BIP uses a single tree to manage memory allocations — such design will suffer from many allocations and not scale well with multiple threads due to lock contention; 2) it is not capable of deallocating file (persistent memory) space. On the other hand, as Metall is not designed for interprocess communication, it does not support process synchronization. Except for the restriction, applications that already work with the allocators in Boost.Interprocess (especially managed_mapped_file allocates) should work with Metall without modification.

NVmalloc [34] enables applications to allocate memory on a distributed non-volatile memory (NVM) storage system. NVmalloc creates a file per memory allocation request, but it does not have a mechanism of aggregating multiple allocations into a single file — creating a file and mapping it to the main memory are expensive operations; therefore, NVmalloc will

not be suitable when the application causes many relatively small allocations.

Persistent Memory Development Kit (PMDK) [12] is a collection of libraries focusing on byte-addressable persistent memory. `libpmemobj` in PMDK provides a persistent memory allocator like Metall with a fine-grained persistence policy (see Section 3.3). `nvm_malloc` [35] is another work for byte-addressable persistent memory with a fine-grained persistence policy. `Ralloc` [15] also targets byte-addressable persistent memory. It supports failure-atomic memory allocations by asking applications to write a function that traverses all active pointers. `Ralloc` possesses better performance over `libpmemobj` [15]. Compared with those works, Metall is designed for both block devices and byte-addressable persistent memory so that applications can utilize a wide range of persistent memory technologies in their environment. We also demonstrated that Metall showed competitive performance with `Ralloc` (Section 6).

Several studies have been conducted and shown remarkable performance as for heap memory allocator, e.g., `jemalloc` [16] and `Superalloc` [4]. However, unfortunately, it is not trivial to extend those allocators for persistent memory because allocators themselves also have to be stored in persistent memory so that they can resume the previous status.

8.3. System Software

Several projects investigated `mmap` technology. For example, `DI-MMAP` [36] improved `mmap` page cache performance; `UMap` is a user-level `mmap` library that lets users control the page cache policy more flexibly [37].

Failure-atomic `msync()` (FAMS) [38] is a mechanism that guarantees that the backing file of a `mmap()` region always reflects the most recent successful `msync()`, regardless of crashes. FAMS could be useful to enhance Metall’s failure atomic support. There are several FAMS implementations. For example, it is implemented in `NOVA` filesystem [39]. `famus` is a user-level FAMS library [40].

8.4. Persistent Data Store

In terms of storing data persistently, a key-value store is a popular database model and is designed to scale to a very large size easily. (for example, `LevelDB` [41], `RocksDB` [42], and `MongoDB` [43] use `mmap`). Metall’s key benefit is that it allows for custom data structures to be stored, not just key-value pairs.

Hierarchical Data Format (HDF) [44], particularly `HDF5`, has been used in many large-scale data analytics applications. It allows applications to store data in portable formats. On the other hand, Metall is designed as a lightweight data store library by limiting data portability.

9. Conclusion

Thanks to the recent notable performance improvements, various types of NVRAM devices are already used in many HPC systems today. We anticipate that persistent data-centric analytics will be a powerful model for accelerating

next-generation large-scale data analytics. To leverage various persistent memory devices in the current and future exascale HPC systems, we developed a persistent memory allocator Metall. Metall allows applications to allocate data structures into persistent memory transparently with a reasonable code migration cost.

Metall achieved up to 11.7x and 48.3x performance improvements over `Boost.Interprocess` and `memkind` (PMEM kind), respectively, on the dynamic graph construction workload with the node-local conventional NVMe SSD and the node-local emerging byte-addressable persistent memory. We demonstrated Metall’s high adaptability and its benefit on the real graph processing workload using `GBTL`. We also investigated and developed the techniques for improving sparse data update performance on network-attached file systems.

This study’s outcomes indicate that Metall will be a strong tool for accelerating future large-scale data analytics by enabling applications to efficiently and fully leverage persistent memory.

Acknowledgment

This work was performed under the auspices of the U.S. Department of Energy by Lawrence Livermore National Laboratory under Contract DE-AC52-07NA27344 (LLNL-JRNL-825588). Experiments were performed at the Livermore Computing facility.

This research was supported by the Exascale Computing Project (17-SC-20-SC), a collaborative effort of the U.S. Department of Energy Office of Science and the National Nuclear Security Administration. Funding from LLNL LDRD project 21-ERD-020 was used in this work.

References

- [1] Exascale computing project, <https://www.exascaleproject.org/>, (Accessed on 03/29/2021).
- [2] D. A. Reed, J. Dongarra, Exascale computing and big data, *Commun. ACM* 58 (7) (2015) 56–68. doi:10.1145/2699414. URL <https://doi.org/10.1145/2699414>
- [3] Boost C++ Libraries, <https://www.boost.org/>, (Accessed on 03/29/2021).
- [4] B. C. Kuszmaul, SuperMalloc: A super fast multithreaded malloc for 64-bit machines, in: *Proceedings of the 2015 International Symposium on Memory Management, ISMM ’15*, ACM, 2015, pp. 41–55. doi:10.1145/2754169.2754178.
- [5] GraphBLAS Template Library (GBTL), v. 3.0, <https://github.com/cmu-sei/gbtl>, (Accessed on 03/29/2021).
- [6] Memkind, <http://memkind.github.io/memkind/>, (Accessed on 03/29/2021).
- [7] J. Lüttgau, M. Kuhn, K. Duwe, Y. Alforov, E. Betke, J. Kunkel, T. Ludwig, Survey of storage systems for high-performance computing, *Supercomputing Frontiers and Innovations* 5 (1) (2018). URL <https://www.superfri.org/superfri/article/view/162>
- [8] T. Hirofuchi, R. Takano, A prompt report on the performance of intel optane dc persistent memory module, *IEICE TRANSACTIONS on Information and Systems* 103 (5) (2020) 1168–1172.
- [9] G. Lee, S. Shin, W. Song, T. J. Ham, J. W. Lee, J. Jeong, Asynchronous i/o stack: A low-latency kernel i/o stack for ultra-low latency ssds, in: *2019 USENIX Annual Technical Conference (USENIX ATC 19)*, USENIX Association, Renton, WA, 2019, pp. 603–616. URL <https://www.usenix.org/conference/atc19/presentation/lee-gyusun>

[10] Intel Optane SSD DC, <https://www.intel.com/> (August 2021).

[11] K. Nguyen, L. Fang, C. Navasca, G. Xu, B. Demsky, S. Lu, Skyway: Connecting managed heaps in distributed big data systems, in: Proceedings of the Twenty-Third International Conference on Architectural Support for Programming Languages and Operating Systems, ASPLOS '18, Association for Computing Machinery, New York, NY, USA, 2018, pp. 56–69. doi:10.1145/3173162.3173200. URL <https://doi.org/10.1145/3173162.3173200>

[12] pmem.io Persistent Memory Programming, <https://pmem.io/>, (Accessed on 03/29/2021).

[13] S. Haria, M. D. Hill, M. M. Swift, MOD: Minimally ordered durable datastructures for persistent memory, in: Proceedings of the Twenty-Fifth International Conference on Architectural Support for Programming Languages and Operating Systems, ASPLOS '20, Association for Computing Machinery, New York, NY, USA, 2020, pp. 775–788. doi:10.1145/3373376.3378472. URL <https://doi.org/10.1145/3373376.3378472>

[14] ioctl_fclonerange(2) - Linux manual page, https://man7.org/linux/man-pages/man2/ioctl_fclonerange.2.html, (Accessed on 03/29/2021).

[15] W. Cai, H. Wen, H. A. Beadle, C. Kjellqvist, M. Hedayati, M. L. Scott, Understanding and optimizing persistent memory allocation, in: Proceedings of the 2020 ACM SIGPLAN International Symposium on Memory Management, ISMM 2020, Association for Computing Machinery, New York, NY, USA, 2020, pp. 60–73. doi:10.1145/3381898.3397212. URL <https://doi.org/10.1145/3381898.3397212>

[16] jemalloc, <http://jemalloc.net/>, (Accessed on 03/29/2021).

[17] Lustre, <https://www.lustre.org/>, (Accessed on 03/29/2021).

[18] A. Uselton, N. Wright, A file system utilization metric for i/o characterization, in: Proc. of the Cray User Group conference, 2013.

[19] Pagemap, from the userspace perspective, <https://www.kernel.org/doc/Documentation/vm/pagemap.txt>, (Accessed on 03/29/2021).

[20] Exascale NAS — whitepaper — VAST Data, <https://vastdata.com/exascale-nas-whitepaper/>, (Accessed on 03/29/2021).

[21] D. Chakrabarti, Y. Zhan, C. Faloutsos, R-MAT: A recursive model for graph mining, in: Proceedings of the 2004 SIAM International Conference on Data Mining, SIAM, 2004, pp. 442–446.

[22] C. Boucher, A. Bowe, T. Gagie, S. Puglisi, K. Sadakane, Variable-order de bruijn graphs, in: 2015 Data Compression Conference, 2015. doi:10.1109/DCC.2015.70.

[23] J. Chen, H. Fang, Y. Saad, Fast approximate knn graph construction for high dimensional data via recursive lanczos bisection, in: Journal of Machine Learning Research 10 (2009), 2009.

[24] J. Kepner, P. Aaltonen, D. Bader, A. Buluç, F. Franchetti, J. Gilbert, D. Hutchison, M. Kumar, A. Lumsdaine, H. Meyerhenke, S. McMillan, C. Yang, J. D. Owens, M. Zalewski, T. Mattson, J. Moreira, Mathematical foundations of the graphblas, in: 2016 IEEE High Performance Extreme Computing Conference (HPEC), 2016, pp. 1–9. doi:10.1109/HPEC.2016.7761646.

[25] J. Leskovec, A. Krevl, SNAP Datasets: Stanford large network dataset collection, <http://snap.stanford.edu/data> (Jun. 2014).

[26] J. Leskovec, J. Kleinberg, C. Faloutsos, Graphs over time: densification laws, shrinking diameters and possible explanations, in: Proceedings of the eleventh ACM SIGKDD international conference on Knowledge discovery in data mining, 2005, pp. 177–187.

[27] J. Leskovec, J. Kleinberg, C. Faloutsos, Graph evolution: Densification and shrinking diameters, in: ACM Transactions on Knowledge Discovery from Data (ACM TKDD), 2007.

[28] M. J. J. Leskovec, Learning to discover social circles in ego networks, in: Proceedings of the The Neural Information Processing Systems 2012, 2012.

[29] J. Leskovec, D. Huttenlocher, J. Kleinberg, Signed networks in social media, in: Proceedings of the international conference of Human-Computer Interaction 2010, 2010.

[30] R. Pearce, M. Gokhale, N. M. Amato, Multithreaded asynchronous graph traversal for in-memory and semi-external memory, in: Proceedings of the 2010 ACM/IEEE International Conference for High Performance Computing, Networking, Storage and Analysis, SC '10, IEEE Computer Society, 2010, pp. 1–11. doi:10.1109/SC.2010.34.

[31] K. Iwabuchi, S. Sallinen, R. Pearce, B. V. Essen, M. Gokhale, S. Matsumura, Towards a distributed large-scale dynamic graph data store, in: 2016 IEEE International Parallel and Distributed Processing Symposium Workshops (IPDPSW), 2016, pp. 892–901. doi:10.1109/IPDPSW.2016.189.

[32] G. Gill, R. Dathathri, L. Hoang, R. Peri, K. Pingali, Single machine graph analytics on massive datasets using Intel Optane DC Persistent Memory, CorR abs/1904.07162 (2019). arXiv:1904.07162. URL <http://arxiv.org/abs/1904.07162>

[33] I. B. Peng, M. B. Gokhale, E. W. Green, System evaluation of the Intel Optane byte-addressable NVM, arXiv preprint arXiv:1908.06503 (2019).

[34] C. Wang, S. S. Vazhkudai, X. Ma, F. Meng, Y. Kim, C. Engelmann, NVMalloc: Exposing an aggregate SSD store as a memory partition in extreme-scale machines, in: 2012 IEEE 26th International Parallel and Distributed Processing Symposium, 2012, pp. 957–968. doi:10.1109/IPDPS.2012.90.

[35] D. Schwalb, T. Berning, M. Faust, M. Dreseler, H. Plattner, nvm_malloc: Memory allocation for NVRAM., ADMS@ VLDB 15 (2015) 61–72.

[36] B. V. Essen, H. Hsieh, S. Ames, M. Gokhale, DI-MMAP: A high performance memory-map runtime for data-intensive applications, in: 2012 SC Companion: High Performance Computing, Networking Storage and Analysis, 2012, pp. 731–735. doi:10.1109/SC.Companion.2012.99.

[37] I. Peng, M. McFadden, E. Green, K. Iwabuchi, K. Wu, D. Li, R. Pearce, M. Gokhale, Umap: Enabling application-driven optimizations for page management, in: 2019 IEEE/ACM Workshop on Memory Centric High Performance Computing (MCHPC), 2019, pp. 71–78. doi:10.1109/MCHPC49590.2019.00017.

[38] T. Kelly, Persistent memory programming on conventional hardware, Queue 17 (4) (2019) 10:1–10:20. doi:10.1145/3358955.3358957. URL <http://doi.acm.org/10.1145/3358955.3358957>

[39] J. Xu, S. Swanson, NOVA: A log-structured file system for hybrid volatile/non-volatile main memories, in: 14th USENIX Conference on File and Storage Technologies (FAST 16), USENIX Association, Santa Clara, CA, 2016, pp. 323–338. URL <https://www.usenix.org/conference/fast16/technical-sessions/presentation/xu>

[40] famus: Failure-atomic msync() in user space, <http://web.eecs.umich.edu/tpkelly/famus/>.

[41] LevelDB, <https://github.com/google/leveldb>, (Accessed on 03/29/2021).

[42] RocksDB: A Persistent Key-Value Store for Flash and RAM Storage, <https://github.com/facebook/rocksdb>, (Accessed on 03/29/2021).

[43] K. Chodorow, MongoDB: the definitive guide: powerful and scalable data storage, ” O'Reilly Media, Inc.”, 2013.

[44] The HDF Group - ensuring long-term access and usability of HDF data and supporting users of HDF technologies, <https://www.hdfgroup.org/>, (Accessed on 02/20/2021).