

*Exceptional service in the national interest*



# Single event effects induced by heavy ions in SONOS charge trapping memory arrays

**T. Patrick Xiao**<sup>1</sup>, Christopher H. Bennett<sup>1</sup>, Sapan Agarwal<sup>1</sup>, David R. Hughart<sup>1</sup>,  
Hugh J. Barnaby<sup>2</sup>, Helmut Puchner<sup>3</sup>, A. Alec Talin<sup>1</sup>, Matthew Marinella<sup>1</sup>

<sup>1</sup>Sandia National Laboratories, Albuquerque, NM

<sup>2</sup>Arizona State University, Temple, AZ

<sup>3</sup>Infineon Technologies, San Jose, CA

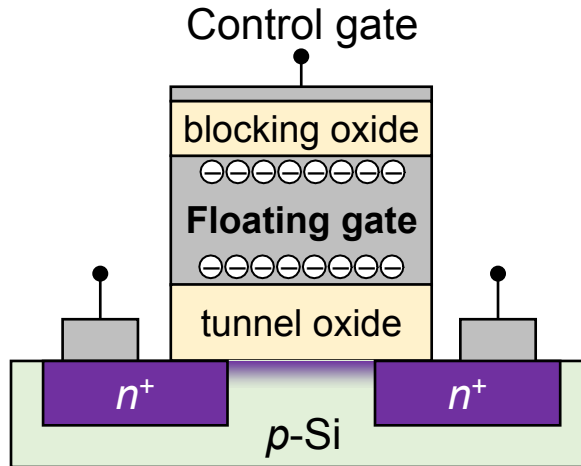
txiao@sandia.gov



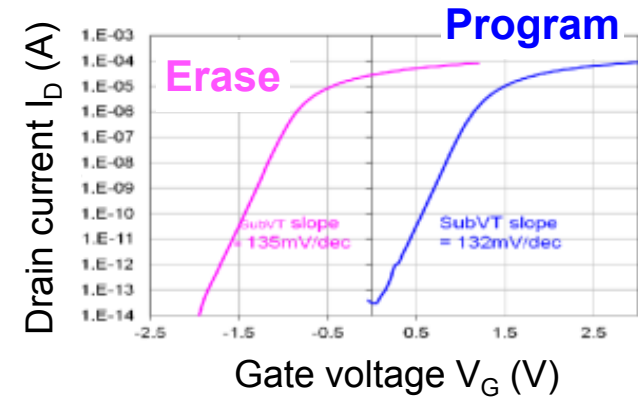
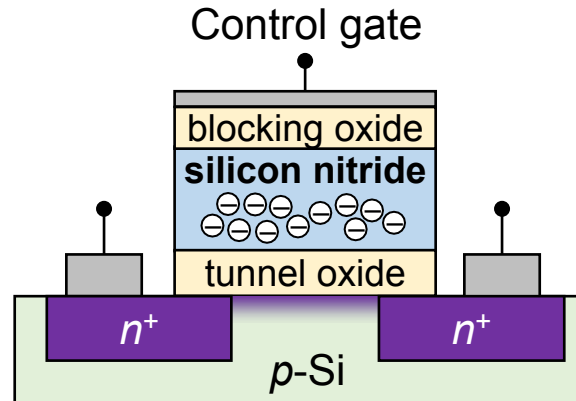
Sandia National Laboratories is a multimission laboratory managed and operated by National Technology and Engineering Solutions of Sandia, LLC, a wholly owned subsidiary of Honeywell International Inc., for the U.S. Department of Energy's National Nuclear Security Administration under contract DE-NA0003525.

# SONOS charge trap memory

## Floating-gate flash



## SONOS flash [Si-oxide-nitride-oxide-Si]



K. Ramkumar et al, *IMW* 2013

Stored charge is confined more strongly in a SONOS gate stack

- Improves retention
- Improves endurance & yield
- Enables thinner oxide
  - Lower programming voltage
  - Reduced cell cross-talk, more scalable
  - Simpler CMOS process integration
- Better radiation response?



**SAMSUNG**

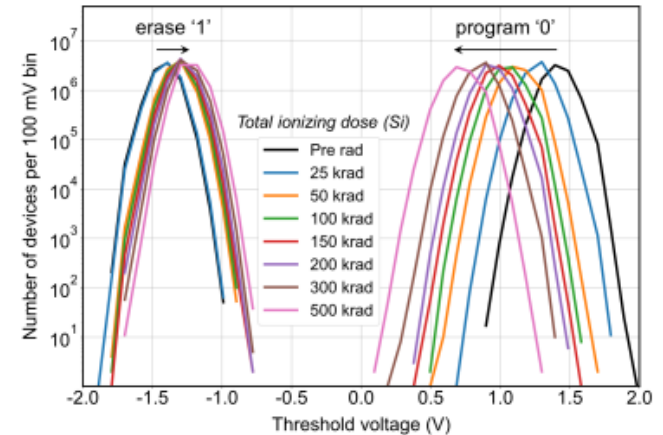
**TOSHIBA** + others

# A quick survey of the radiation effects literature on flash memory

✓ = literature exists

	Floating-gate	SONOS
Total ionizing dose	✓	✓
Single event effects	✓	?

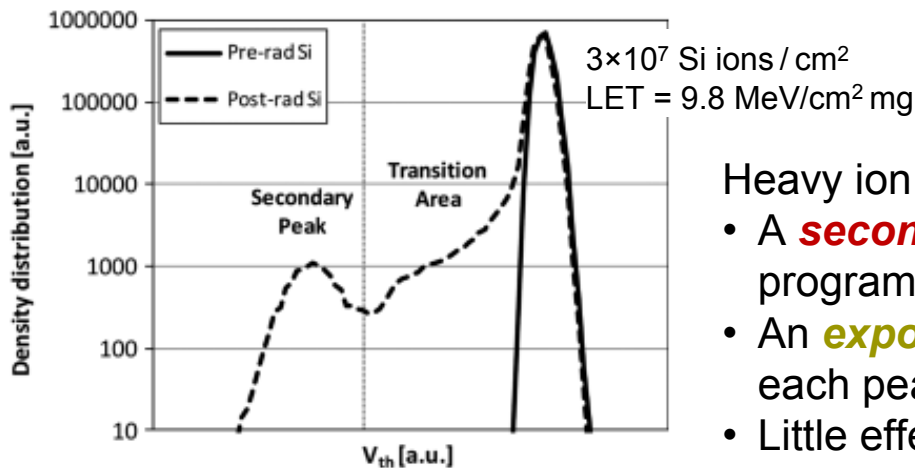
## 40nm Cypress (Infineon) SONOS



*TID shifts the  $V_T$  distributions in both SONOS and floating gate*

H. Puchner et al TNS 2014, T. Xiao et al TNS 2021

## Numonyx (Micron) 65nm NOR floating-gate



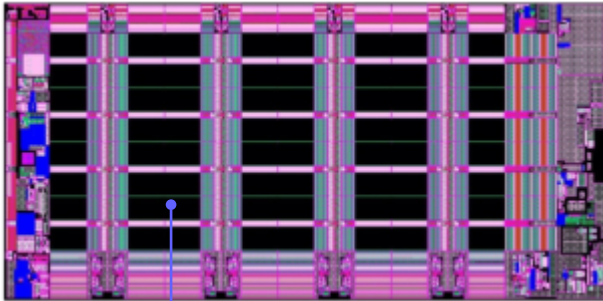
Heavy ion irradiated floating-gate arrays show:

- A **secondary  $V_T$  peak** to the left of the main program peak
- An **exponential tail** of affected devices around each peak
- Little effect on the erase state

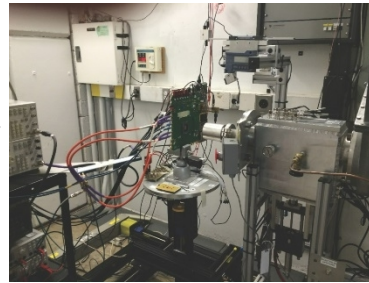
S. Gerardin et al TNS 2010, G. Cellere et al TNS 2006

# Heavy ion irradiation experiments

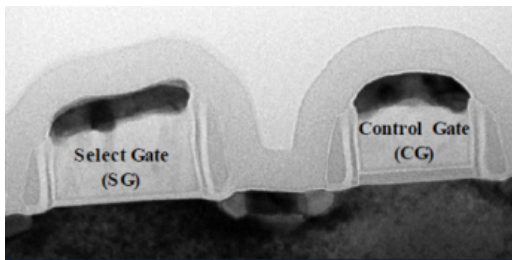
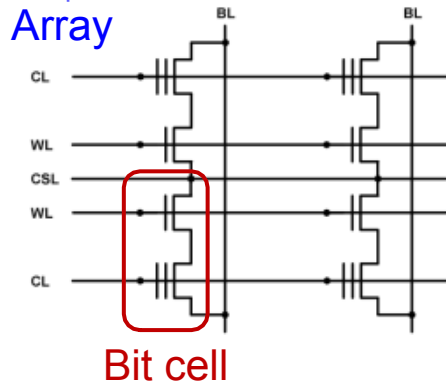
 40nm SONOS chip, 8 Mb



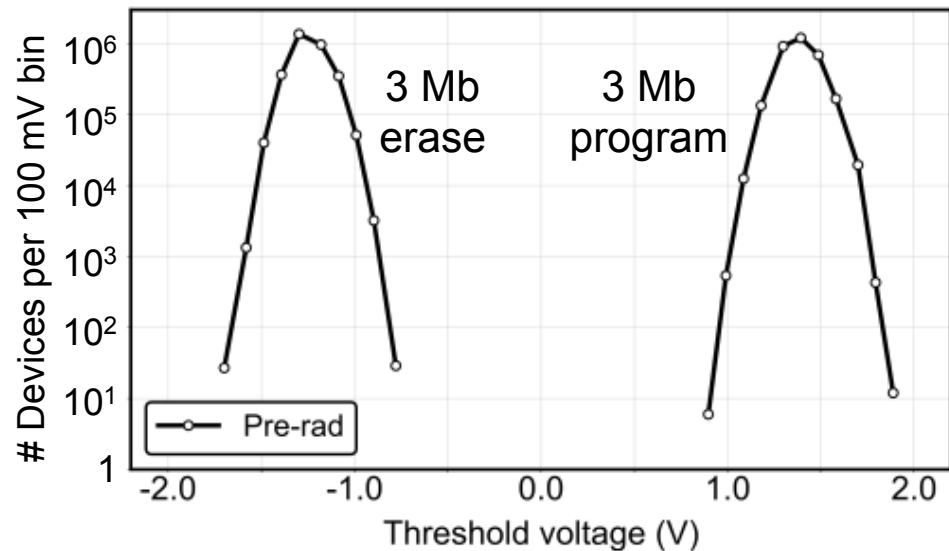
Texas A&M University K500 Cyclotron



- Beams: 1259 MeV Kr, 599 MeV Ar
- Flux:  $0.2\text{--}1.0 \times 10^5$  ions/cm<sup>2</sup>/s
- Different tilt angles of beam relative to test chip to vary LET (10-51 MeV/cm<sup>2</sup>mg)
- Cells left unbiased during irradiation, at 25°C unless noted



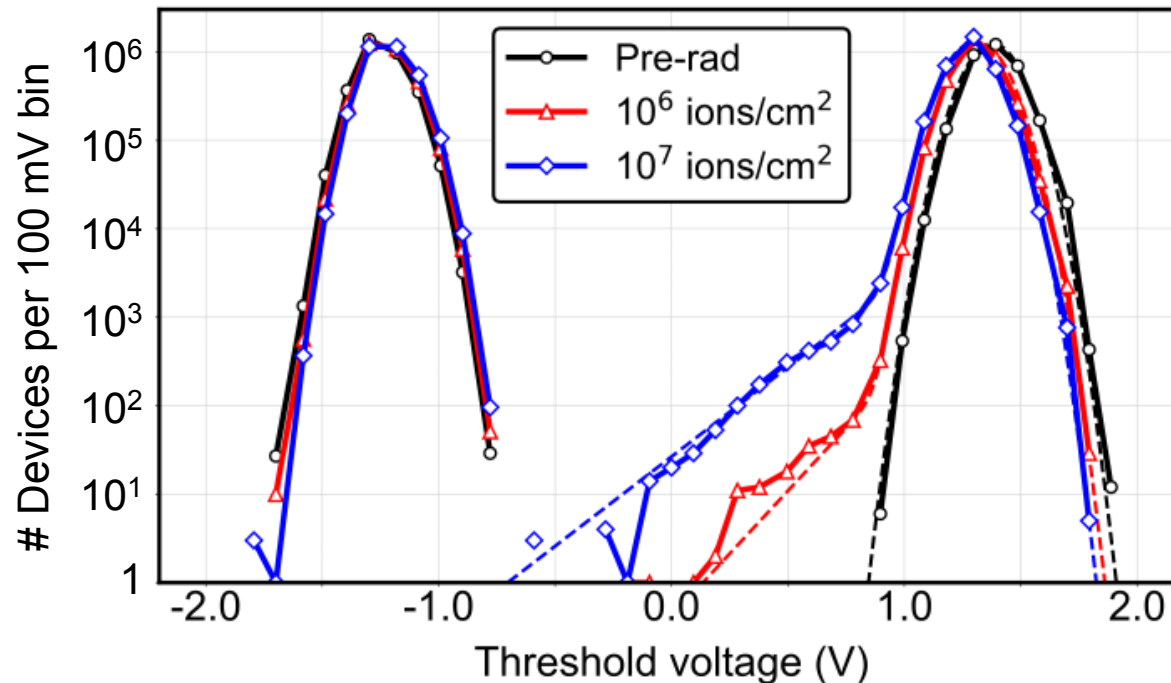
***V<sub>T</sub> distribution prior to irradiation (6 Mb)***



I. Kouznetsov et al, *IMW* 2018

# Kr ion irradiation

LET = 51.1 MeV cm<sup>2</sup>/mg in tunnel oxide



--- Analytical fit using normal distribution + one-sided exponential distribution

Fluence [ions/cm <sup>2</sup> ]	# affected cells*
10 <sup>6</sup>	195
10 <sup>7</sup> (exp 1)	1775
10 <sup>7</sup> (exp 2)	2483

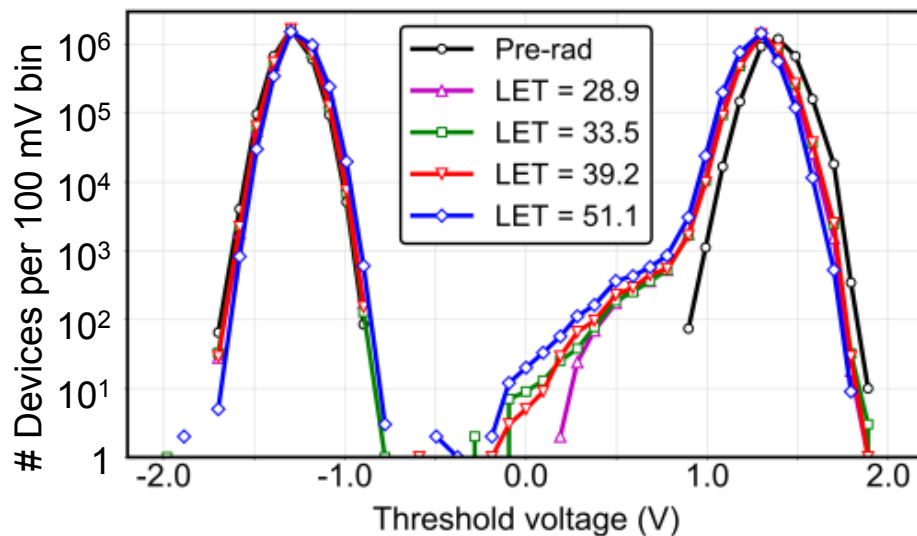
\* Affected =  $V_T$  is more than  $5\sigma$  below the mean  $\mu$  of the normal distribution

- **No secondary  $V_T$  peak**
- Observed an exponential  $V_T$  tail of affected cells
  - The effect is linear with beam fluence
- No effect on the erase state ( $E$ -field  $\approx 0$ )

# Single event effects LET dependence

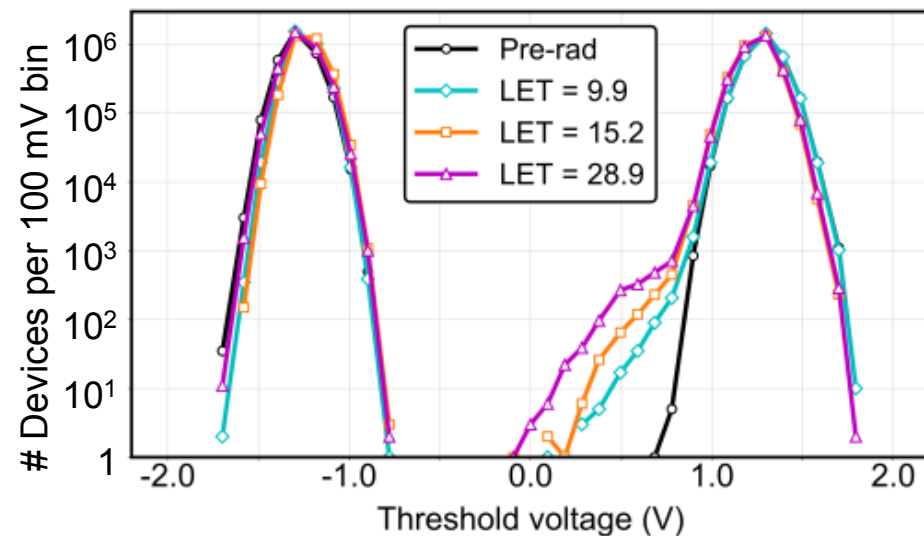
## Test chip B (6 Mbit)

Kr ions at 0°, 30°, 42°, 55° incidence  
10<sup>7</sup> ions/cm<sup>2</sup>

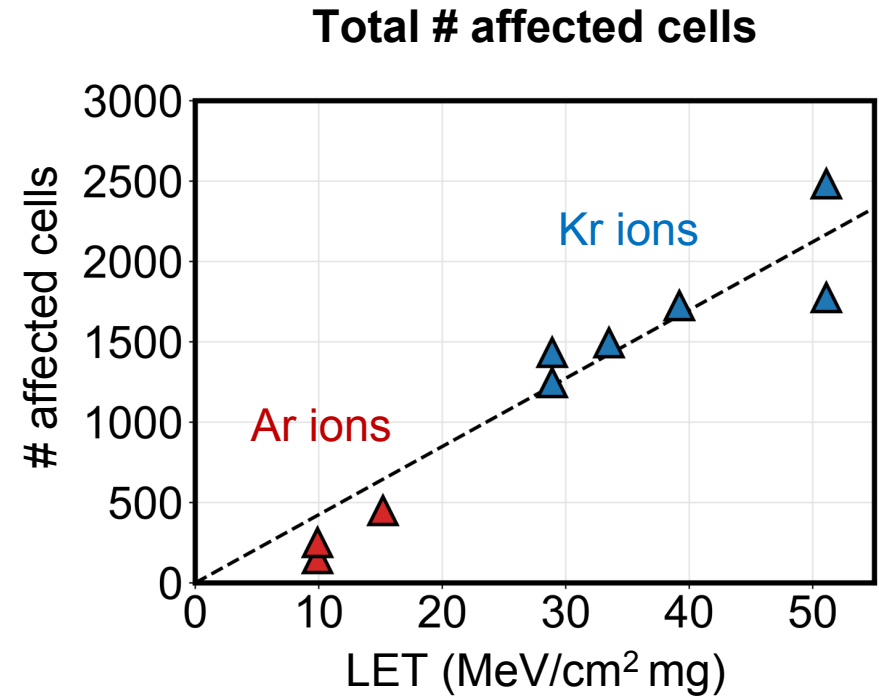
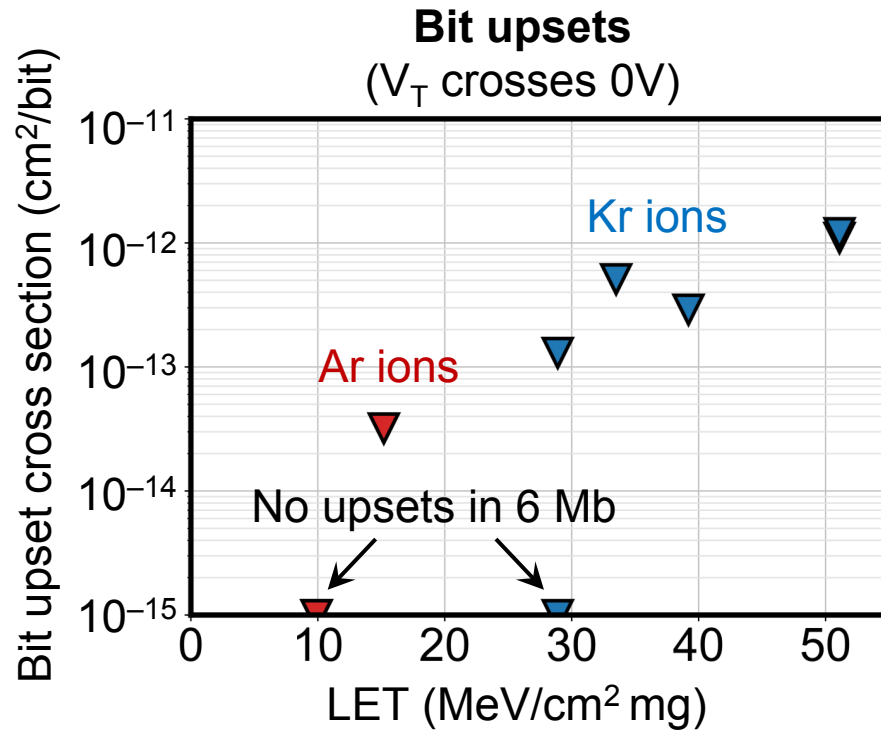


## Test chip C (6 Mbit)

Kr ions at 0°, Ar ions at 30°, 55°  
10<sup>7</sup> ions/cm<sup>2</sup>

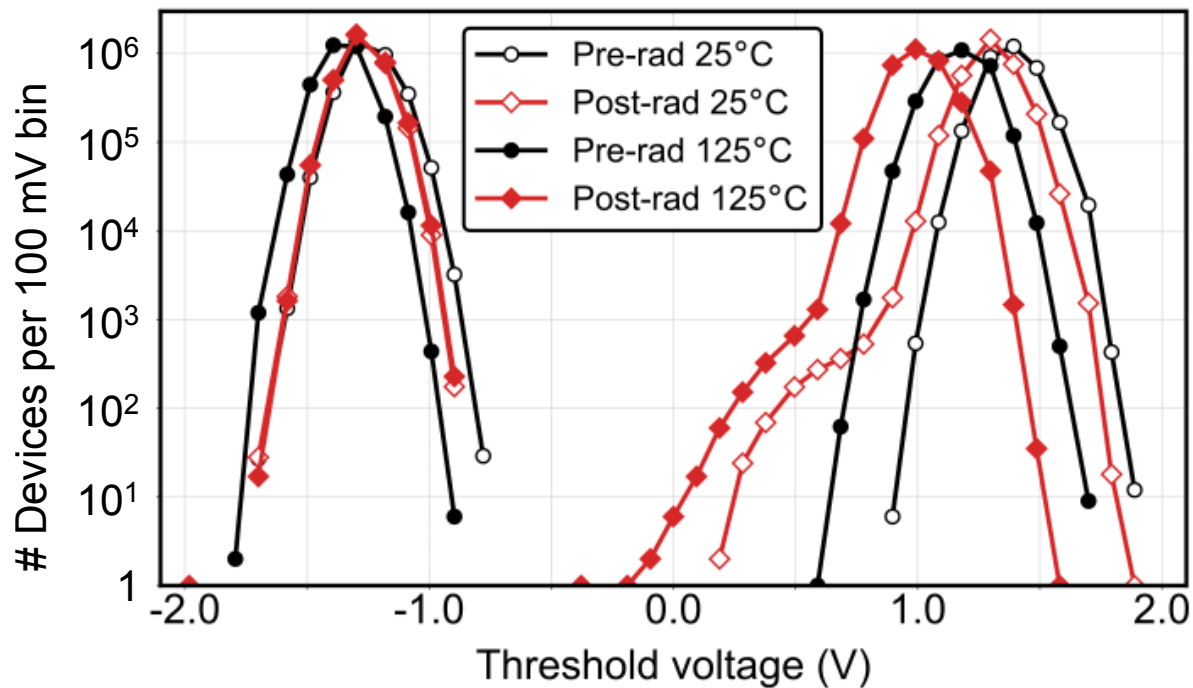


# Single event effects LET dependence



- The number of cells in the tail is roughly linear with LET

# Single event effects temperature dependence

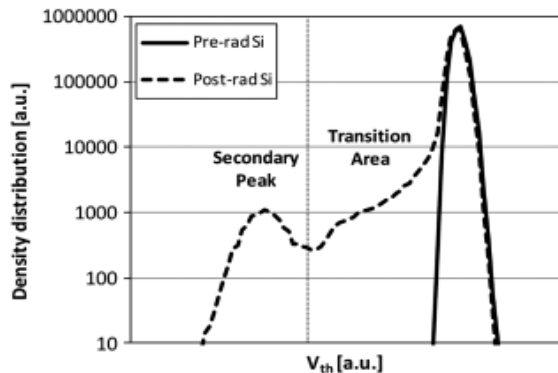


- Lateral shift in both distributions likely due to temperature dependence of program circuitry
- Ion-induced  $V_T$  tail is narrower (lower average  $\Delta V_T$ ) at high temperature due to the lower program state electric field



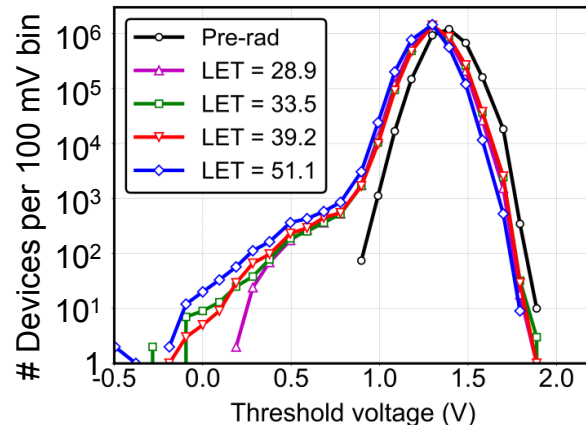
# The missing secondary peak (hypothesis)

## 65nm floating gate



$3 \times 10^7$  Si ions/cm<sup>2</sup>, LET = 9.8 MeV/cm<sup>2</sup> mg  
Gerardin et al, TNS 2010

## 40nm SONOS



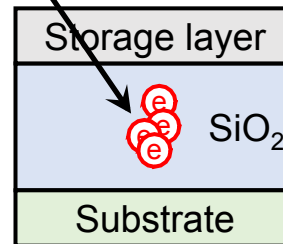
$10^7$  Kr ions / cm<sup>2</sup>

\*Meftah et al, *Phys Rev B* 1994.  
\*Toulemonde et al, *Phys Rev B* 1992.  
\*Cellere et al, *TNS* 2006.

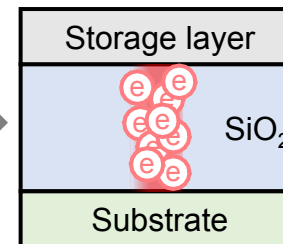
- In floating-gate, the secondary peak is believed to be caused by a direct ion hit to the gate stack. A prevailing theory for its origin is the *thermal spike model* [\*]

1) Ion strike creates hot carriers

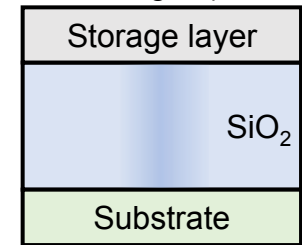
heavy ion



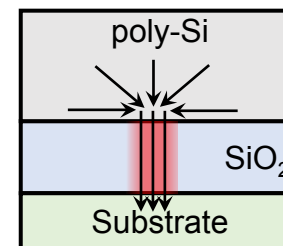
2) Carriers cool and diffuse. Phonons heat up



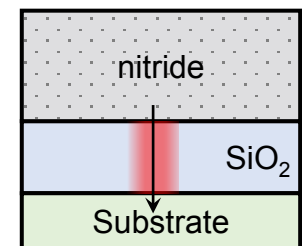
4) Oxide cools, removing filament (but may leave an amorphous region)



3) High temperature causes localized phase change in SiO<sub>2</sub> and creates a conductive filament through it



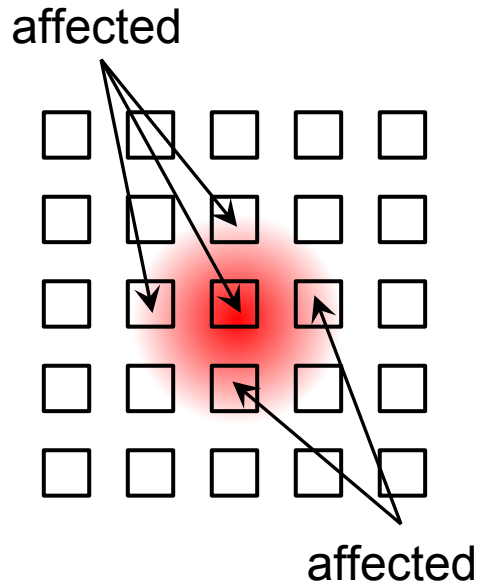
**FG:** Stored charge across the conducting FG funnels through the pinhole



**SONOS:** Since storage layer is insulating, only the charge closest to the filament is lost

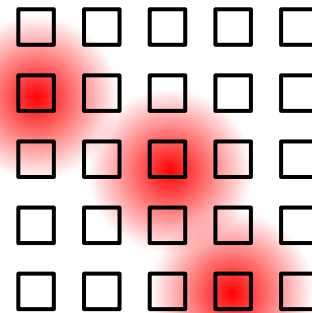
# The exponential $V_T$ tail (hypothesis)

Cells are affected by a nearby ion strike  
Effect is similar to floating gate  
(Gerardin TNS 2010, Cellere TNS 2006)

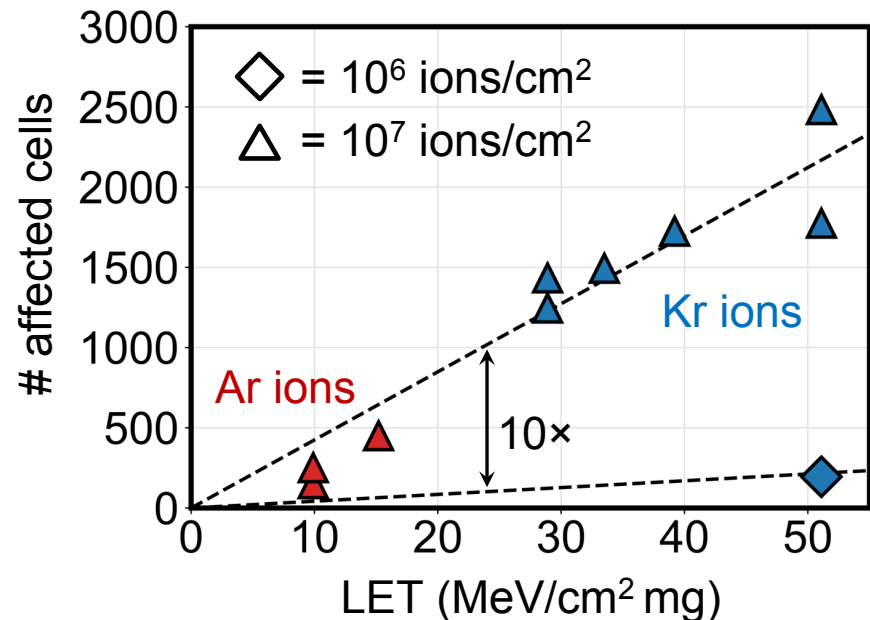
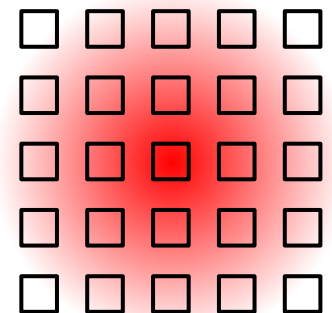


Energy of the ion strike diffuses laterally via carriers and phonons, and this energy promotes electrons out of traps

Higher fluence



Higher LET



# Conclusions

- We conducted the first study of heavy-ion-induced single event effects in SONOS flash memory
- No ion-induced secondary peak in the  $V_T$  distribution; SONOS cells that are directly struck by an ion are not strongly affected: **a significant distinction from floating-gate flash**
- An exponential tail in the distribution due to charge loss in cells in the vicinity of an ion strike; the further the cell from the strike, the smaller the  $V_T$  shift (similar to floating-gate)
- Future work
  - Stronger experimental validation of the thermal spike model for direct ion strikes
  - Better understanding of how energy propagates to distant SONOS cells to induce charge leakage

***Thank you!***