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Single event effects induced by heavy ions in SONOS charge trapping memory arrays

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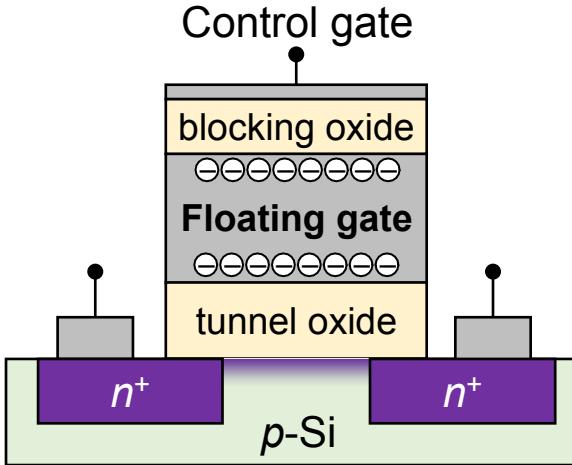
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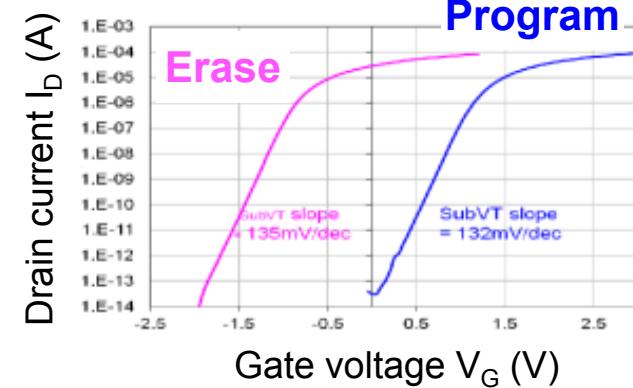
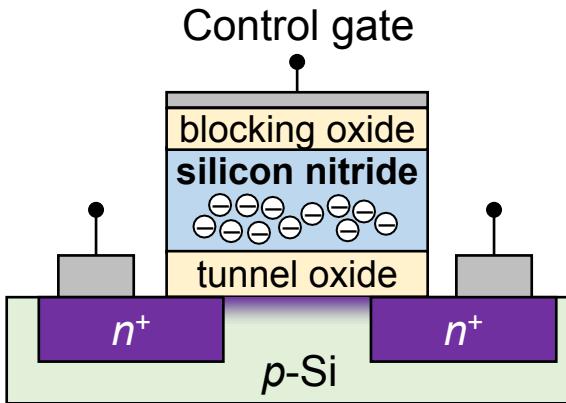
SONOS charge trap memory

Floating-gate flash



SONOS flash

[Si-oxide-nitride-oxide-Si]



K. Ramkumar et al, IMW 2013

Stored charge is confined more strongly in a SONOS gate stack

- Improves retention
- Improves endurance & yield
- Enables thinner oxide
 - Lower programming voltage
 - Reduced cell cross-talk, more scalable
 - Simpler CMOS process integration
- Better radiation response?





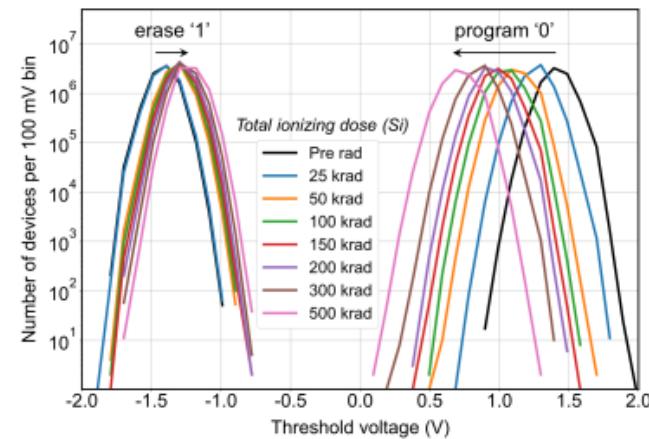
+ others

A quick survey of the radiation effects literature on flash memory

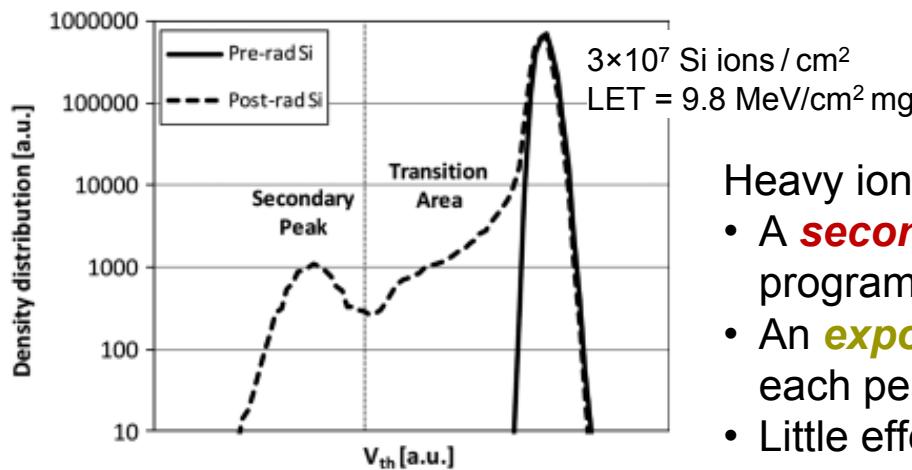
✓ = literature exists

	Floating-gate	SONOS
Total ionizing dose	✓	✓
Single event effects	✓	?

40nm Cypress (Infineon) SONOS



Numonyx (Micron) 65nm NOR floating-gate



S. Gerardin et al TNS 2010, G. Cellere et al TNS 2006

TID shifts the V_T distributions in both SONOS and floating gate

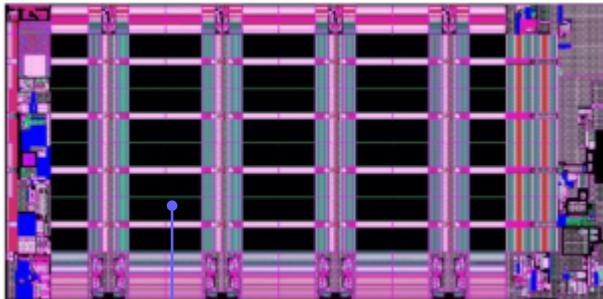
H. Puchner et al TNS 2014, T. Xiao et al TNS 2021

Heavy ion irradiated floating-gate arrays show:

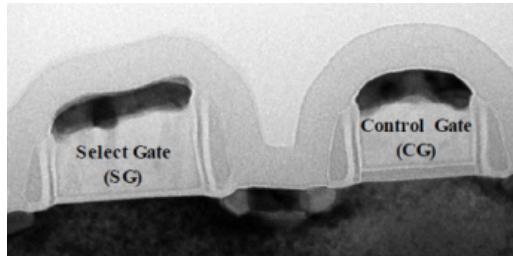
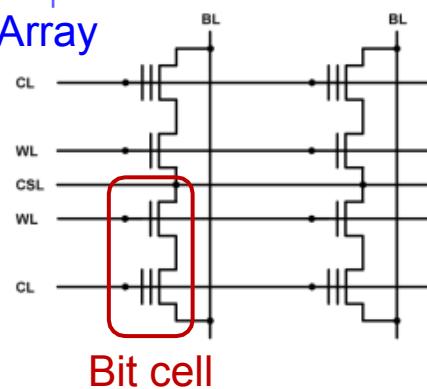
- A **secondary V_T peak** to the left of the main program peak
- An **exponential tail** of affected devices around each peak
- Little effect on the erase state

Heavy ion irradiation experiments

 40nm SONOS chip, 8 Mb



Array



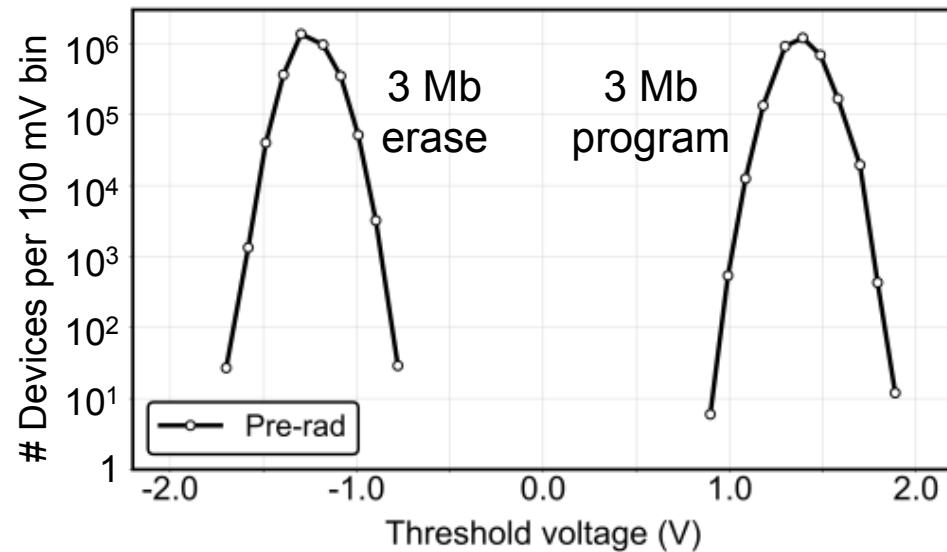
I. Kouznetsov et al, IMW 2018

Texas A&M University K500 Cyclotron



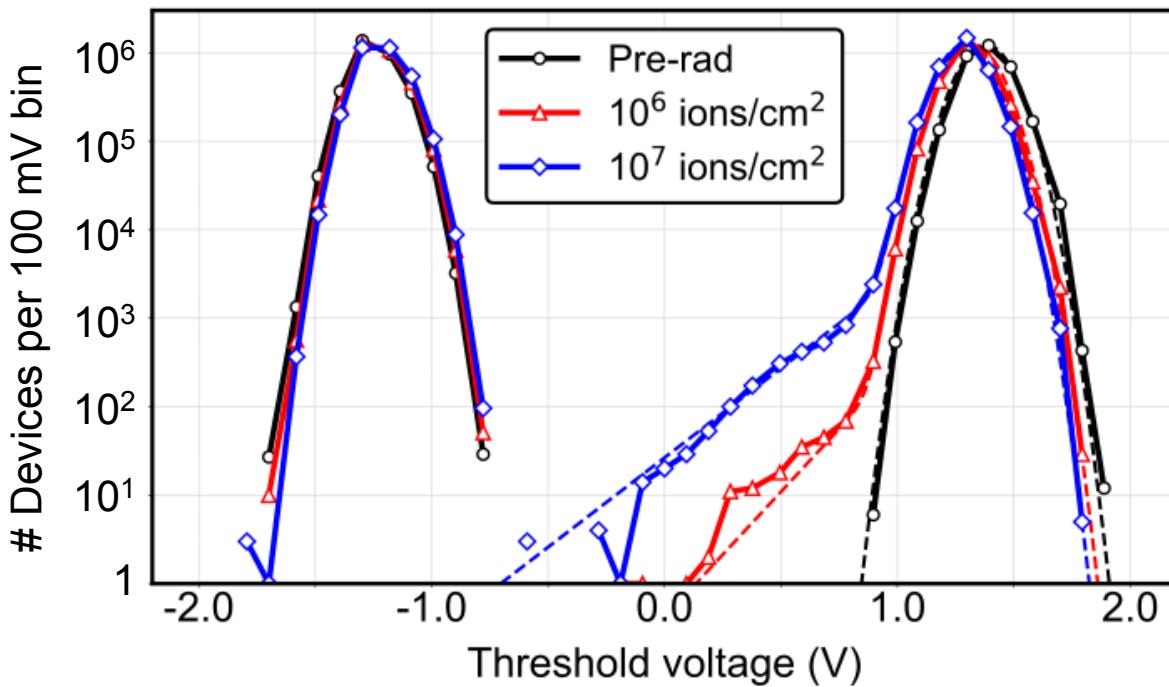
- Beams: 1259 MeV Kr, 599 MeV Ar
- Flux: $0.2\text{-}1.0 \times 10^5$ ions/cm²/s
- Different tilt angles of beam relative to test chip to vary LET (10-51 MeV/cm²mg)
- Cells left unbiased during irradiation, at 25°C unless noted

V_T distribution prior to irradiation (6 Mb)



Kr ion irradiation

LET = 51.1 MeV cm²/mg in tunnel oxide



— Analytical fit using
normal distribution +
one-sided exponential
distribution

Fluence [ions/cm ²]	# affected cells*
10 ⁶	195
10 ⁷ (exp 1)	1775
10 ⁷ (exp 2)	2483

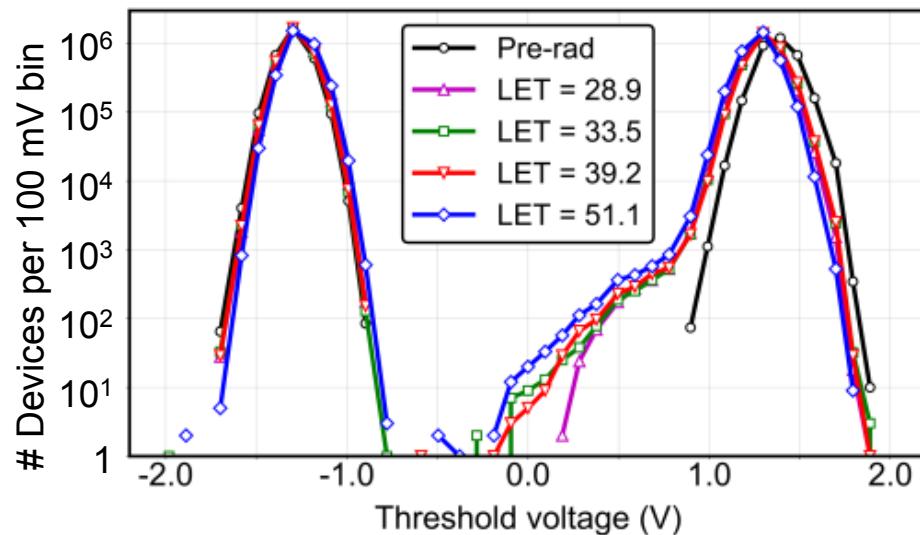
* Affected = V_T is more than 5σ below the mean μ of the normal distribution

- **No secondary V_T peak**
- Observed an exponential V_T tail of affected cells
 - The effect is linear with beam fluence
- No effect on the erase state (E -field ≈ 0)

Single event effects LET dependence

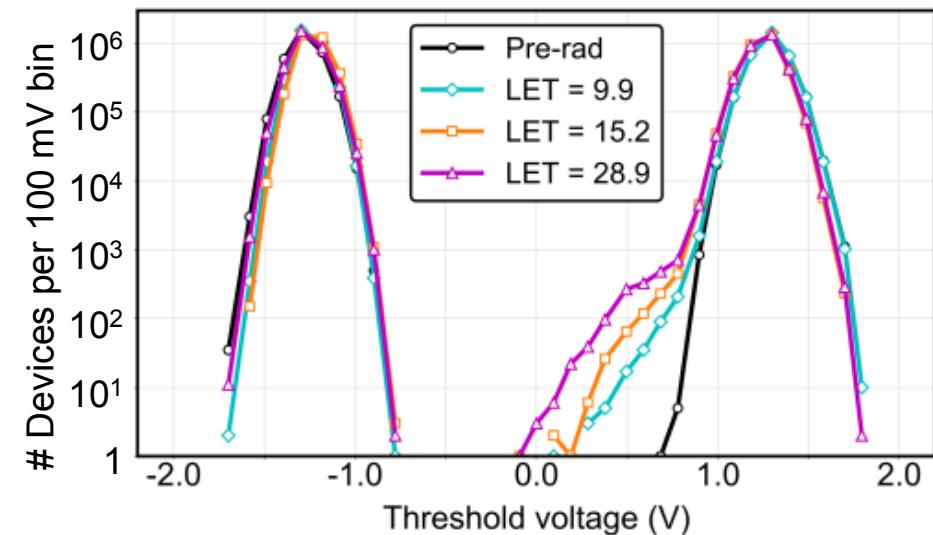
Test chip B (6 Mbit)

Kr ions at 0° , 30° , 42° , 55° incidence
 10^7 ions/cm 2

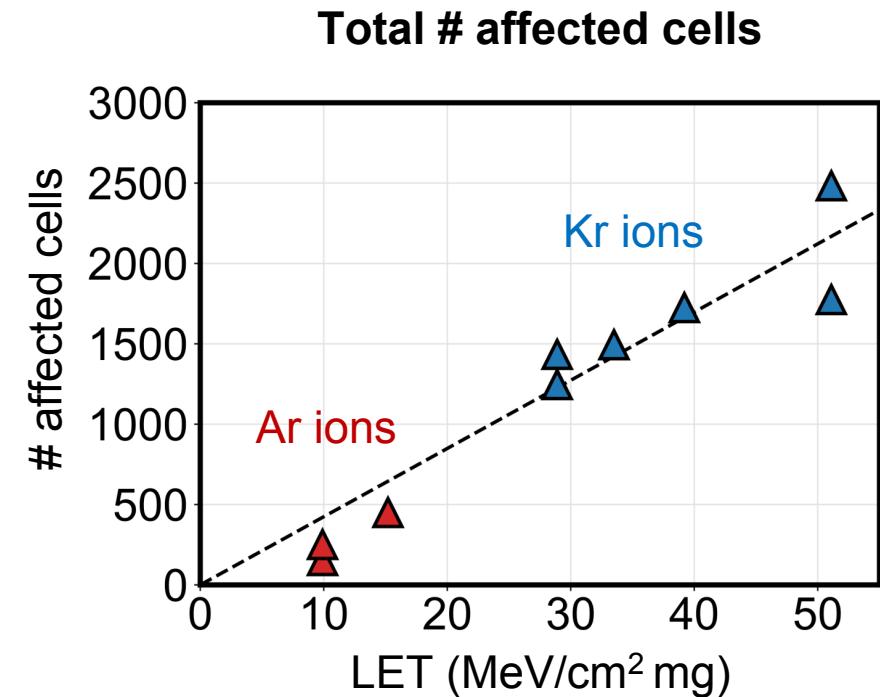
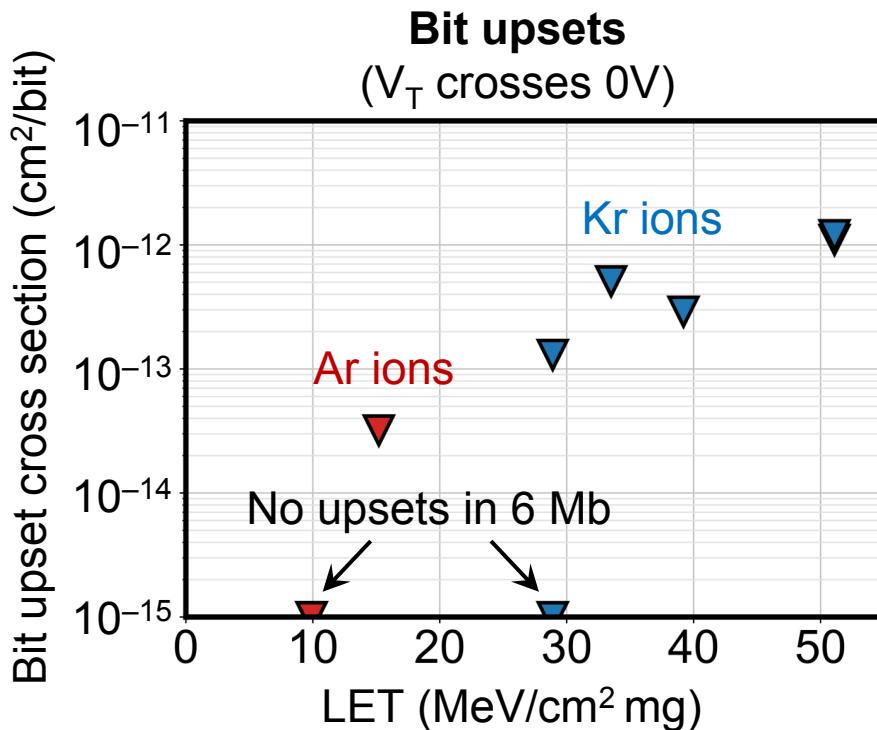


Test chip C (6 Mbit)

Kr ions at 0° , Ar ions at 30° , 55°
 10^7 ions/cm 2

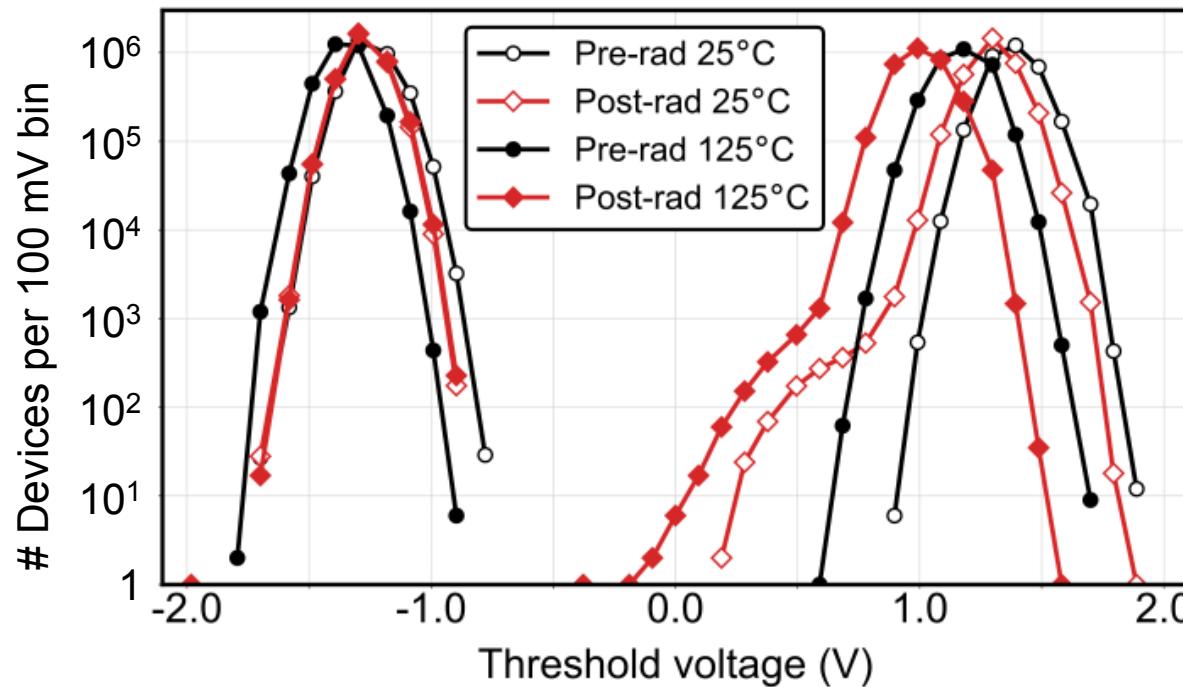


Single event effects LET dependence



- The number of cells in the tail is roughly linear with LET

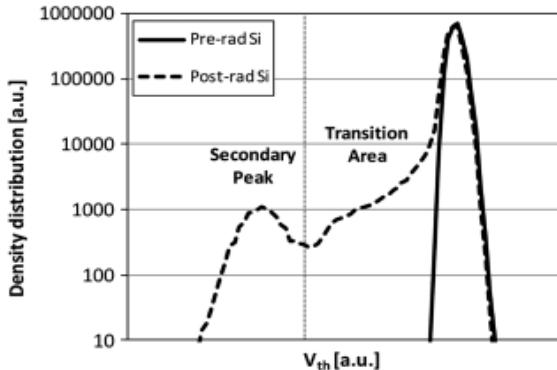
Single event effects temperature dependence



- Lateral shift in both distributions likely due to temperature dependence of program circuitry
- Ion-induced V_T tail is narrower (lower average ΔV_T) at high temperature due to the lower program state electric field

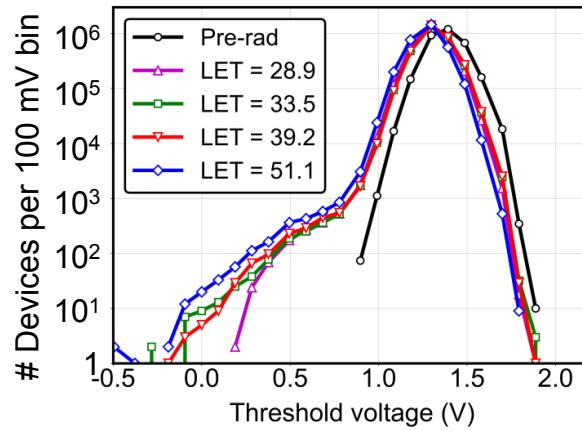
The missing secondary peak (hypothesis)

65nm floating gate



3×10^7 Si ions/cm², LET = 9.8 MeV/cm² mg
Gerardin et al, TNS 2010

40nm SONOS



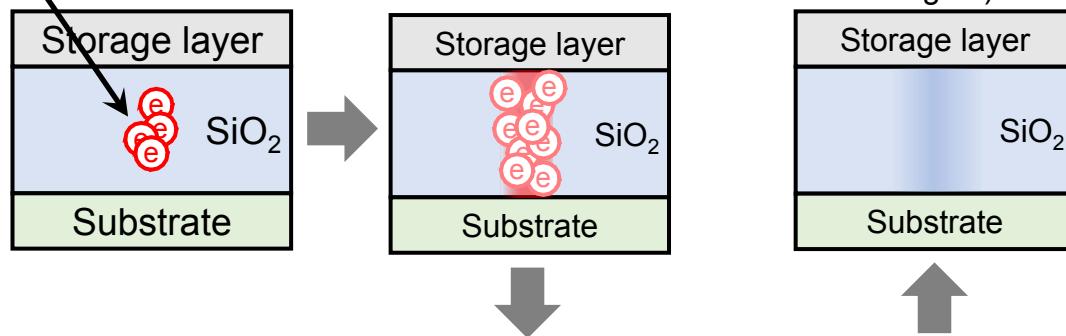
*Meftah et al, Phys Rev B 1994.

*Toulemonde et al, Phys Rev B 1992.

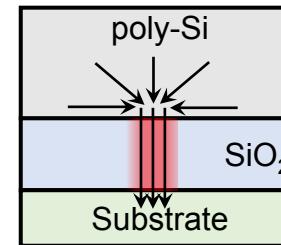
*Cellere et al, TNS 2006.

- In floating-gate, the secondary peak is believed to be caused by a direct ion hit to the gate stack. A prevailing theory for its origin is the *thermal spike model* [*]

- 1) Ion strike creates hot carriers
- 2) Carriers cool and diffuse. Phonons heat up
- 4) Oxide cools, removing filament (but may leave an amorphous region)



- 3) High temperature causes localized phase change in SiO₂ and creates a conductive filament through it

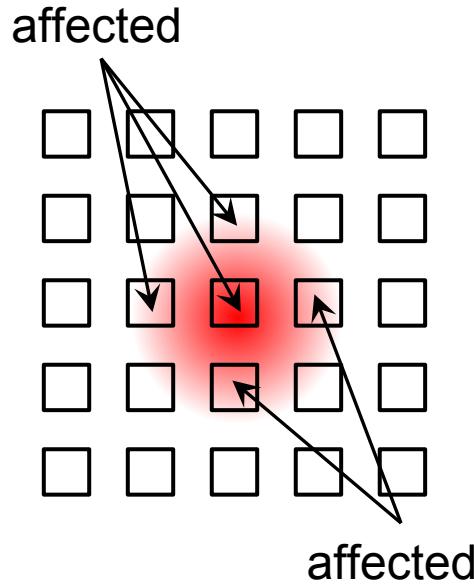


FG: Stored charge across the conducting FG funnels through the pinhole

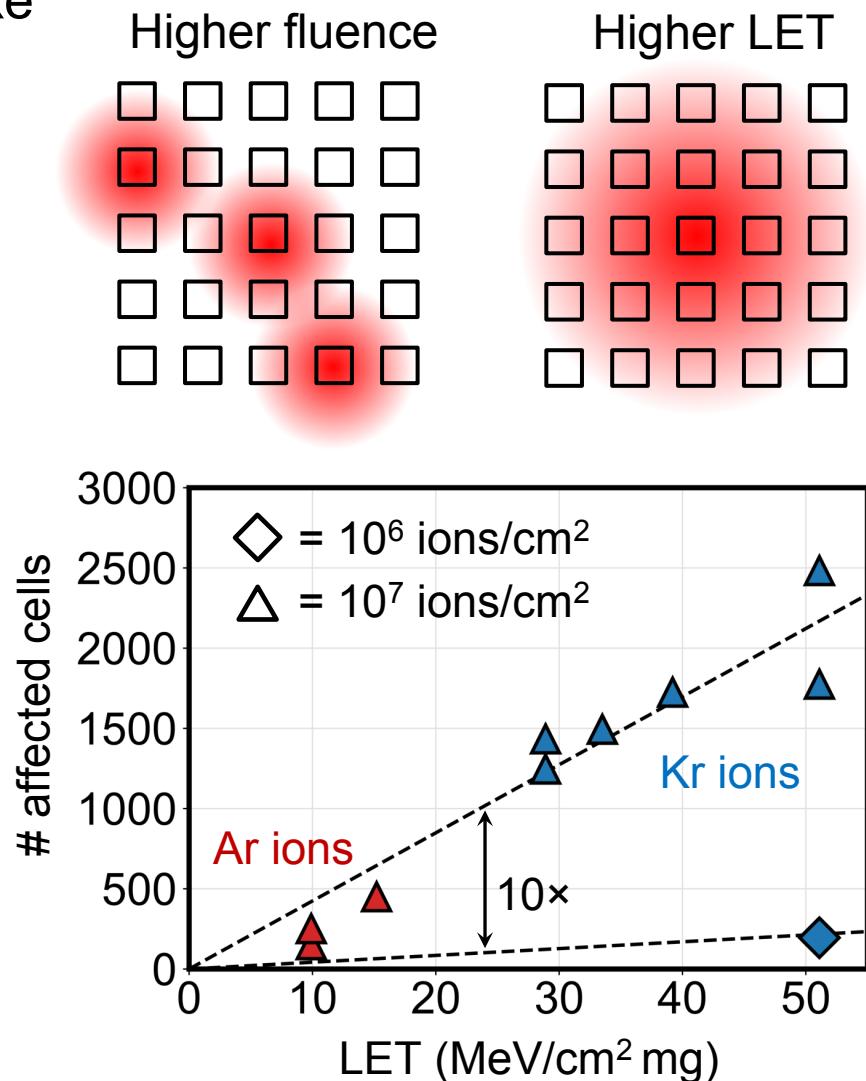
SONOS: Since storage layer is insulating, only the charge closest to the filament is lost

The exponential V_T tail (hypothesis)

Cells are affected by a nearby ion strike
 Effect is similar to floating gate
 (Gerardin TNS 2010, Cellere TNS 2006)



Energy of the ion strike diffuses laterally via carriers and phonons, and this energy promotes electrons out of traps



Conclusions

- We conducted the first study of heavy-ion-induced single event effects in SONOS flash memory
- No ion-induced secondary peak in the V_T distribution; SONOS cells that are directly struck by an ion are not strongly affected: **a significant distinction from floating-gate flash**
- An exponential tail in the distribution due to charge loss in cells in the vicinity of an ion strike; the further the cell from the strike, the smaller the V_T shift (similar to floating-gate)
- Future work
 - Stronger experimental validation of the thermal spike model for direct ion strikes
 - Better understanding of how energy propagates to distant SONOS cells to induce charge leakage

Thank you!