

Title/subtitle

Contributions to Dielectric Constant Enhancement in Thin Film Metal Insulator Metal Capacitors

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Abstract

Thin film metal-insulator-metal capacitors were fabricated with varying dielectric and electrode thicknesses and areas. Measurement of the dielectric properties of the capacitors consistently yielded higher than predicted capacitance values, which prompted the exploration of three factors that could enhance the capacitance. Modeling of the fringing capacitance and field enhancement due to the capacitor geometry, both yield capacitance gains, albeit insufficient to fully explain the observed behavior. The

possible role of changes in polarizability of the dielectric film constituents, due to the amorphous nature of the films was also evaluated, but the results of the investigation of this contribution were inconclusive. The role of accumulation/depletion layers on capacitance enhancement was also analyzed. For capacitors fabricated with thinner dielectrics, the presence of these layers can effectively reduce the dielectric thickness, resulting in contributions to capacitance that are significant. The calculations completed suggest these contributions may be more important than fringing field contributions. Additional studies of these mechanisms are in progress.

Keywords—

Thin Film Capacitor, Fringe Field Capacitance, Dielectric Enhancement

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2. INTRODUCTION

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A. *Thin Film Capacitors on LTCC Substrates*

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Thin film capacitors fabricated on low temperature cofired ceramic (LTCC) substrates are of interest for reducing the number of solder joints and discrete components for high density multichip modules (MCMs) and incorporation into glass interposers.¹⁻³ In this work, thin film metal-insulator-metal capacitors have been developed with varying electrode and dielectric compositions for application frequencies up to 15 MHz. The capacitors also have varying thickness and area in order to achieve specified capacitance values between 10 pF and 10 nF. Dielectric materials were selected for their stable and predictable dielectric constant and high breakdown strengths that allow operation of thin devices at fields approaching 10^6 V/cm.

While the overall research included a larger set of materials, the dielectric utilized in this study was Al₂O₃. Polycrystalline Al₂O₃ is a linear dielectric with a dielectric constant of \sim 9.8. Combined with its electrical resistivity $>10^{12}$ Ω cm, Al₂O₃ is desirable for capacitor applications that require exposure to elevated temperatures and voltage. In thin film form, capacitance values of interest can be achieved with dielectric constants \leq 10, making these materials suitable choices for many development efforts.

However, in this work nearly all experimental samples exhibited higher than predicted, or enhanced, capacitance values. Potential contributions to the enhanced capacitance include: (i) fringe effect capacitance; (ii) polarizability changes due to the amorphous structure of the materials; and (iii) the presence of accumulation or inversion layers in the dielectric layers, resulting in effectively thinner dielectric layers. These contributions have been studied individually and previously have been thought to be a

1 minor contribution, and therefore, of minimal use to consider for devices of the size and
2 capacitance studied here. However, as the operational frequencies of capacitors have
3 increased to 50-100 GHz and beyond, these contributions can minimize insertion losses
4 and therefore their usefulness has increased.⁴ Understanding the contributions to the
5 apparent enhancement of capacitance may facilitate more accurate predictive models of
6 such devices in new higher frequency applications.

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8 *B. Fringe Field Contributions to Capacitance*

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10 For a dielectric under bias, electric field lines extend beyond the geometry of the
11 electrodes through the surrounding medium, be that air or an encapsulant. The relative
12 magnitude of this contribution will depend upon the dielectric constant, K, of the
13 dielectric and the surrounding medium, the geometry of the electrode edge, and the
14 overall size of the capacitor. For the latter, smaller capacitors exhibit a relatively larger
15 fringe effect as the ratio of the capacitor perimeter to the area increases.

16 In semiconductors and circuit design, fringe capacitance can lead to parasitic and
17 undesirable performance, however in discrete devices it can be used to engineer
18 enhanced performance.⁴ Accurately understanding and characterizing the extent of
19 fringe capacitance has been studied through empirical methods and basic experiments.⁵

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7 For example, Subramanian et al.⁸ developed the following Eq 1 to empirically
8 account for the fringe capacitance contribution, where the edge capacitance
9 contribution, C_e , is a function of the capacitor perimeter, P , and the dielectric thickness,
10 t .

$$C_e = [0.019 \ln(P/t) - 0.043]P \quad (1)$$

1 However, little work has been published that utilizes modern physics-based modeling
2 tools to evaluate factors that contribute to, and can therefore be used, to predict and
3 engineer the extent of fringe capacitance within specific devices. In this study, the
4 fringe field capacitance was calculated using 3D electromagnetic analysis software by
5 applying physics-based calculations.

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14 *C. Polarizability Contributions to Capacitances*

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16 Thin film dielectrics are typically amorphous, and as such, can certainly exhibit
17 dielectric constants significantly different from crystalline systems of the same
18 composition. Intuitively, the lower density of an amorphous system should lead to a
19 lower K, simply due to the lower density of charged species in the material associated
20 with increased atomic spacing. However, this assumes the ionic polarizability is
21 constant. The more open structure of an amorphous system could lead to higher ionic
22 displacements under field application. For example, Shannon, et al.⁹ observed that the
23 greater molar volume of glasses and amorphous phases, compared to crystalline
24 compounds of identical compositions, led to an abnormal positive deviation of dielectric
25 constant. They postulated that the “rattling” of loosely bound cations and disordered
26 oxygen anions contributed to the enhancement. Given these different possible
27 contributions to dielectric constant, determining the effects of amorphicity on
28 contributions to capacitance was a second goal of this study. The dielectric
29 polarizability, consisting of both ionic and electronic polarizabilities, has long been
30 estimated for crystalline materials using the Clausius-Mossotti equation, and that
31 approach is used in this work to explore contributions of polarizability to the observed
32 dielectric constant.

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54 *D. Accumulation and Inversion Layer Contributions to Capacitance*

Calculation of the dielectric constant from the capacitance value incorporates the dielectric thickness; for thin film capacitors this is typically measured directly using a profilometer. Yet this basic assumption may be incorrect if the effective dielectric thickness is actually less due to the formation of semiconducting accumulation or inversion layers within the dielectric immediately adjacent to the electrode. In the world of semiconductors, it is well known that these layers form between two materials in contact with each other due to differences in the work function. Estimations of the relative impact of this effect are provided in this work.

2. EXPERIMENTAL

A. Capacitor Fabrication and Characterization

A series of thin film Al_2O_3 capacitors with Al electrodes was fabricated on DuPont 951 LTCC substrates using sputter deposition through physical “shadow” masks. As shown in Fig. 1, each substrate consisted of 48 capacitors designed with electrode widths of 150, 375, 530, and 750 μm , yielding active areas of approximately 0.0225 to 0.5625 mm^2 . There are 16 different electrode configurations in this layout, with 10 different capacitor areas. The parallel plate capacitors were fabricated with a targeted bottom metal electrode of 500nm thickness, Al_2O_3 dielectric of 1000nm thickness, and a top metal electrode of 500nm thickness. Metal electrode layers were deposited from 99.99% pure metal targets at 300 W using a DC magnetron power supply. The Al_2O_3 dielectric was deposited from a 99.9% pure ceramic target at 200 W using an RF power supply. All materials were deposited with a Denton (Moorestown, NJ) Discovery 18 sputter deposition system using an atmosphere of 99.999% pure Ar at a working pressure of 5-8 mtorr. No substrate heating was used during deposition. Variations in

1 electrode width due to shadowing, and dielectric thicknesses due to process variations,
2 were accounted for by making lateral and thickness measurements of all capacitors
3 using a KLA Tencor (Milpitas, CA) P-17 profilometer system. A representative
4 dimensional measurement is shown in Fig. 2.

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6 Dielectric characterization was performed in a Faraday cage using a Hewlett
7 Packard (Santa Clara, CA) 4194A Impedance Analyzer at 1 KHz, 10 KHz, and 100
8 KHz at room temperature. Though not reported in this work, capacitors fabricated
9 using these processes have also been characterized up to 50 GHz.¹⁰

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12 *B. Modeling of Fringe Field Effects and Fringe Field Capacitance*

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14 In this study, models were developed that approximate the physical structure of the
15 as-fabricated devices to account for observed tapering of the electrode and dielectric
16 layers. Tapering of the layers is a result of the use of a physical “shadow” mask and
17 physical vapor deposition.

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19 Models were developed for the capacitor devices using CST Microwave Studio
20 (3DS Dassault Systemes) using the electrostatic solver in which the capacitor
21 dimensions for the models utilized the defined physical mask dimensions. Perfect
22 electrical conductors (PECs) were used for the top and bottom electrodes with the
23 dielectric material between the two electrodes. The simulation model is shown in Fig. 3
24 and simulations were carried out where the capacitor was placed on an LTCC substrate
25 with $\epsilon_r = 7.8$.

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27 Tapers were added to the edges of all dielectric and electrode layers, however, only
28 the taper of the electrodes impacted the model calculations since the dielectric taper was
29 outside of the active device area. The dielectric followed the contour of the bottom

1 electrode taper, as show in Fig. 4. The tapered devices have the same overall active
2 area as the non-tapered. The taper starts 10, 100, 200, or 300 μm from the edge of the
3 nominal dimensions. Tapering was added to the edge of electrodes as illustrated in Fig.
4 4 and Fig. 5, and the dielectric layer followed the contour of the electrode layer below
5 it. Where the taper length was greater than $\frac{1}{2}$ the nominal width of the electrode, that
6 taper length was not applied to that electrode geometry.
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9 Each of the taper geometry angles was calculated using Eq. 2 and ranged from 0.06°
10 to 7.47° in this study depending on the electrode and dielectric thickness combination.
11 The model was completely parameterized to allow for a parameter sweep that changed
12 the dielectric thickness, electrode thickness, capacitor width, and capacitor length. Two
13 sets of simulations were performed: one with fringing fields and one without fringing
14 fields. To eliminate fringing fields, magnetic boundary conditions, which force the
15 tangential magnetic field to zero, were placed on the sides of the capacitor. For the
16 simulation with fringing fields, open boundary conditions were used.
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$$36 \quad \text{Angle (degrees)} = \left(\sin^{-1} \frac{t_{\text{electrode}}}{\text{taperlength}} \right) \times \left(\frac{360}{2\pi} \right) \quad (2)$$

39 A tetrahedral mesh was used for the simulations. The mesh density of the model
40 was increased around the electrode edges as the magnitude of the electric field varied
41 significantly with position near the edges. The finer, more dense mesh near the edges
42 was required for the calculation precision where the field enhancement was significant.
43 Since there was not a field gradient beyond the edge region, a coarser mesh was used,
44 which reduced the complexity of the model without reducing the repeatability of the
45 results. The mesh of the simulation model is shown in Fig. 6 as a top view
46 representation of Fig. 3 looking through the electrode at the meshing of the dielectric
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1 layer. The left image of Fig. 6 provides the entire top view of the model's mesh and the
2 right image is an enlarged view showing the mesh detail at the edges.
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14 An electric potential was defined on the top electrode while no potential was defined
15 on the bottom electrode. An effective dielectric constant was then calculated by solving
16 the parallel-plate capacitor equation using the capacitance from the simulation.
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30 In these simulations, the behavior for Al_2O_3 was modeled, and amorphous SiO_2 , a
31 lower dielectric constant material ($K = 3.3$), was also modeled. The second material
32 was selected for study because of a possible decrease in the dielectric constant of Al_2O_3 ,
33 due to decreased polarizability contributions. Modeling both materials also presented a
34 more complete study of the possible magnitude of fringe field contributions to effective
35 dielectric constant, enabling consideration of device architectures for high frequency
36 use, as noted earlier.
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39 C. Calculations of Polarizability Contributions

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45 As stated above, polarizability contributions of atoms to the dielectric constant may
46 be estimated using the Clausius-Mossotti equation (Eq. 3):
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$$\frac{K_e - 1}{K_e + 2} = \frac{1}{3\epsilon_0} \sum_i n_i \alpha_i \quad (3)$$

49 where K_e is the effective dielectric constant of the material of interest, ϵ_0 is the
50 permittivity of free space ($8.85 \times 10^{-12} \text{ F/m}$), n_i is the number of i atoms, and α_i is the
51 polarizability of i atoms. The contributions of the various electronic polarizabilities N_e ,
52 ionic polarizabilities N_i , and dipolar polarizabilities N_d , can be summed in the Clausius-
53 Mossotti equation in a static electric field and represented with the following equations:
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$$\frac{K_e - 1}{K_e + 2} = \frac{1}{3\epsilon_0} (N_e \alpha_e + N_i \alpha_i + N_d \alpha_d) \quad (4)$$

$$\frac{K_e - 1}{K_e + 2} = \frac{4\pi}{3} N_i \alpha_i \quad (5)$$

Eq. 5 can be rewritten to solve for the predicted K_e when the dielectric constant of the material is unknown, for example, as would be the case for amorphous Al_2O_3 :

$$K_e = \frac{\frac{8\pi}{3} N_i \alpha_i + 1}{1 - \frac{4\pi}{3} N_i \alpha_i} \quad (6)$$

By performing calculations with the reported polarizabilities of Al and O, possible effects of material structure/density on dielectric constant may be analyzed. The results of these calculations were compared with the results reported by Shannon and coworkers⁹, to evaluate contributions to apparent dielectric constant.

D. Estimation of Accumulation or Inversion Layer Depth

For a metal:dielectric interface, the solution to Poisson's equation yields the penetration depth, x (m) of band bending and therefore an estimate of accumulation or inversion layer depth as:

$$x = \sqrt{\frac{2\epsilon_0 K \Delta\phi}{qN}} \quad (7)$$

where $\Delta\phi$ is the difference in work function (J), q is the charge on an electron (1.6×10^{-19} C), and N is the carrier concentration ($\#/m^3$). If the work function of the metal is less than that of the dielectric, electrons are transferred into the dielectric to equilibrate the

1 Fermi levels and an accumulation layer forms. Note the lack of any extrinsic doping
2 yields a low N for an insulating dielectric such Al_2O_3 , potentially resulting in a
3 significant accumulation or inversion layer depth. Other work within our group¹¹ has
4 shown that for some metals, notably Pt, all thin film capacitor structures were rendered
5 semiconducting and hence inoperable as a capacitor.
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3. RESULTS AND DISCUSSION

A. Experimental measurements

21 Samples were fabricated and measured across 1 kHz to 100 kHz for capacitance and
22 dielectric loss. The capacitors were fabricated on DuPont 951[®] LTCC. As seen in Fig.
23 7, the capacitance at 1kHz increased with increasing area, as expected, although the
24 calculated dielectric constant for these samples increased with decreasing area, from a
25 value of approximately 12 to a value of 23 for the smallest devices. The results at 10
26 kHz and 100 kHz were similar to those shown here at 1 kHz.
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B. Model calculations

40 CST Studio Suite solves for the electric field (E) and the electric flux density (D)
41 through the structure. Various post-processing steps can be performed, which include
42 the generation of cross sections of the 3D fields and evaluation of the field strength
43 along the length of the path or electrode in this case. Fig. 8 shows the electric field
44 enhancement at the edge of the electrode. To obtain the effect of the electric field
45 enhancement at the edge of an electrode, the electric field strength was evaluated on a
46 curve across the width of the capacitor.
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1 Various parameters were evaluated using the fringe field model, including electrode
2 thickness, dielectric thickness, and capacitor length and width for the cases with and
3 without fringing. The model predicts a strong dependence of the dielectric constant
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5 enhancement due to the fringe field on the capacitor size, as seen in Fig. 9. For the
6 amorphous alumina dielectric, the field enhanced dielectric constant increased by 9%,
7 from 9.9 to 10.7, when the capacitor area decreased from 0.5625 to 0.0225 mm² in this
8 material. For comparison, the fringe field contributed to an increase in the effective
9 dielectric constant of amorphous SiO₂ by 16% using the same experimental parameters,
10 and assuming a bulk dielectric constant of 3.3. This general trend agrees with the
11 observed experimental results of Fig. 7.

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13 The dielectric thickness also had an effect on the enhancement in the dielectric
14 constant. Fig. 10 shows that thicker dielectric devices, e.g., devices with lower
15 capacitance, had a stronger enhancement response compared to thinner dielectric
16 devices.

37 *C. Polarizability Contributions*

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39 Crystalline Al₂O₃ with ideal lattice parameters has a predicted dielectric constant of
40 using the Clausius-Mossotti relationship. There would be no dipolar contributions to
41 the dielectric constant with Al₂O₃ due to the nature of the material. Hexagonal
42 crystalline Al₂O₃ has a theoretical density of approximately 3.98 g/cm³. In contrast, the
43 density of amorphous Al₂O₃ thin films has been reported to be 2.9 – 3.3 g/cm³, which
44 suggests a free volume of 19 – 37% greater compared to the crystalline material.^{12, 13}

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46 The Clausius-Mossotti relationship from Eq. 6 was used to calculate the dielectric
47 constant for hexagonal Al₂O₃ assuming the standard (crystalline) and expanded

(amorphous) interatomic spacing parameters in Table I. The associated dielectric constant of amorphous Al_2O_3 based on an increased interatomic spacing comparative to densities as low as $3.3 - 2.9 \text{ g/cm}^3$ ranges from 6.1 to 4.6, respectively. From this analysis, polarizability contributions might not be expected to contribute to the enhanced dielectric constants that are observed; in fact, they might be expected to decrease the effective dielectric constant of the films.

While this approach seems to provide a straightforward method to estimate polarizability contributions, it may be too simplistic: the effects of film density have been accounted for, but the role of film structure (atomic arrangement and structural openness) on the polarizability of the species in the film has been neglected. The work of Shannon and colleagues⁹ seems to suggest the polarizability of the Al and/or O species is increased in the amorphous state. Such an increase could reduce, or potentially even eliminate, the expected reduction in dielectric constant predicted by the Clausius-Mossotti equation and the lower density of the amorphous state.

There is one other factor here that should be considered. It has been demonstrated above in the analysis of fringe field contributions to dielectric constant enhancement that such contributions increase with decreasing dielectric constant (9% enhancement for Al_2O_3 , $K = 9.8$; 16% enhancement for SiO_2 , $K = 3.3$). So, if the predictions of the Clausius-Mossotti equation shown in Table I are correct, i.e., films with lower density have a lower dielectric constant, such films should demonstrate a greater increase in capacitance due to fringe fields.

Because of the concomitant but potentially competing effects of density on polarizability and fringe field contributions, predicting the magnitude of these effects is difficult. Based on the calculations, it may be tempting to state that polarizability

1 contributions decrease, rather than enhance, the observed dielectric constant. However,
2 it is not possible to unequivocally state this, because of the uncertainty of changes in the
3 magnitude of the fringe field contributions and any potential increase in polarizability
4 due to greater structural free volume. Further experimentation to better quantify the
5 polarizability of the Al and O species in these films is required. While density
6 measurements might provide some insight, they are insufficient to clarify the nature of
7 the polarizability.
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10 *D. Accumulation or Inversion Layer Contributions*
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13 Modeling efforts of accumulation or inversion layers are limited since the carrier
14 concentration for Al_2O_3 is unknown. Current-voltage measurements on these devices
15 using an HP4140 picoammeter and an applied potential of 100V yield current
16 measurements below the noise level of 10^{-13} amps. If the intrinsic reaction is assumed
17 to control conduction, the band gap of Al_2O_3 of ≈ 7 eV yields a calculated carrier
18 concentration at room temperature of $\approx 10^{-32}/\text{m}^3$, a negligible number. While certainly
19 these values are desirable for a capacitor dielectric, the question remains if thin
20 dielectrics are adversely affected. For instance, using $N = 10^4/\text{m}$, $K = 10$ and $\Delta\phi = 1$
21 eV, as an example, the penetration depth using Eq. 7 calculates to be 133 nm at each
22 electrode, a number that when compared to the dielectric thickness of 1000 nm would
23 yield an observed capacitance 27% higher than expected. Band bending of this
24 magnitude might, therefore, decrease the effective thickness of the dielectric,
25 contributing to higher calculated dielectric constants. This effect is being further
26 explored by varying the electrode composition and measuring the capacitor performance
27 at elevated temperatures and field levels.
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4 4. CONCLUSIONS
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Thin film Al-Al₂O₃-Al capacitors on LTCC were fabricated and characterized to have higher than predicted capacitance. The measured capacitance was nearly double the predicted value in some cases. Fringe field, Clausius-Mossotti polarizability, and accumulation layer contributions to capacitance were modeled for this system as potential sources of increased capacitance through dielectric enhancement. Fringe field enhancement to dielectric constant was successfully modeled using CST Microwave Studio's electrostatic solver and demonstrated up to a 9% increase in the dielectric constant due to fringe enhancements when a tapered electrode was present. While the fringe field model does not account for the entire dielectric enhancement observed experimentally, it does indicate a trend of increasing dielectric enhancement as the capacitor size decreases, as observed experimentally.

The polarizability modeling that was carried out was inconclusive due to competing effects of density on effective dielectric constant (Clausius-Mossotti calculations), and uncertainty in the polarizability of the species as a function of the free volume of the amorphous structure. Polarizability effects and density can also be expected to affect fringe field contributions, making the analysis of these effects on dielectric constant enhancements even more difficult.

Another likely contribution to the dielectric enhancement of the thin film dielectrics in this study is the reduced effective dielectric thickness arising from accumulation layers in the dielectric adjacent to the electrodes. This contribution has been calculated to increase the capacitance by as much as 27% based on assumptions documented in this paper. Electron beam induced current (EBIC) is being used to characterize the

1 cross section of these samples for evidence of accumulation layers and their respective
2 reduction of the effective dielectric thickness if present.
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7 5. ACKNOWLEDGMENTS
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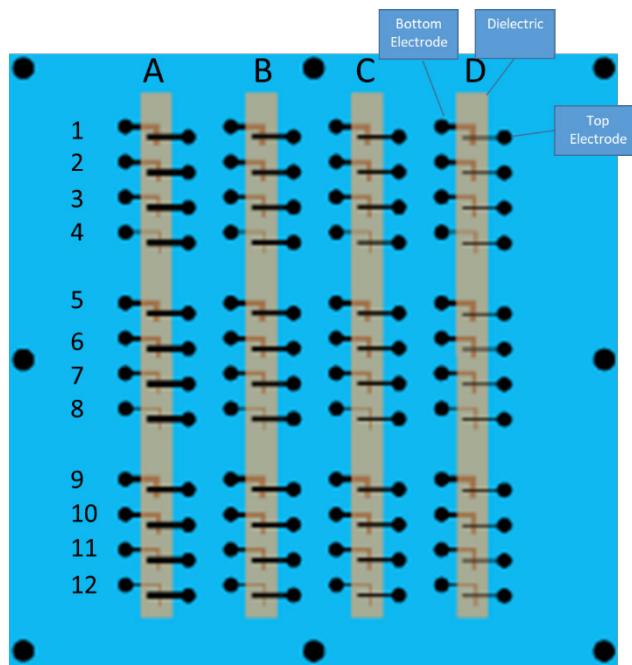
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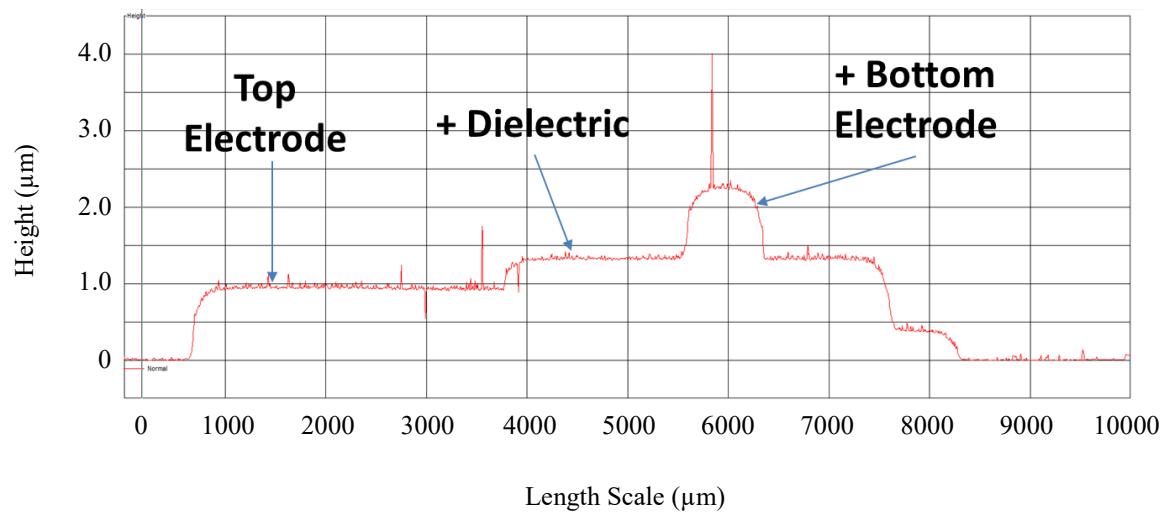
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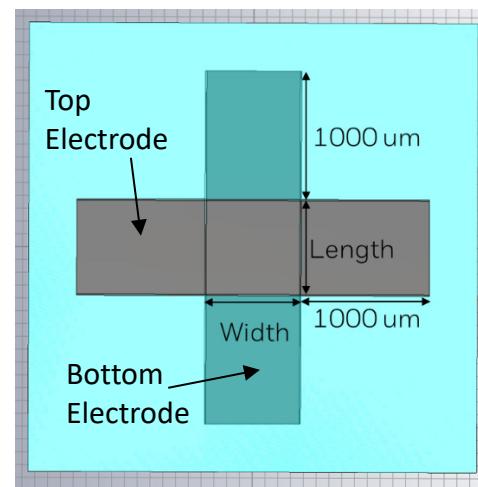
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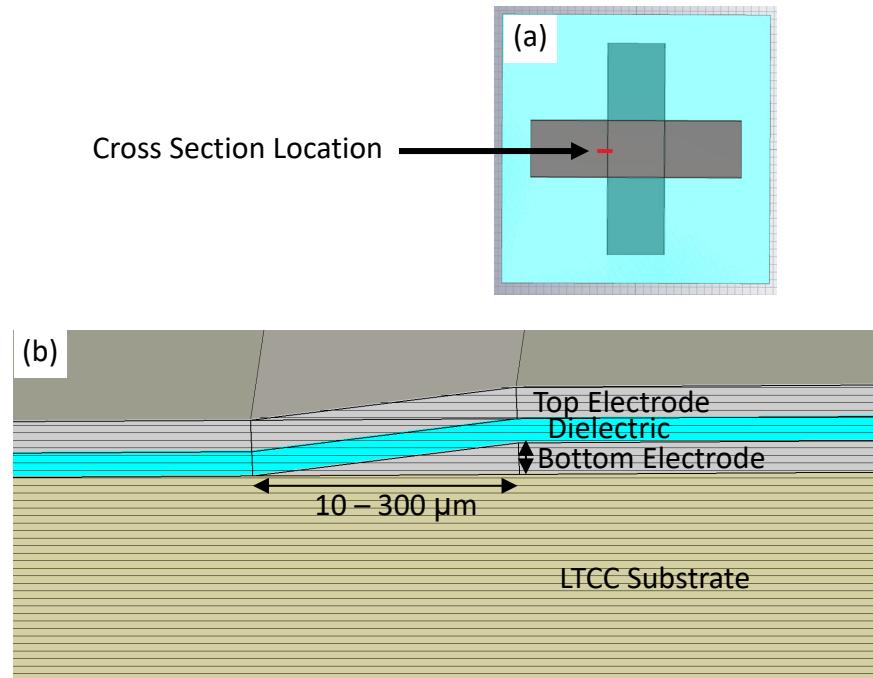
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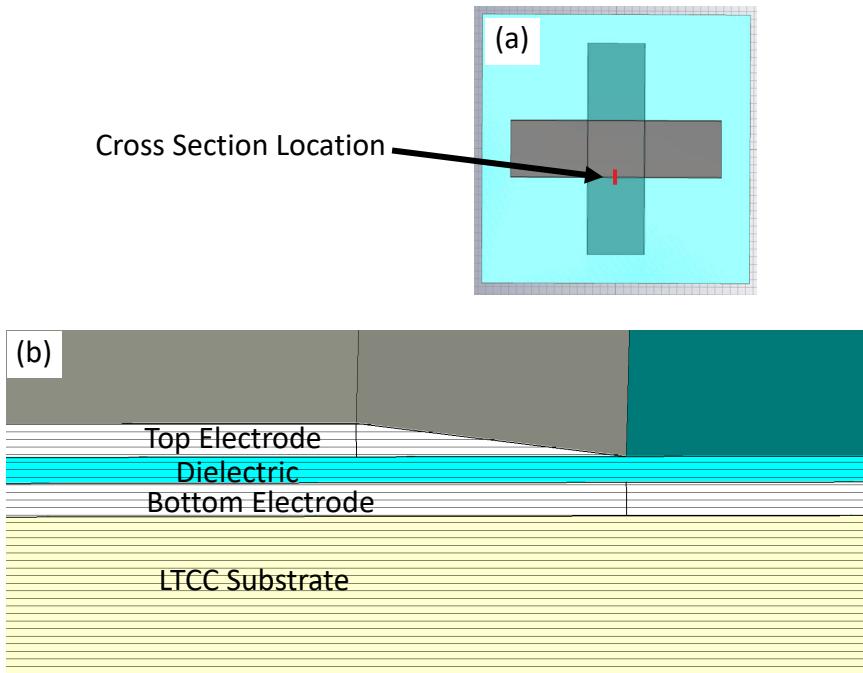
¹³ Etinger-Geller, Y., Katsman, A., Pokroy, B.: Density of Nanometrically Thin Amorphous Films Varies by Thickness, *Chem. Mater.*, **29**, 4912-4919, (2017)











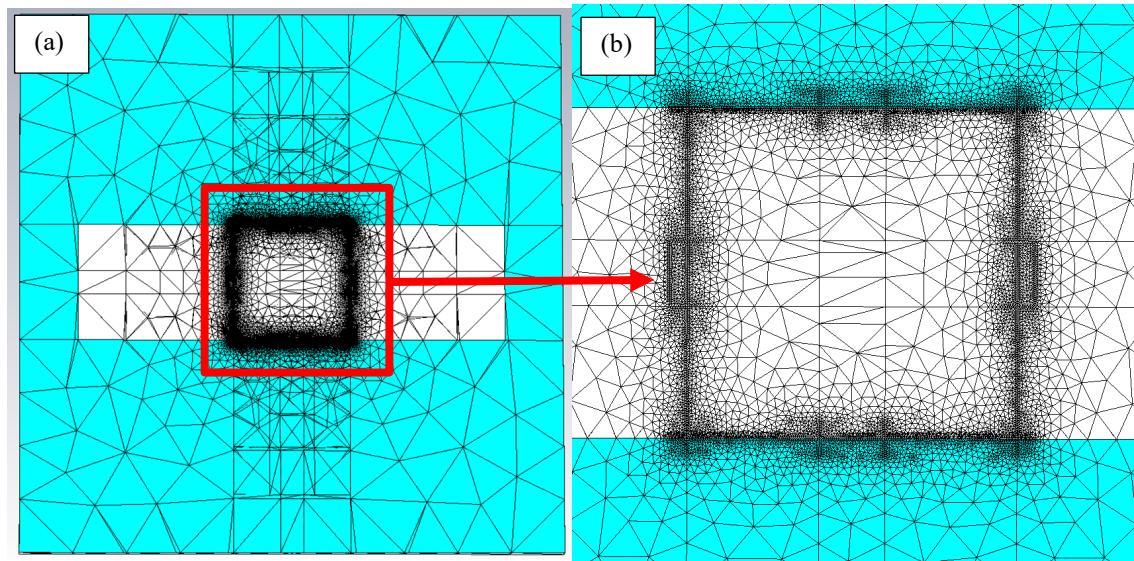


Figure 7

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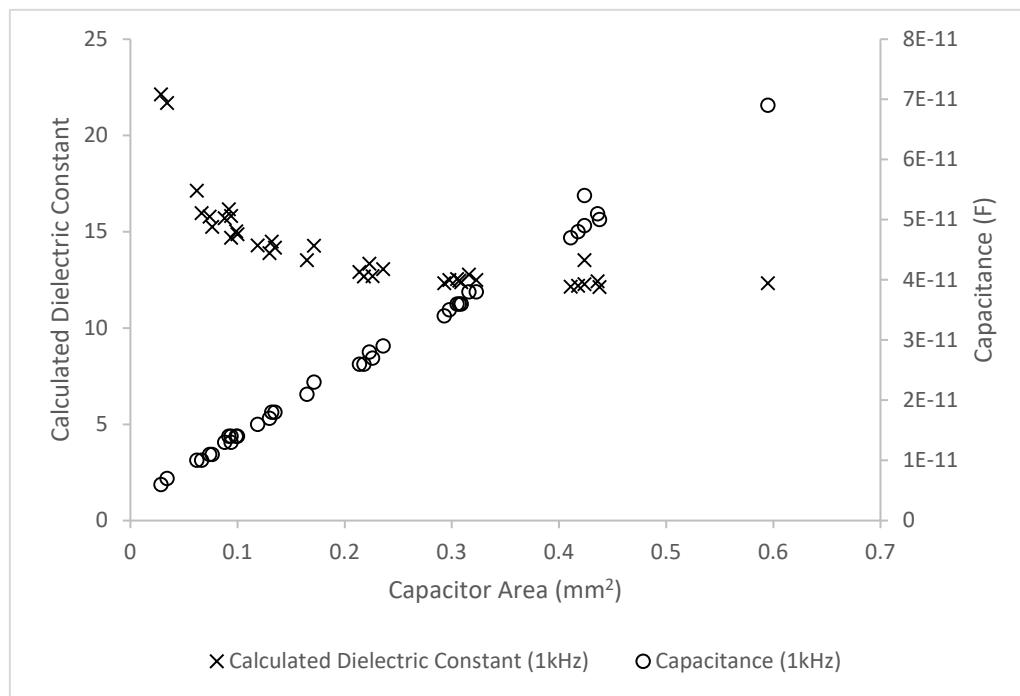


Figure 8

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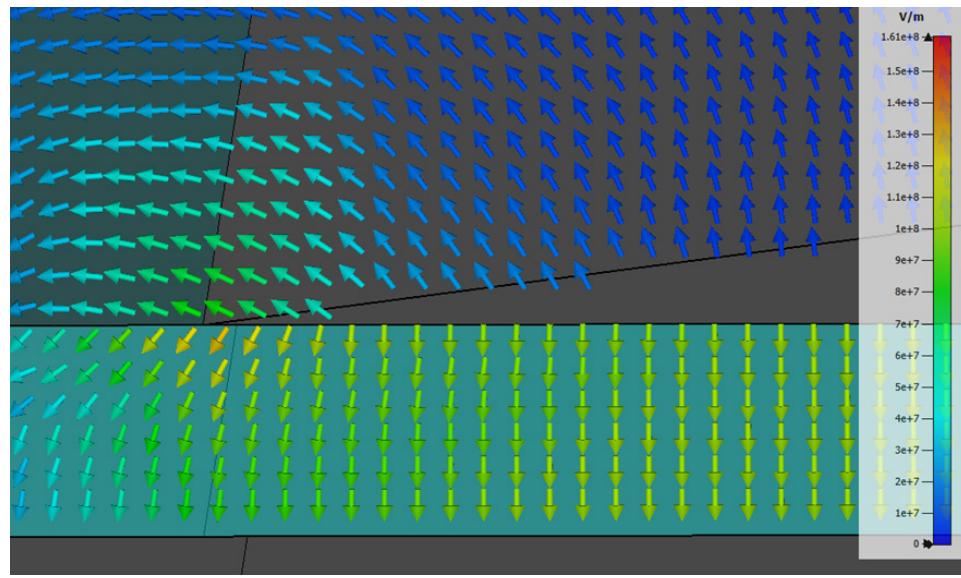


Figure 9

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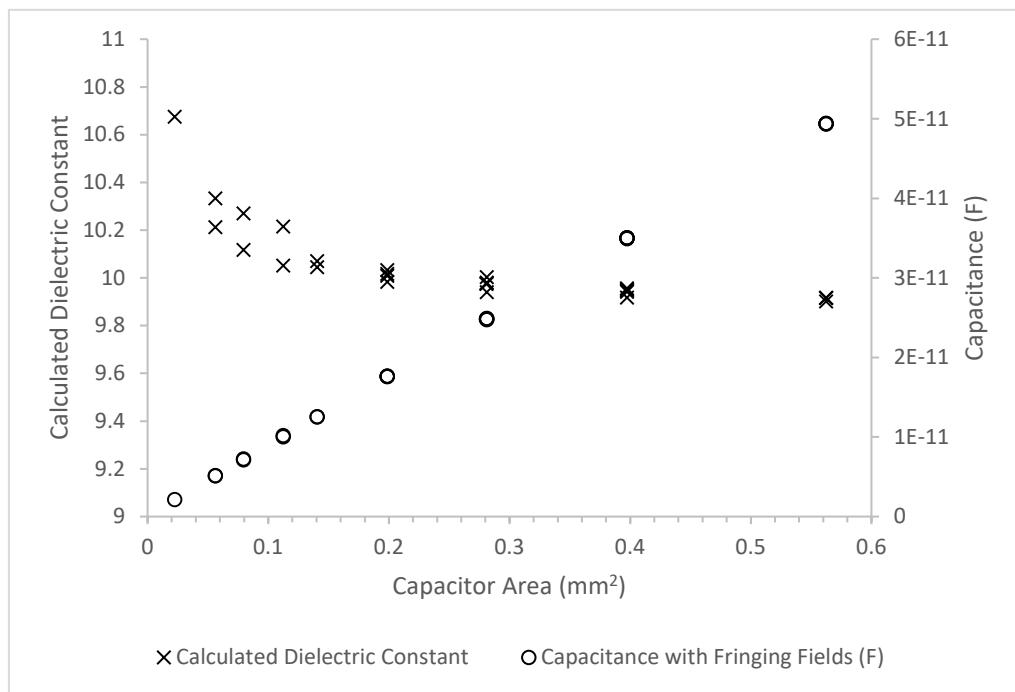
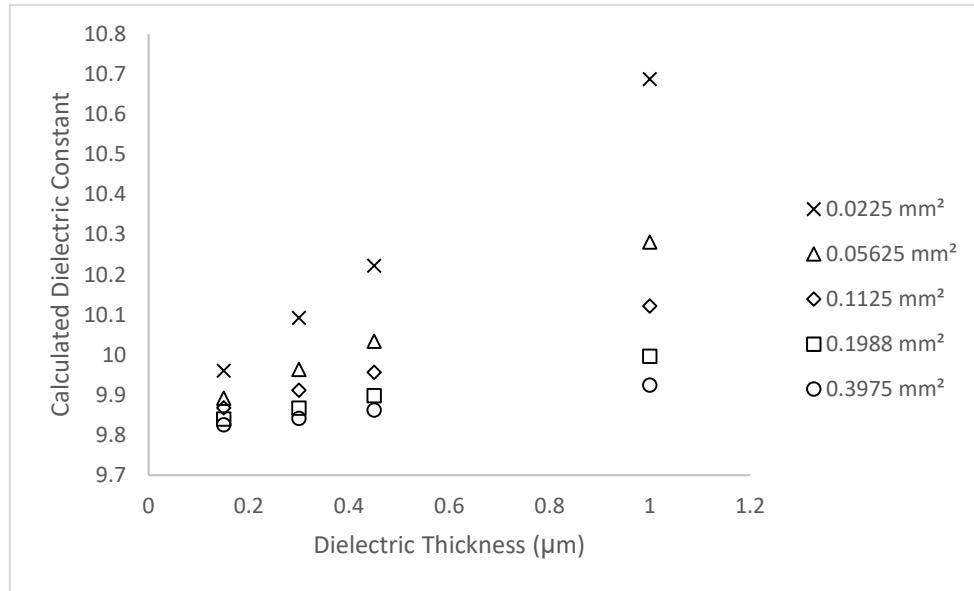


Figure 10

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Interatomic Spacing Increase	a (Å)	c (Å)	Calculated Dielectric Constant	Calculated Volume (Å ³)	Calculated Vol % Increase	Calculated Density (g/cm ³)
0%	4.76	12.99	10.00	255.02	0.0%	3.98
1%	4.81	13.12	9.03	262.74	3.0%	3.87
2%	4.86	13.25	8.23	270.63	6.1%	3.76
3%	4.90	13.38	7.56	278.66	9.3%	3.65
4%	4.95	13.51	7.00	286.86	12.5%	3.54
5%	5.00	13.64	6.52	295.21	15.8%	3.44
6%	5.05	13.77	6.10	303.73	19.1%	3.35
7%	5.09	13.90	5.74	312.41	22.5%	3.25
8%	5.14	14.03	5.41	321.25	26.0%	3.16
9%	5.19	14.16	5.13	330.25	29.5%	3.08
10%	5.24	14.29	4.87	339.43	33.1%	2.99
11%	5.28	14.42	4.64	348.77	36.8%	2.91
12%	5.33	14.55	4.44	358.28	40.5%	2.84

Caption

Table I: Clausius-Mossotti based dielectric constant calculations for hexagonal Al₂O₃ having expanded lattice parameters.

Captions

Figure 1: Schematic layout of thin film capacitors on LTCC substrate.

Figure 2: Typical KLA Tencor P-17 thickness profile.

Figure 3: Model diagram 1 (note: the aqua color is the dielectric layer).

Figure 4: Model Cross Section 1 with (a) top view indicating location of cross section with red line and (b) showing top electrode and dielectric conform to taper created by bottom electrode.

Figure 5: Model Cross Section 2 with (a) top view indicating location of cross section with red line and (b) showing top electrode with taper over planar dielectric and bottom electrode.

Figure 6: (a) Tetrahedral mesh of capacitor simulation model. (b) Magnified model showing mesh detail at electrode edges of active capacitor area.

Figure 7: Dielectric constant and capacitance vs. active dielectric area for Al-Al₂O₃-Al capacitors on LTCC substrate.

Figure 8: 2D cross section of electric field at electrode edge.

Figure 9: Model results for dielectric constant and capacitance vs. active dielectric area for Al-Al₂O₃-Al capacitors.

Figure 10: Model results of the dependence of the effective dielectric constant on the dielectric thickness for Al-Al₂O₃-Al capacitors with various areas (averages of model results displayed).