

Low Temperature Transport in Low Dimensional Systems

Research Interests

Coupled Nanoelectronic Systems

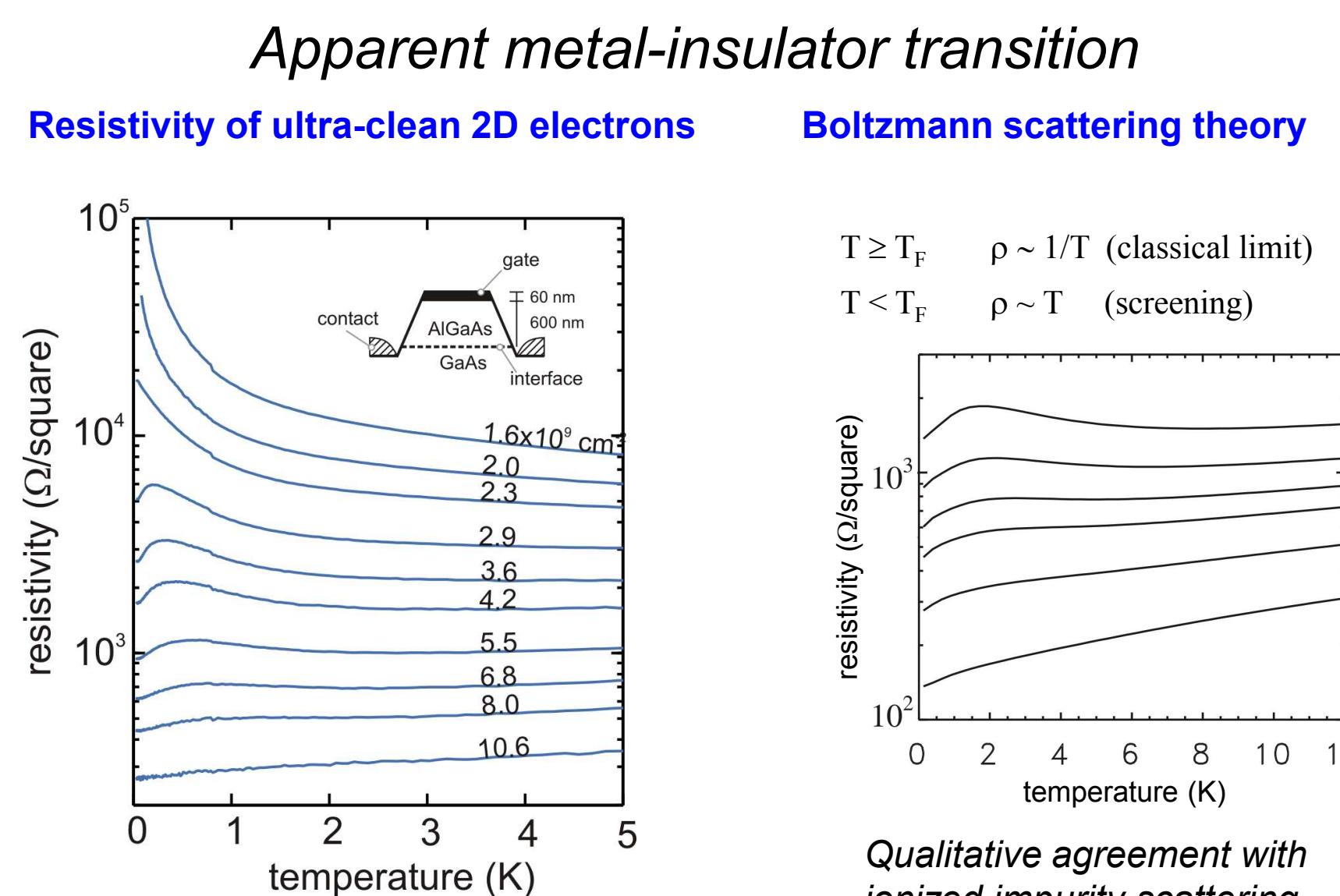
- Vertically coupled 1D wires
Transport studies, time resolved transport
- Coupled quantum dots
Quantum computing

2D electron physics

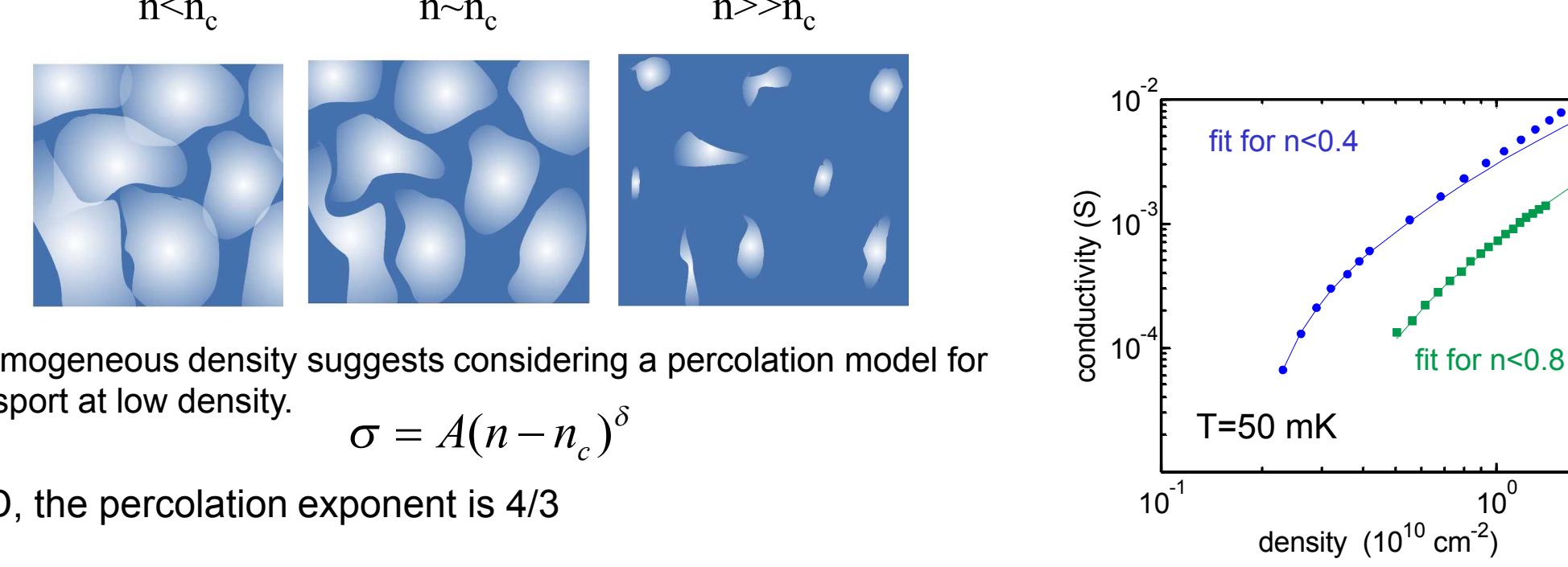
- Bilayers
Electron-hole bilayers for exciton condensation
- Quantum Hall effects
- High mobility 2D electrons

Jumpstart User Project

2D metal-insulator transition



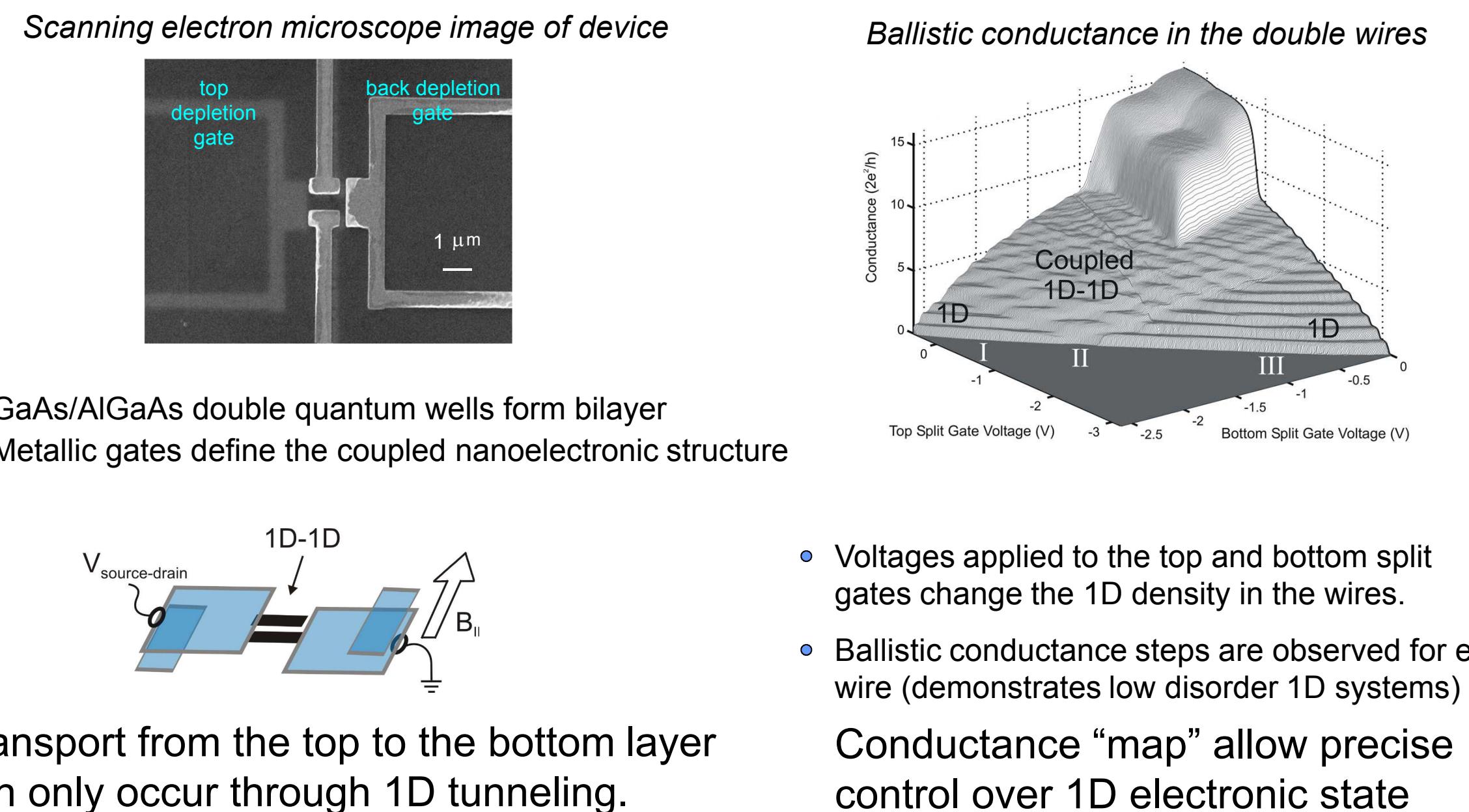
Percolation at low density



The agreement between experiment and theory for both the non-monotonic resistivity and the percolation scaling in the insulating regime suggest that the standard picture of Fermi liquid physics can explain the 2D metal-insulator transition in high mobility 2D electrons.

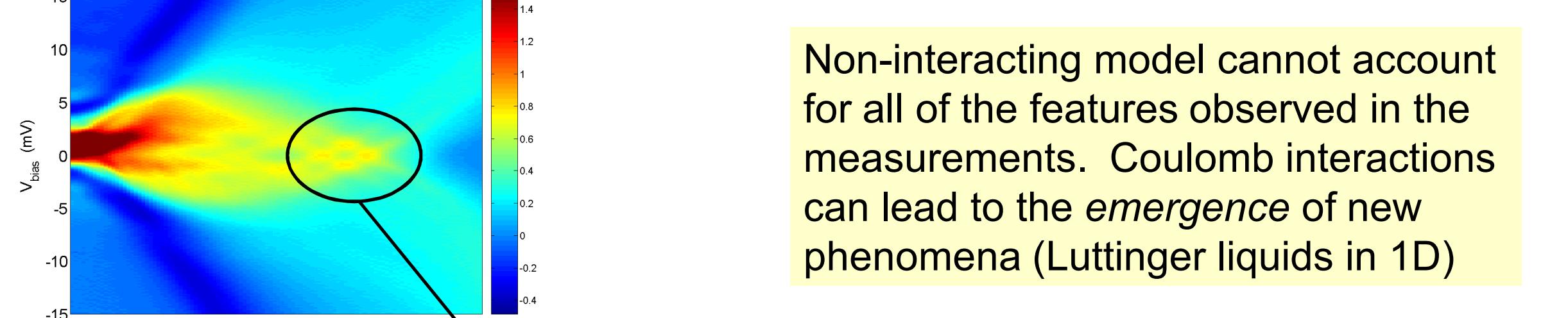
Coupled Nanoelectronics

Vertically coupled double quantum wires



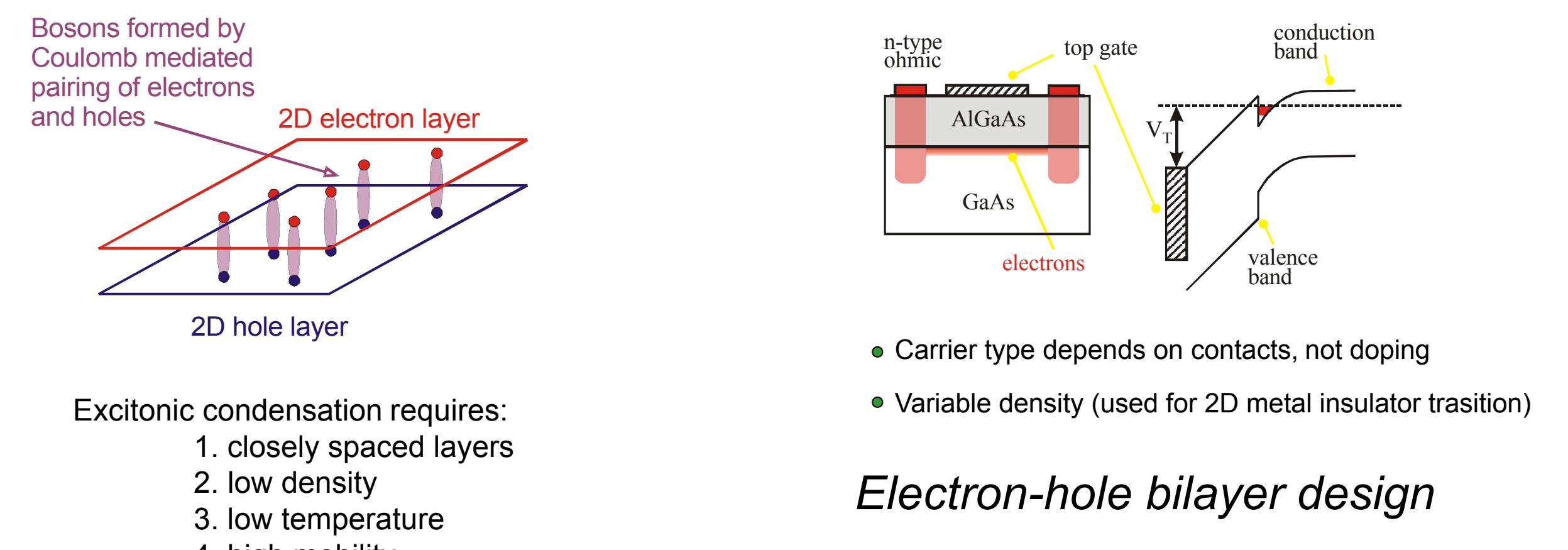
1D Tunneling

Tunneling spectroscopy in 1D-1D system



Electron-hole bilayer fabrication for exciton condensation

Exciton condensation



The prediction of exciton condensation 60s dates to the 70s. Experimental implementation of electron hole bilayers, especially for transport, is quite difficult

Technique is ideal for electron-hole bilayers: consistent with high mobility, low density, and bipolar operation.

Next - Coulomb drag to detect condensation

Associated Capabilities

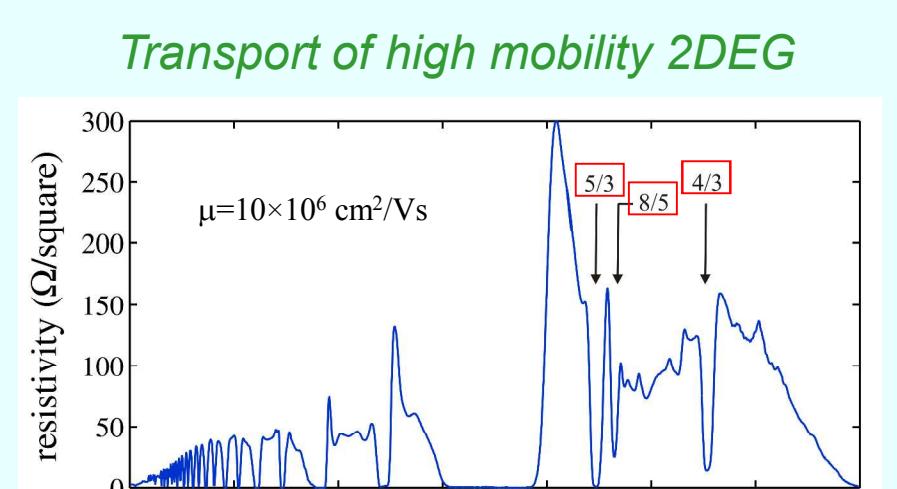
Low temperature transport

- Oxford MX400 Dilution refrigerator (available Winter 2006)
*Base temperature < 10 mK
Multiple experimental inserts - dc transport, high frequency, rotation*
- 3He system for rapid characterization (available Fall 2006)
Temperature range 0.3 to 300 K
- 13 T superconducting solenoid magnet
*3" bore, provides extra High homogeneity (1 part in 10⁵)
Field cancelled region at mixing chamber*

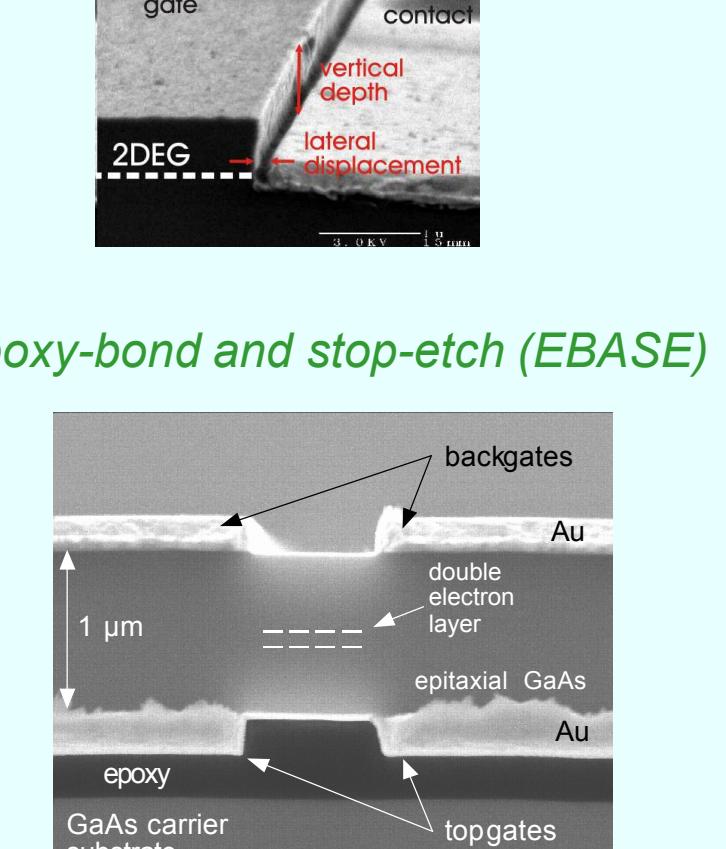


III-V molecular beam epitaxy

- Ultra-clean GaAs/AlGaAs heterostructures
10 million mobility 2D systems bilayers and complex structures
- MBE system for experimental materials



FET design for 2D systems



Sample processing

- Techniques for new devices
*FET design 2D systems
Variable density bilayers
Thinning techniques (EBASE)*
- Electron beam lithography

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