Title: 'First of a Kind' Experimental Cluster based on Intel® Many Integrated Core Architecture for highly-parallel HPC workloads

Abstract (1000 characters): Learn more about the Appro and Sandia National Laboratories collaboration to develop a "first of a kind" experimental cluster using the Intel® Many Integrated Core Architecture (Intel® MIC) and next-generation Appro Xtreme-X™ Supercomputer architecture. Explore this new system based on the Intel's Knights Ferry (KNF) software development platform and learn how it will be an important platform for future Intel® MIC architecture-based scalable system designs. Discover how this project will provide an experimental foundation for the development of applications for this new class of platforms. Learn how this collaboration will help develop applications for current and future co-processor computational systems, and develop new system software capabilities that can support the efficient movement of data between cores in the Intel® MIC and Intel® Xeon® heterogeneous compute node. Examples of the system configuration and implementation will be discussed.