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Transitioning the Scientific Software Toolchain to Clang/LLVM

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Abstract—For the past 25 years, many of the largest scientific software applications at Lawrence Livermore National Laboratory (LLNL) have used the Intel C/C++ compiler (icc/icpc) to compile the executables provided to users on x86. This spring 2020, the Monte Carlo Transport Project will release our first executable compiled with clang, which builds 25% faster and runs 6.1% faster than icpc. The poster accompanying this paper will describe the challenges of switching toolchains and the resulting advantages of using a clang/LLVM toolchain for large scientific software applications at LLNL.

I. BACKGROUND

In 1995 at Lawrence Livermore National Laboratory (LLNL), we observed that the Intel C compiler (icc) generated the best code on x86 and so we used it to compile the executables that we provided to users running on x86. Today our applications look very different than they did in 1995. At least 12 of our 15 largest applications [1] are written in a mix of C, C++98, and C++11, so we use the Intel C++ compiler (icpc) instead of icc. Together these 12 applications consist of about 5 million lines of code. This poster will focus on Mercury, a 3D massively parallel Monte Carlo particle transport application containing approximately 300,000 lines of code [2].

II. MOTIVATION

Our motivation to investigate clang was based off an analysis of the relative advantages of icpc (TABLE I) and clang (TABLE II) for our applications. *We are not dropping support for icpc.* We will continue to build with icpc for testing and we will support users who want to build Mercury with icpc.

TABLE I
ADVANTAGES OF STAYING WITH ICPC

- no work required to continue using the same compiler
- compatibility with Intel developer tools
- compatibility with Intel libraries

III. PERFORMANCE

We provide users with two Mercury executables on x86, a MPI-only executable and a MPI+OpenMP executable. The

TABLE II
ADVANTAGES OF SWITCHING TO CLANG

- generates faster code
- faster compilation
- better C++ standard support
- can target non-x86 platforms like ARM, Power, and GPUs
- better documentation [3] [4]
- compatibility with open source developer tools
- compatibility with open source libraries
- open source
 - no vendor lock-in
 - we can fix bugs ourselves
 - we can understand what the compiler is doing
 - external collaborators can build our code and reproduce our results
 - no denial-of-service due to license server outage
 - no license fee
- big community
 - active mailing lists (llvm-dev, cfe-dev, etc.)
 - annual developer meetings in San Jose and Europe
 - more users often means fewer bugs and more features
- innovative toolchain technologies like ThinLTO [5] and sanitizers [6]
- better support for popular gcc language extensions [7]
- our vendor partners’ compilers are increasingly clang-based (e.g. Cray)

performance analysis in this paper is limited to the former. For a discussion of the latter, please see Section IV.

We used LLNL’s RZGenie cluster [8] which contains 48 dual-socket nodes with Intel Xeon E5-2695 v4 “Broadwell” CPUs [9]. The nodes are connected by an Intel Omni-Path interconnect and they run RHEL-based TOSS3 Linux.

TABLE III shows the times to compile the Mercury executable. TABLE IV shows the compile and link flags. A serial compile is 15% faster with clang, but clang takes almost twice as long to link. The **real build**, a hybrid distributed- and shared-memory parallel build that our developers use to build an executable (i.e. compile and link using 4 nodes), is 25% faster with clang. We are using ThinLTO [5] to do whole-program optimization at link time with lld [10]. Without ThinLTO, clang links in 4.3s, a 35% speedup over icpc.

TABLE III
COMPILE TIME COMPARISON

	icpc 19.0.4	clang 9.0.0	icpc/clang
serial compile	17m37s	15m16s	1.15
link time	5.8s	11.3s	0.51
real build	1m21s	1m5s	1.25

TABLE IV
COMPILE AND LINK FLAGS

	icpc 19.0.4	clang 9.0.0
CXXFLAGS	-g -O2 -std=c++11 -no-ftz -fp-model precise -fp-model source -nolib-inline -prec-div -prec-sqrt -unroll-aggressive -funroll-loops -diag-disable cpu-dispatch -finline-functions -gxx-name=/path/to/gcc-7.1.0 -wd10397 -wd2650 -qoverride-limits -fno-builtin-malloc -fno-builtin-calloc -fno-builtin-realloc -fno-builtin-free -ip -no-ipo	-g -O2 -std=c++11 -ffp-contract=off -flto=thin -fstandalone-debug
LDFLAGS	-	-flto=thin -fuse-lld=lld

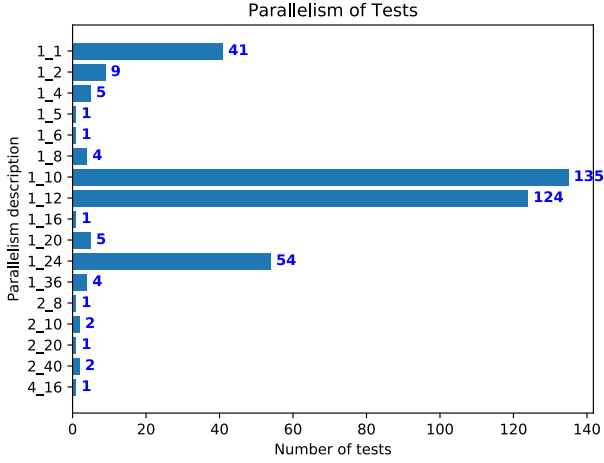


Fig. 1. This plot shows the parallelism distribution of the 391 tests. 41 of the tests were serial, meaning they ran on 1 node using 1 task (1_1) while the remaining 350 tests were parallel. The vertical axis label **X_Y** means a total of Y tasks ran across X nodes.

For the runtime comparison we ran a suite of 391 tests. The runtime of a test is the time spent tracking particles (for a brief description of the Monte Carlo particle transport algorithm in Mercury see Appendix A). Fig. 1 shows the parallelism distribution of the 391 tests.

Fig. 2 shows the runtime distribution of the 391 tests. The 391 tests varied from a minimum of 3 milliseconds to a maximum of 16 minutes.

We ran the suite five times using the same executable in order to quantify the run-to-run variability. The sum of the runtimes for the 391 tests for the fastest run of the 5 runs with the icpc-compiled executable was 14125 seconds or 3 hours, 55 minutes, and 25 seconds. This is the runtime to which all other runs of the test suite are compared.

Fig. 3 compares the runtime of our test suite for icpc and clang, as well as clang with and without ThinLTO. We found that without ThinLTO, clang was 1% slower than icpc, but with ThinLTO clang was 6.1% faster than icpc. We did not see a significant difference when we used ThinLTO for Mercury versus ThinLTO for everything (i.e. Mercury and all libraries).

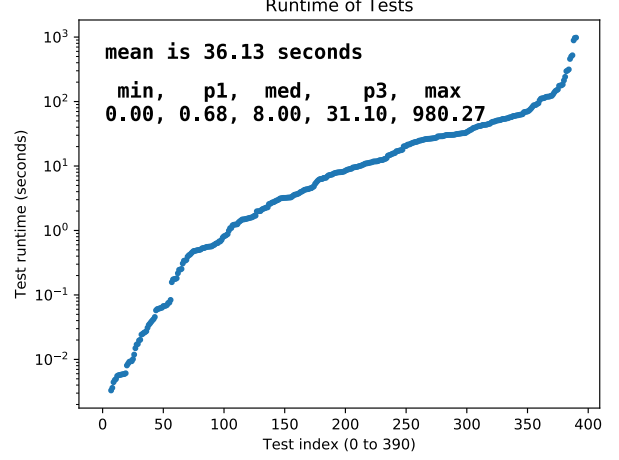


Fig. 2. This plot shows the runtime distribution of the 391 tests. The shortest tests ran for 3 milliseconds and the longest test ran for 16 minutes 20 seconds. **p1** is the 25th percentile and **p3** is the 75th percentile. The total was 14125 seconds or 3 hours, 55 minutes, and 25 seconds.

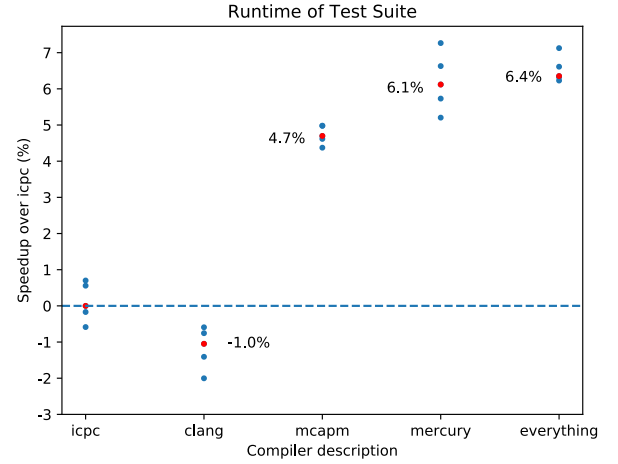


Fig. 3. This plot shows the percent speedup of 5 test suite runs for 5 executables compared to the fastest run with icpc (through which the horizontal line is drawn). The median of the 5 runs is labeled and highlighted in red. **icpc** is the executable compiled with icpc 19.0.4. **clang** is the executable compiled with clang 9.0.0. **mcapm** is the clang-compiled executable with ThinLTO used for Mercury and MCAPM [11]. **mercury** is the clang-compiled executable with ThinLTO used for Mercury. **everything** is the clang-compiled executable with ThinLTO used for everything.

However, we did see a slowdown when we used it for Mercury and MCAPM [11], which we are still trying to understand. For a list of libraries used in Mercury see TABLE V.

IV. DISCUSSION

The 25% build time speedup and 6.1% runtime speedup are great, and we may be able to improve on 6.1% if we can understand and tweak the ThinLTO settings. For example, `llvm/lib/Transforms/IPO/FunctionImport.cpp` has a value `import-instr-limit` with default value 100 and description “Only import functions with less than N instructions.” If we increase this parameter will we get more

TABLE V
LIBRARIES LINKED INTO MERCURY

LLNL Libraries:

Name	Version
timers	2.1.27
ft_hash	2.1.27
overlink	4.7.1
mcapm	2.7.12
mpism	1.1.19
tdf	2.3.58
silo	4.10.3
sha	1.0
memusage	2.1.27
physicstools	0.0.190904
gidplus	3.17.115
nuclear	r184
rng	3.0
yogrt	-

Third-Party Libraries:

Name	Version
mvapich2	2.3.1
python	2.7.14
tcmalloc	2.6.1
hdf5	1.8.10p1
zlib	1.2.9
szip	2.1

cross-translation-unit inlining and an even better runtime? We also still need to try PGO.

We are running all our tests with Address Sanitizer, but with Leak Sanitizer turned off. We want to use Thread Sanitizer and Archer [12] to fix problems with our MPI+OpenMP executable which appear when compiling with clang at `-O2` but not `icpc`.

Finally, we want to use clang on Sierra, a hybrid POWER9 CPU and NVIDIA Volta V100 GPU machine sited at LLNL [13] and currently the number two supercomputer on the TOP500 [14]. We already use clang to target POWER9, but we use `nvcc` to compile our CUDA code for the V100. We would prefer to use clang for all the reasons described in [15].

V. ACKNOWLEDGEMENTS

The title for this document was inspired by a presentation at the 2019 LLVM Developers' Meeting, "Transitioning the Networking Software Toolchain to Clang/LLVM" [16].

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APPENDIX A MONTE CARLO PARTICLE TRANSPORT

Monte Carlo particle transport has a single high-level **while** loop which performs most of the useful work (see Algorithm 1). Calculations spend 90% of the runtime in this loop. Calculations spend 50% of the runtime in the "cycleTracking" phase of the loop which contains about 100,000 lines of reachable code (see Algorithm 2). For this analysis, we compare time in "cycleTracking" instead of total runtime. That means we ignore input parsing, allocation, I/O, and most other syscalls.

Algorithm 1 Monte Carlo particle transport

```

Read in nuclear data
while cycles remain do
  cycleInit
  cycleTracking
  cycleFinalize
end while

```

Algorithm 2 Particle tracking ("cycleTracking")

```

Compute distance to cell boundary,  $d_{cell\_boundary}$ 
Sample distance to collision,  $d_{collision}$ 
Compute distance to census,  $d_{census}$ 
Compute min ( $d_{cell\_boundary}$ ,  $d_{collision}$ ,  $d_{census}$ )
case cell_boundary: move particle across cell boundary
case collision: move particle to collision site, sample rxn
case census: move particle to census site, save particle

```

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