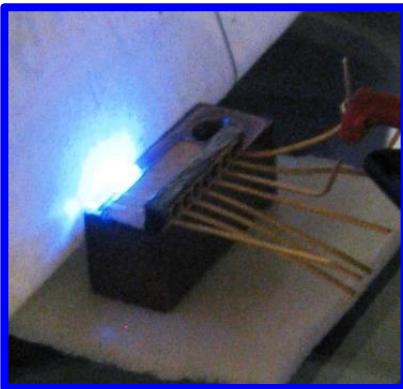


352-nm laser diodes enabled by low-dislocation-density AlGaN templates



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Sandia National Labs, Albuquerque, NM

Acknowledgements: A. Armstrong, W. Chow, B. Clarke
Sandia National Laboratories

Funding: Sandia Laboratory Directed Research and Development



Veeco D-125 MOCVD system

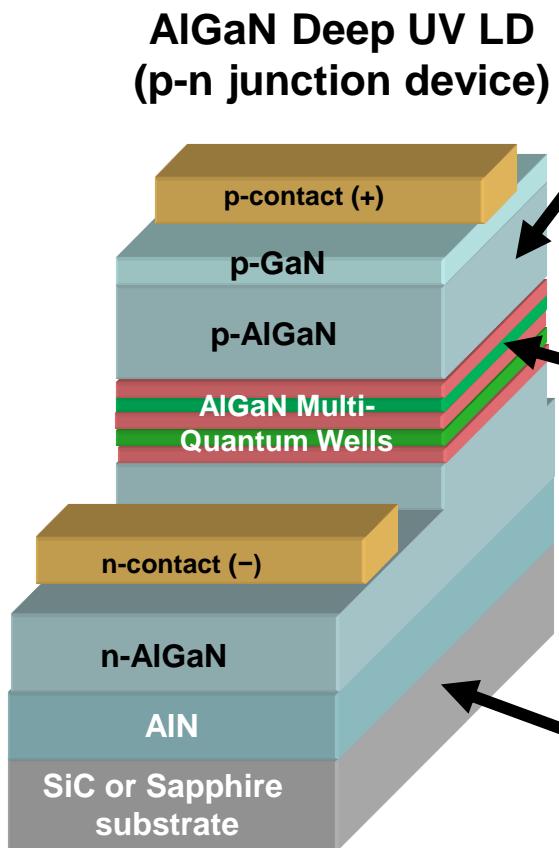
Sandia National Laboratories is a multi-program laboratory managed and operated by Sandia Corporation, a wholly owned subsidiary of Lockheed Martin Corporation, for the U.S. Department of Energy's National Nuclear Security Administration under contract DE-AC04-94AL85000.



Outline

- **Introduction:**
 - Materials Challenges for UV LDs
 - Previous work on AlGaN LDs
- **Dislocation reduction of $\text{Al}_{0.32}\text{Ga}_{0.68}\text{N}$ grown over etched trenches**
 - Processing and growth
 - Improvements in PL and EL
- **UV Lasing**
 - Optical pumping
 - Electrical injection
- **Summary**

Materials challenges for high performance AlGaN deep UV LDs



p-type AlGaN is very difficult

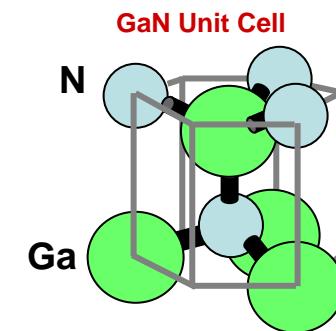
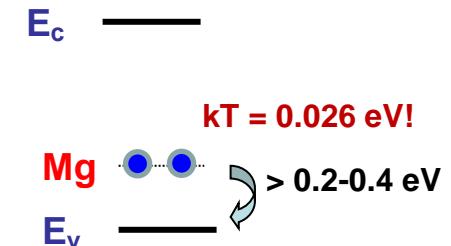
- Large acceptor ionization energies
- Compensating defects

AlGaN Quantum Wells may have low optical efficiency

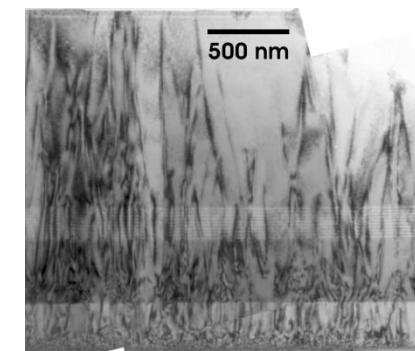
- Non-radiative crystalline defects (e.g., impurities, vacancies)

Lack of AlGaN Substrates

- high densities of extended defects (threading dislocations) $> 10^9 \text{ cm}^{-2}$
- Reduced device efficiency and operational lifetime

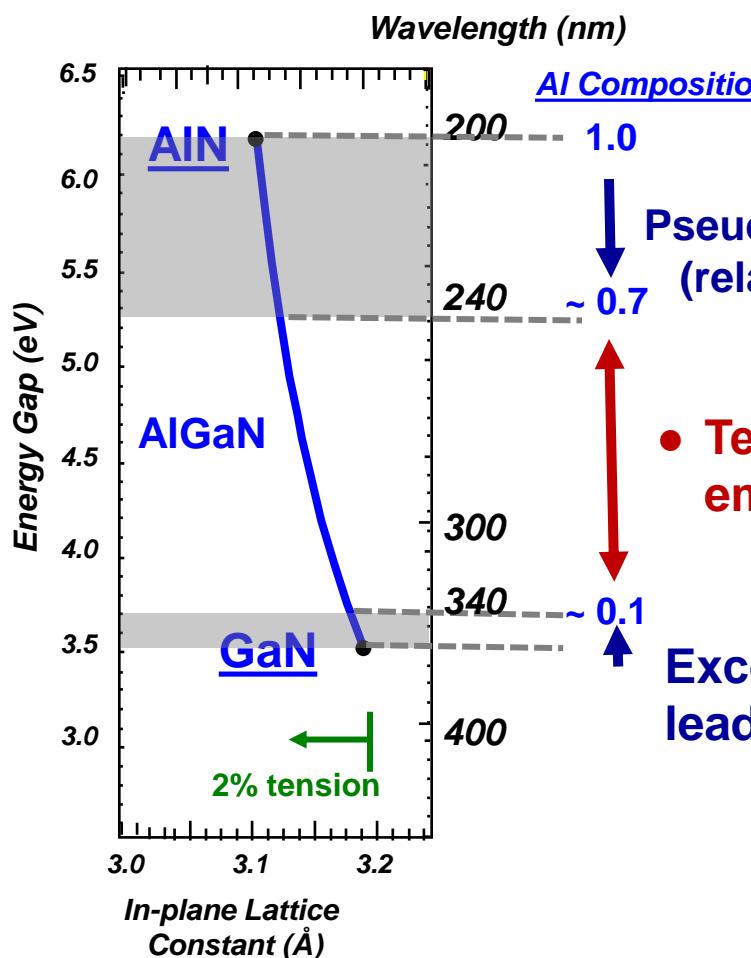


TEM image of AlGaN on sapphire



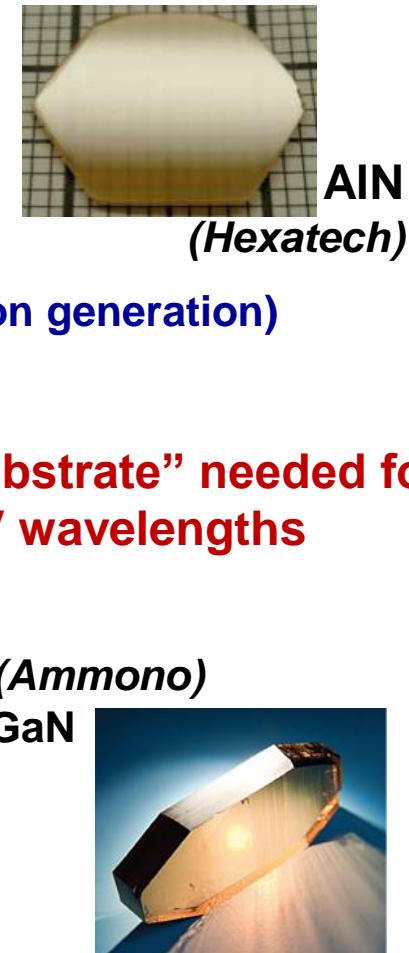
Sapphire substrate

Options for Low Defect Substrates



- **Ternary (AlGaN) “substrate” needed for emitters at many UV wavelengths**

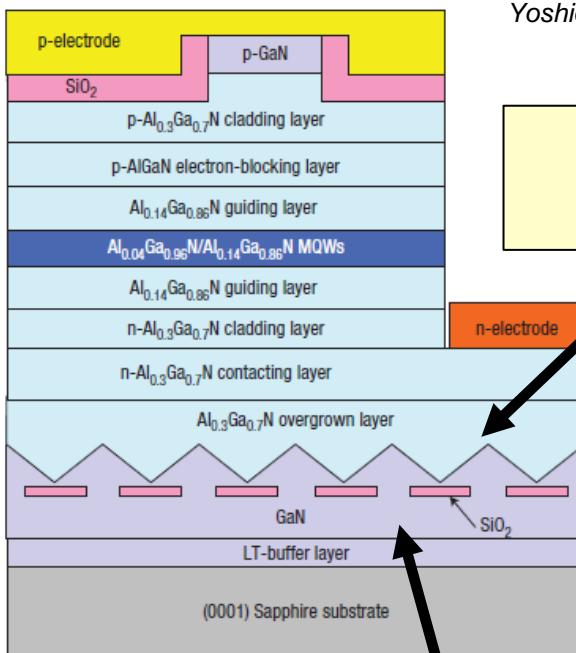
Excess tensile strain leads to cracking



→ How to fabricate a low dislocation template for mid-alloy AlGaN UV-emitters?

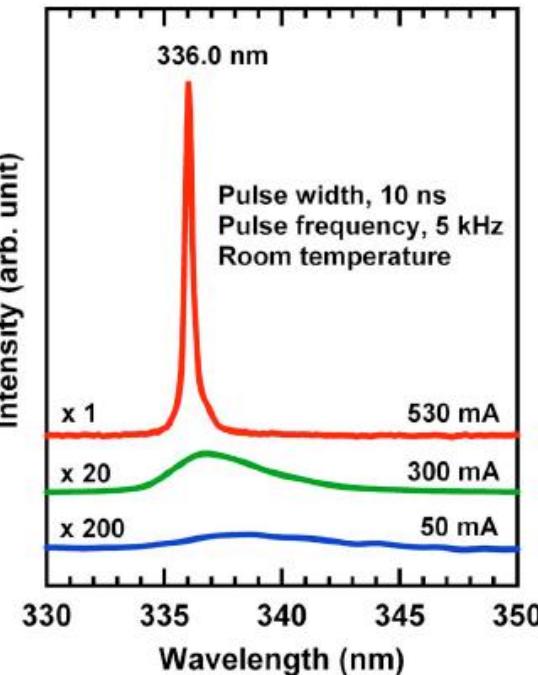
Previous Work: Laser Diodes Employing Patterned Overgrowth

Laser Heterostructure



- Dislocations uniformly reduced across wafer

Pulsed Laser Performance



- **ELOG-GaN with $\text{Al}_{0.3}\text{Ga}_{0.7}\text{N}$ claddings:**
 - 336 nm, 17.6 kA/cm^2
 - 342 nm, 8.7 $\text{k A}/\text{cm}^2$
 - Etched facets, 10 ns,

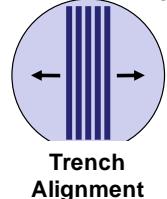
Yoshida (*APL*, 2008)

Dislocation reduction with AlGaN overgrowth of etched trenches

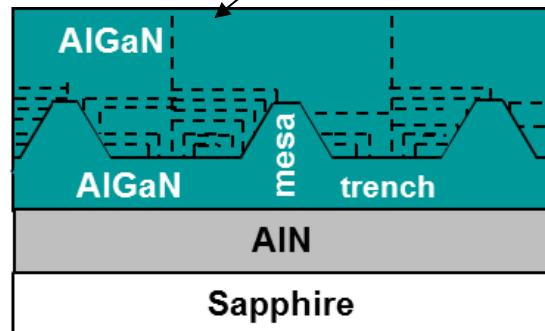
AlGaN with reduced dislocations

AlGaN Overgrowth

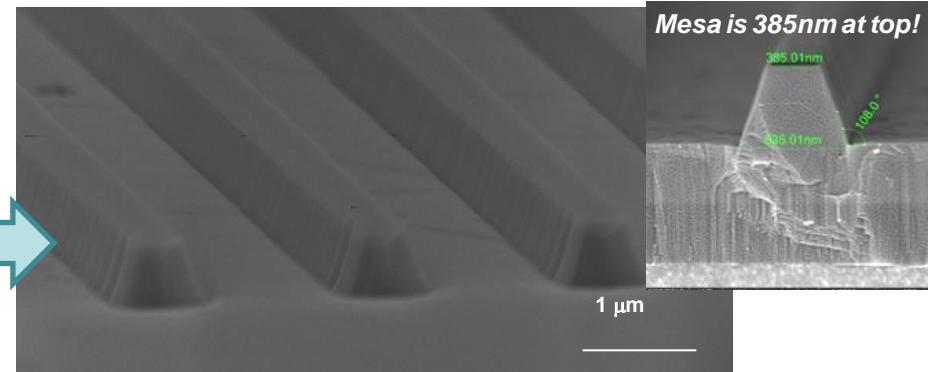
$<1-100>_{\text{AlGaN}}$



dislocations



Patterned template formed by plasma etching

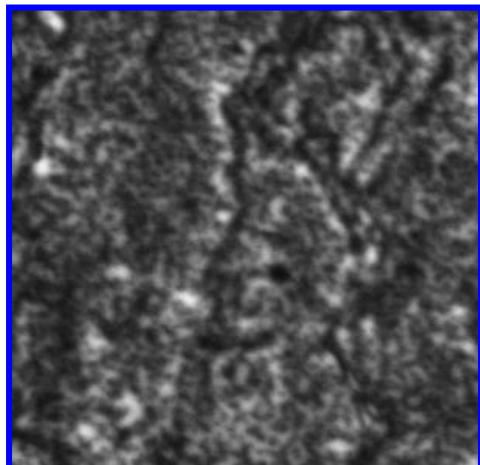


Mesa is 385nm at top!

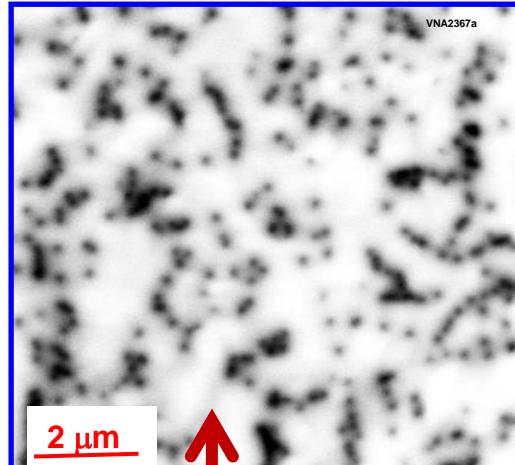
1 μm

CL of 340 nm AlGaN QWs (Al=0.30)

Planar Growth



Patterned Overgrowth

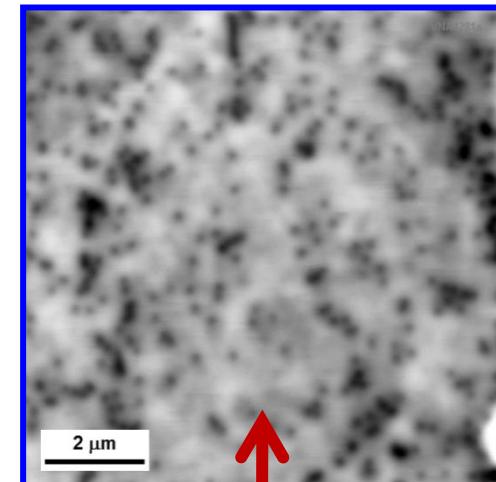


2 μm

10-30X TD reduction to $2-4 \text{ e}8 \text{ cm}^{-2}$

CL of 280 nm AlGaN QWs (Al=0.70)

Patterned Overgrowth



$3-4 \text{ e}8 \text{ cm}^{-2}$



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Two-beam BF-STEM of $\text{Al}_{0.32}\text{Ga}_{0.68}\text{N}$ Overgrowth of patterned $\text{Al}_{0.32}\text{Ga}_{0.68}\text{N}$

B. Clarke

Mask: 1 / 1 (μm)

Etch Depth: 0.66 μm

Overgrowth: 7 μm

→ *Introducing surface roughness
drives dislocation reduction*

• Overgrowth of etched trenches

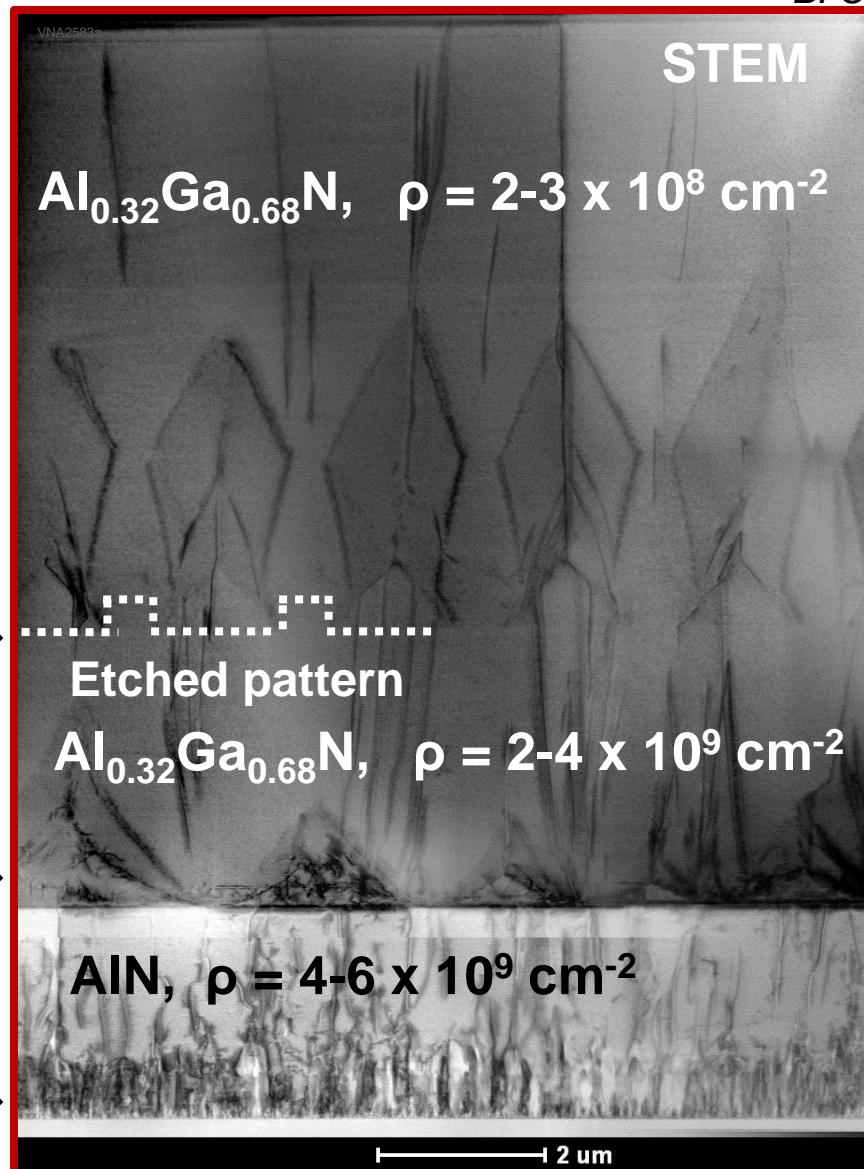


Etched pattern

• Strain induced 3D islanding

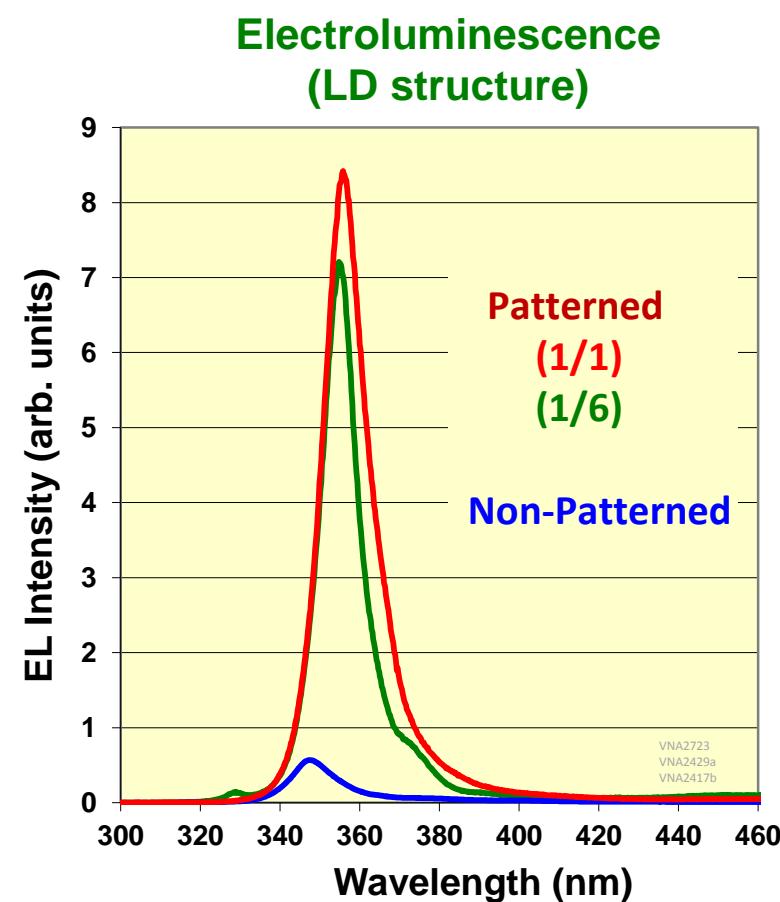
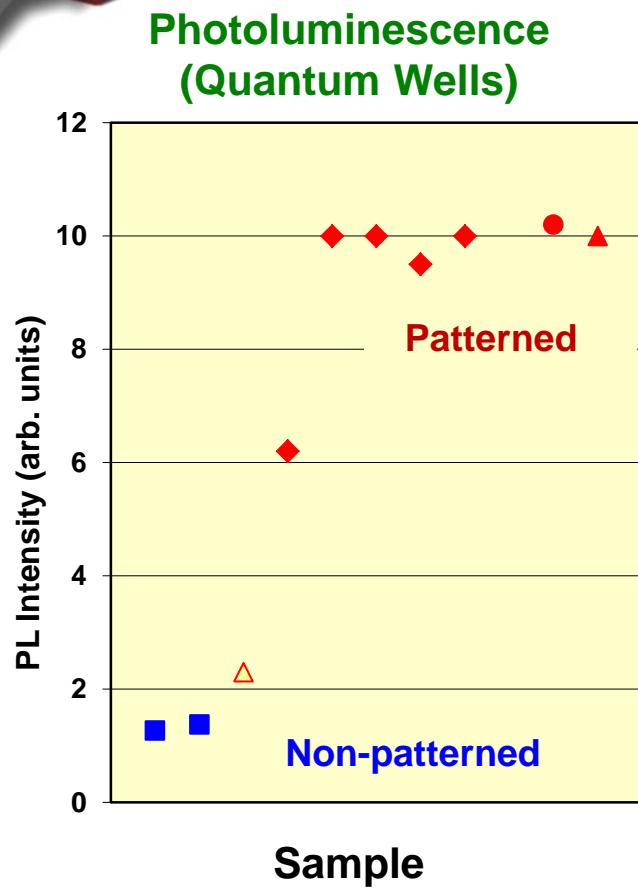


• Roughened, transitional layer



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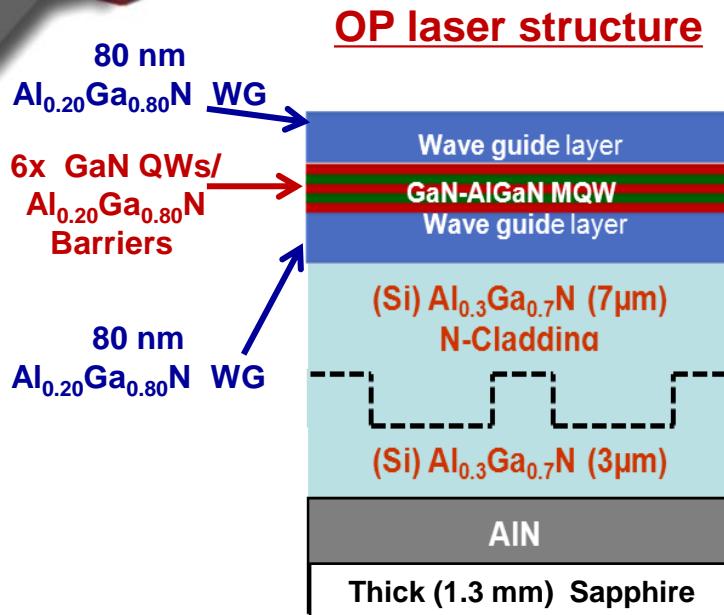
Photoluminescence and electroluminescence of GaN-AlGaN QWs on patterned and non-patterned templates



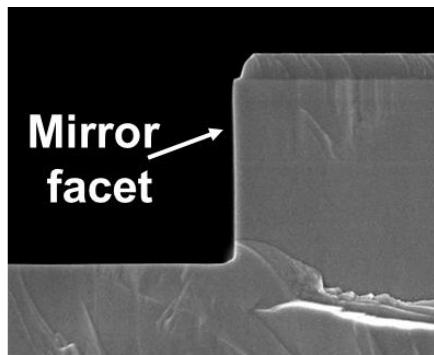
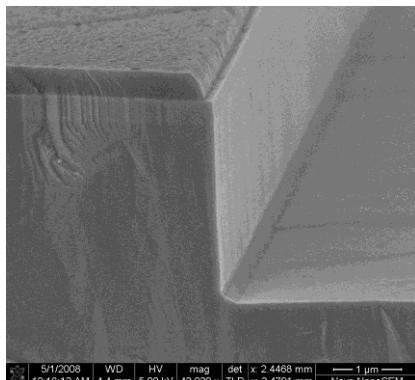
With AlGaN overgrowth of patterned templates:

- ➔ ~7-8x increase in PL
- ➔ ~15x increase in EL

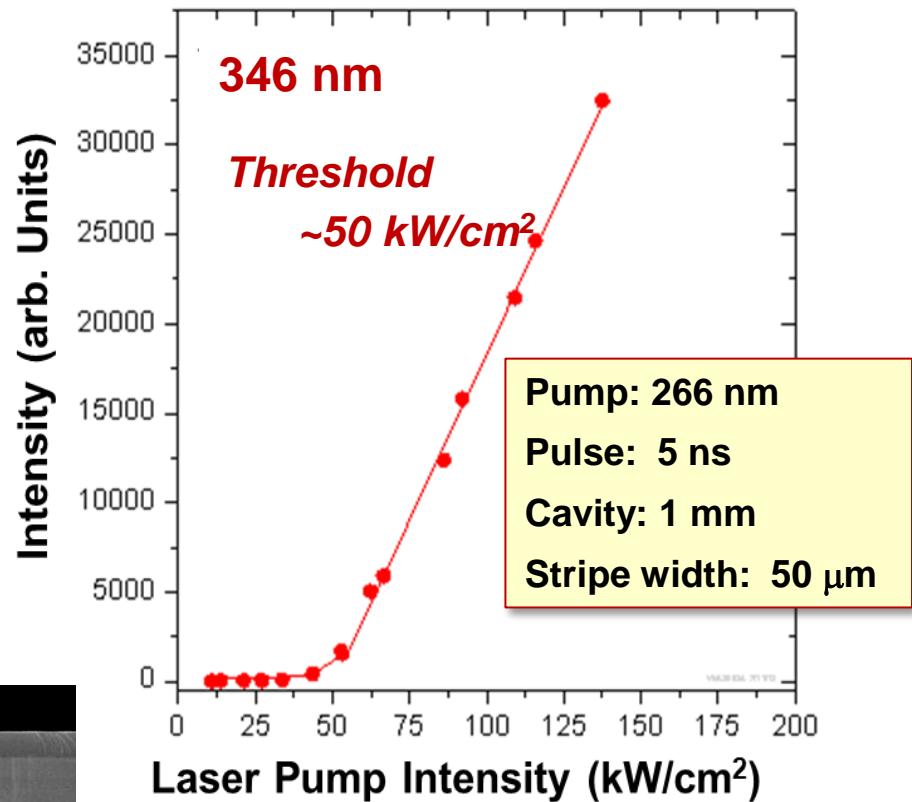
Optically pumped lasing at 346nm



Etched Facets



→Cl₂-based plasma etch and crystallographic wet etch*



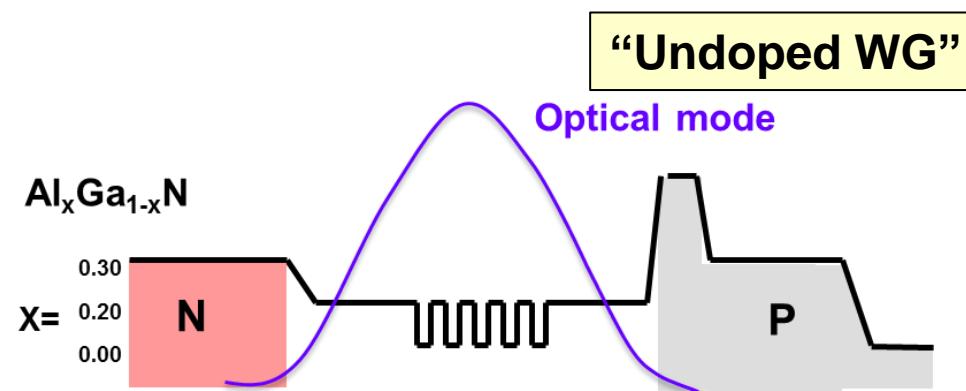
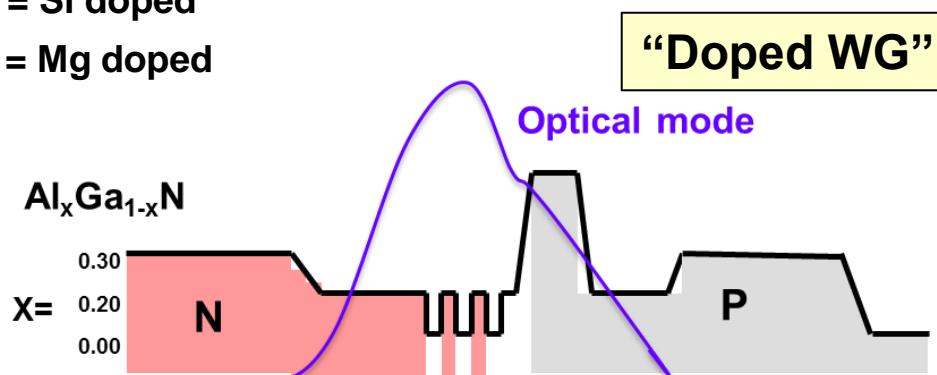
→ Low lasing threshold
~50 - 150 kW/cm²

* Miller et al. J Electron Mat 2009

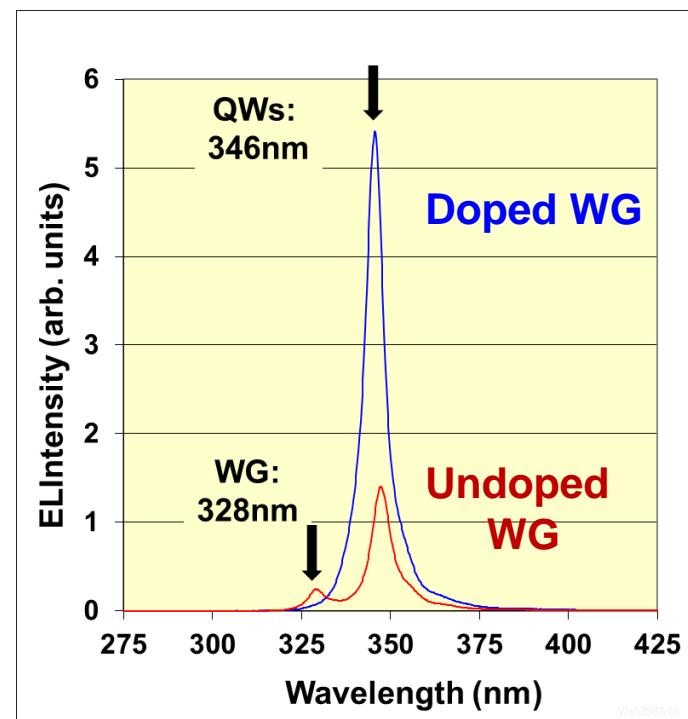
Laser Heterostructure Designs

Two designs to examine trade-off between carrier injection efficiency and optical loss

■ = Si doped
■ = Mg doped



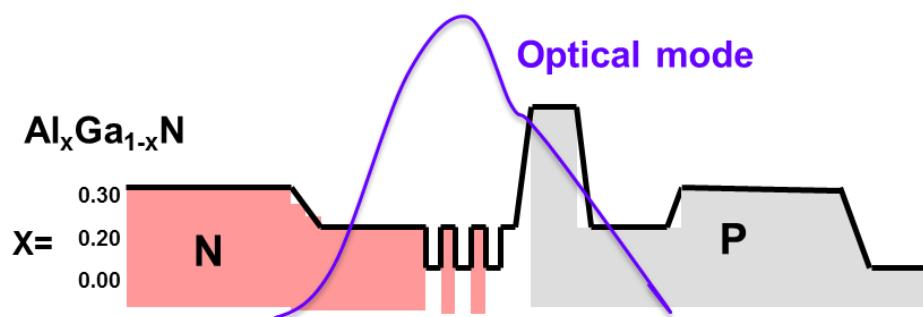
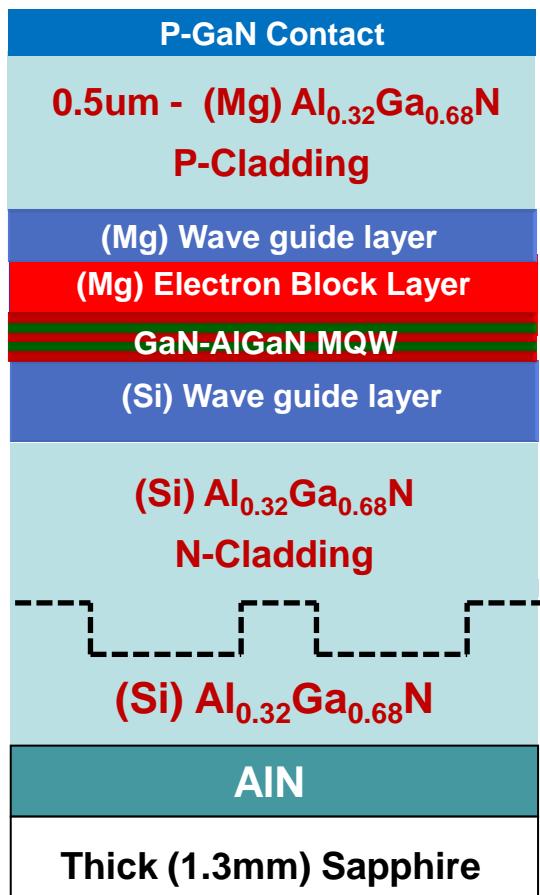
Electroluminescence (~13 A/cm²)



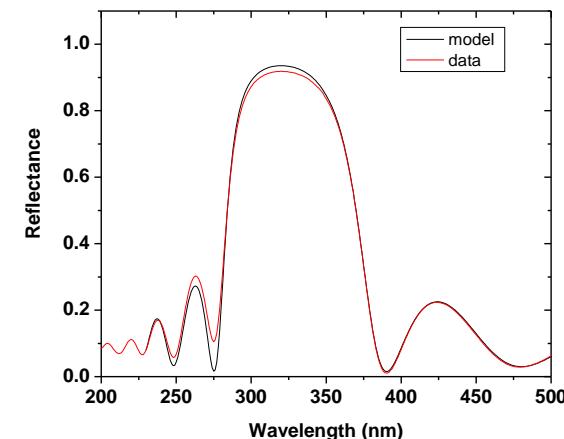
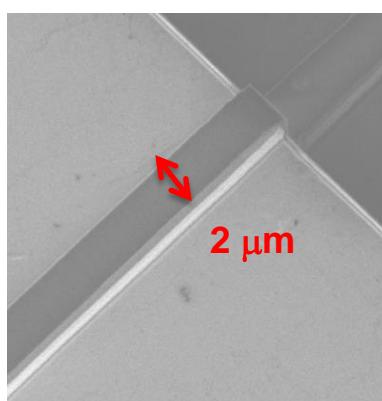
- Eliminate doping in waveguide for reduced optical loss
- Thin barriers, increase quantum well number for increased gain

Doped waveguide laser design

Laser Heterostructure



Fabrication



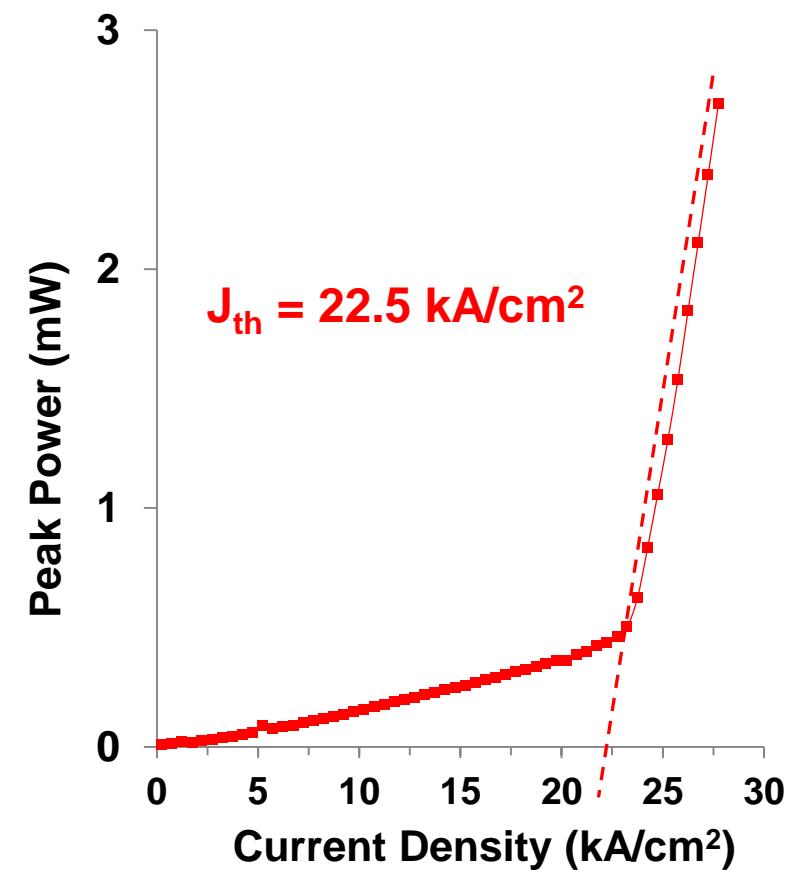
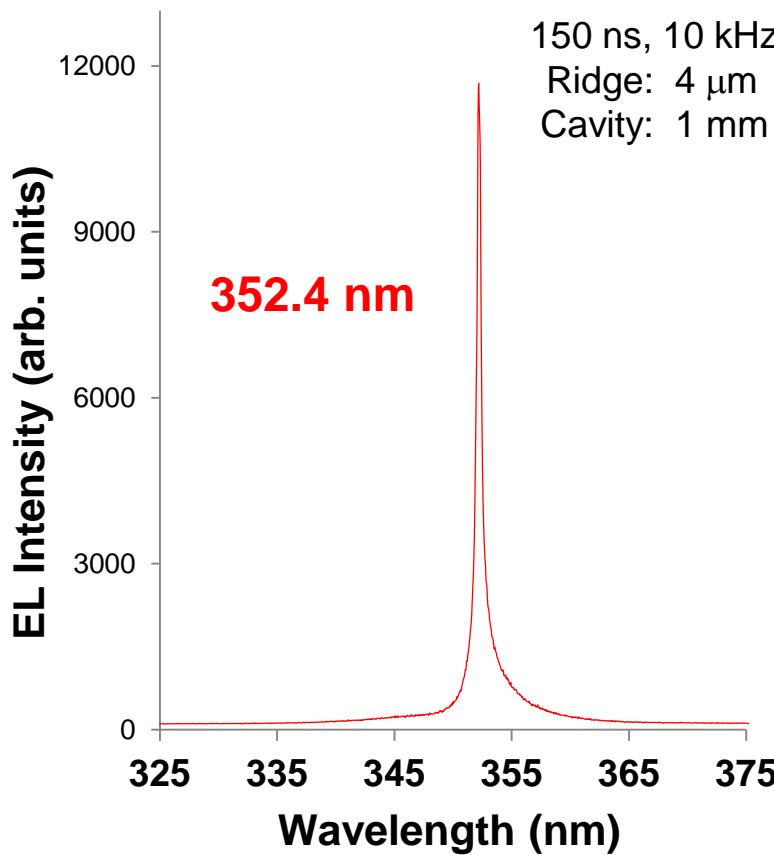
- Ridge Waveguide \rightarrow 2-7 μm width
- Cavity lengths \rightarrow 0.7-1.3 mm

$\text{HfO}_2/\text{SiO}_2$ Facet Coating

- Demonstrated $R > 0.90$ @ 320nm

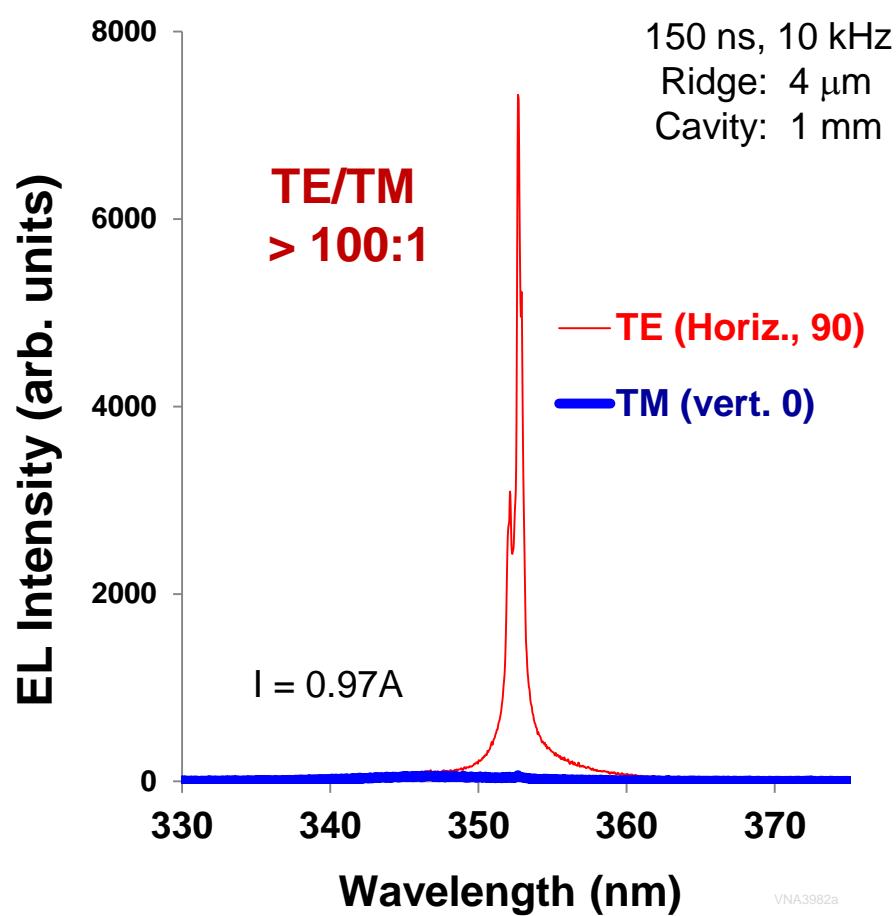
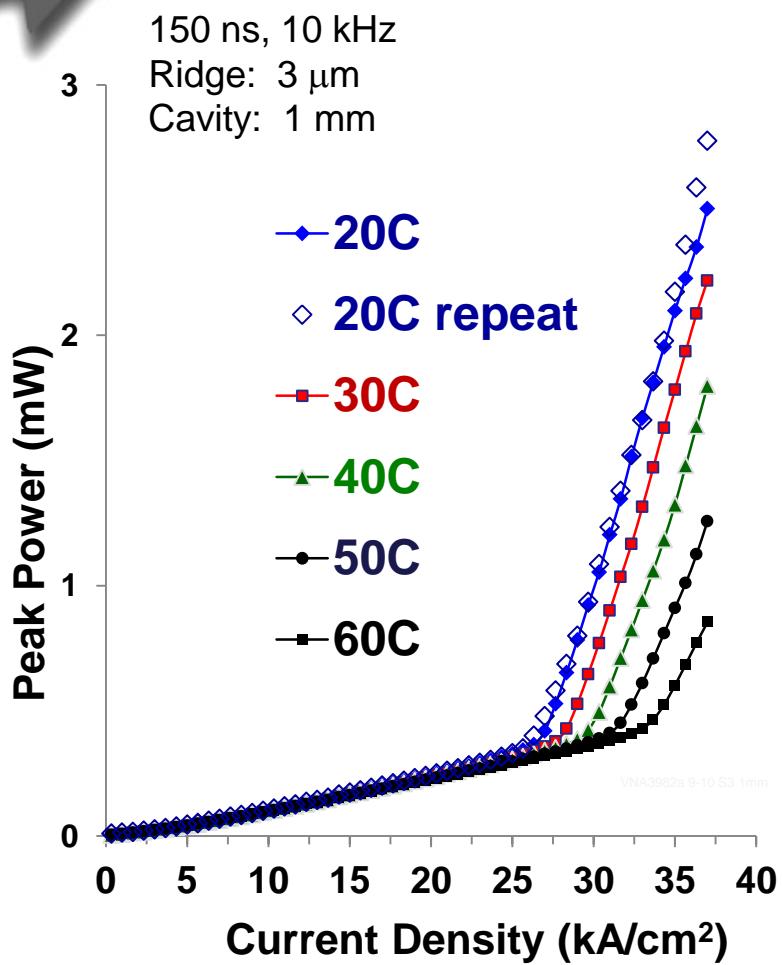
Doped waveguide design: Spectra and LI-data (pulsed)

Ridge waveguide process with etched, coated facets



→ Lasing from devices with 2-4um ridges, 0.7 - 1.3 mm cavities

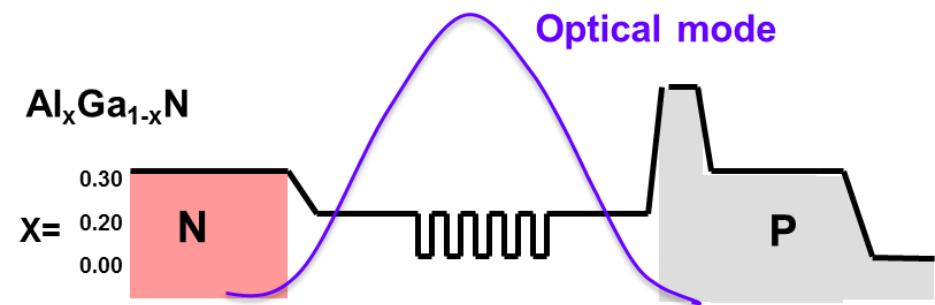
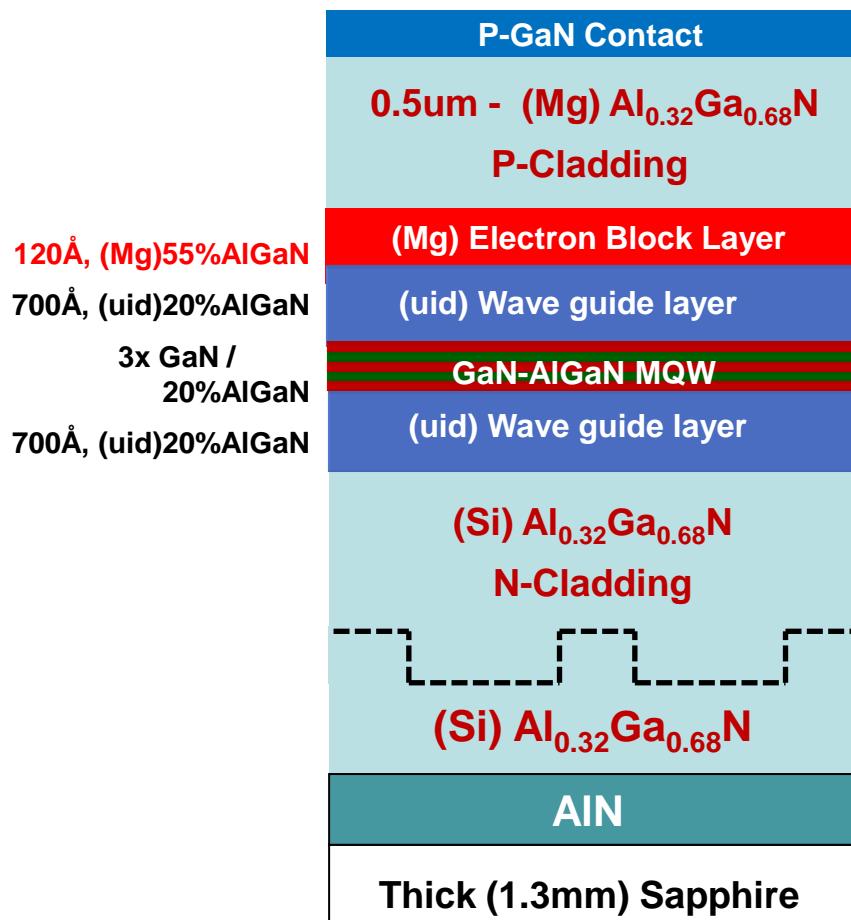
Doped waveguide design: Temperature dependent LI and polarization



- Devices are robust to 60°C and 37 kA/cm²
- TE / TM polarization > 100:1

Undoped waveguide laser design

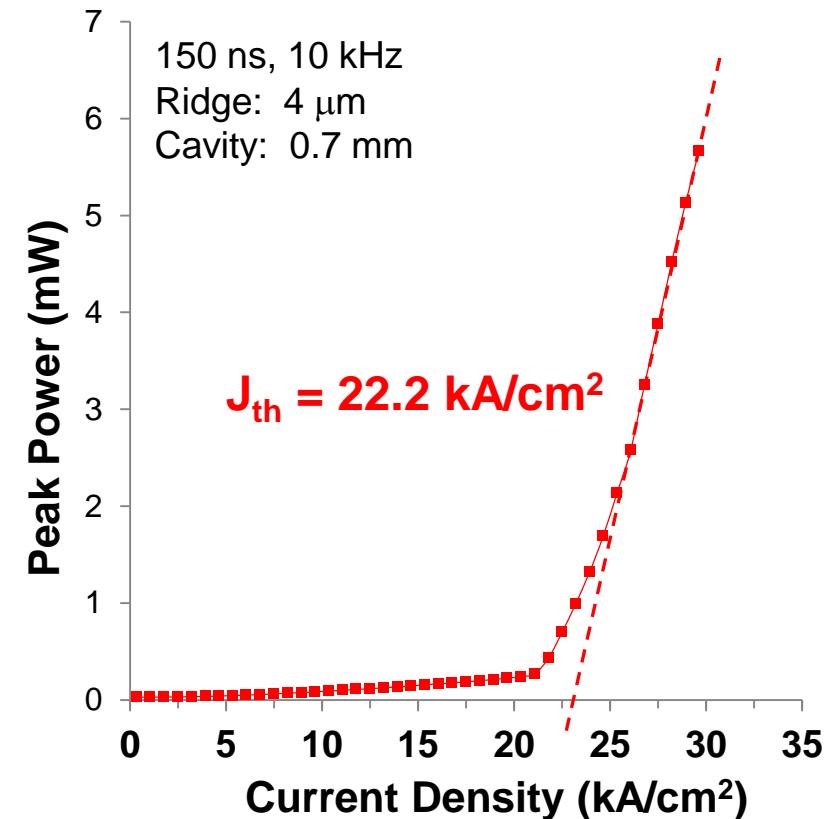
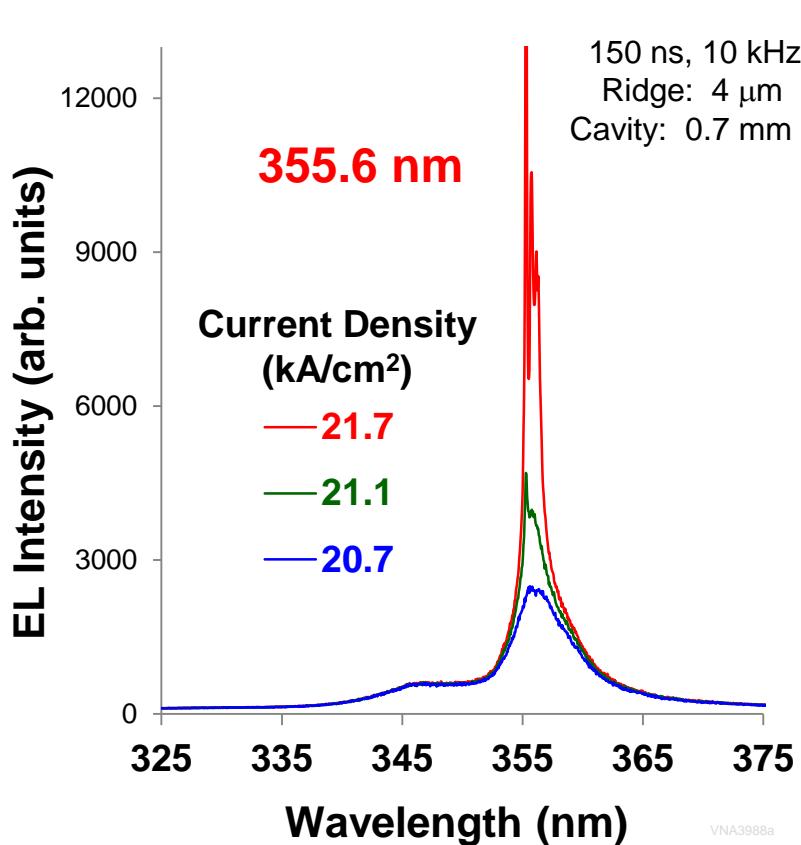
Undoped WG



- Lower optical losses
- Reduced carrier injection efficiency

Undoped waveguide design: Spectra and LI (pulsed)

Ridge waveguide process with etched, coated facets



- Threshold current densities are similar for both doped and undoped waveguide laser structures
- Anticipate common loss mechanism (e.g., p-cladding thickness)



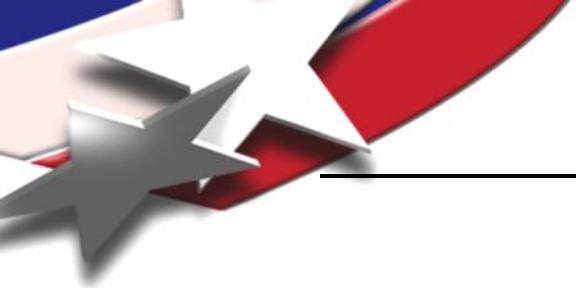
Summary

- Reduced dislocation density of $\text{Al}_{0.3}\text{Ga}_{0.7}\text{N}$ epilayers by growing over trenches etched in $\text{Al}_{0.3}\text{Ga}_{0.7}\text{N}$.

$$\rho = 2-3 \times 10^8 \text{ cm}^{-2}$$

- Transparent template \rightarrow bottom-emitting LEDs
- Spatially uniform reduction \rightarrow no device alignment to template
- Doped with Si \rightarrow simplifies vertical structure

- Optically pumped lasing at low thresholds ($J_{th} \sim 50 \text{ kW/cm}^2$)
- Diode lasing at 352-355nm from doped and undoped waveguide structures.



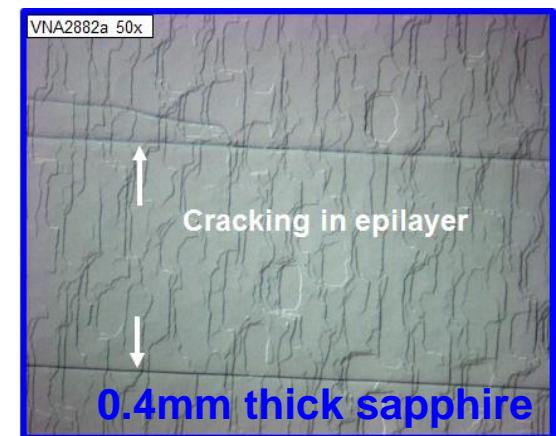
Back up Slides

Reduction of wafer bow and cracking using 3x thicker sapphire substrates

AlGaN template for low dislocation density



Optical Image of AlGaN surface



- Tensile strain in thick AlGaN overgrowth causes wafer to bow and epilayers to crack.
- 3x thicker sapphire substrate withstands strain and reduces wafer bowing and cracking.
- Photolithography over larger areas is enabled with less bow.

➔ 3x thicker sapphire substrate reduced wafer bow and epilayer cracking, greatly increasing wafer area for devices

