



Veeco D-125 MOCVD system

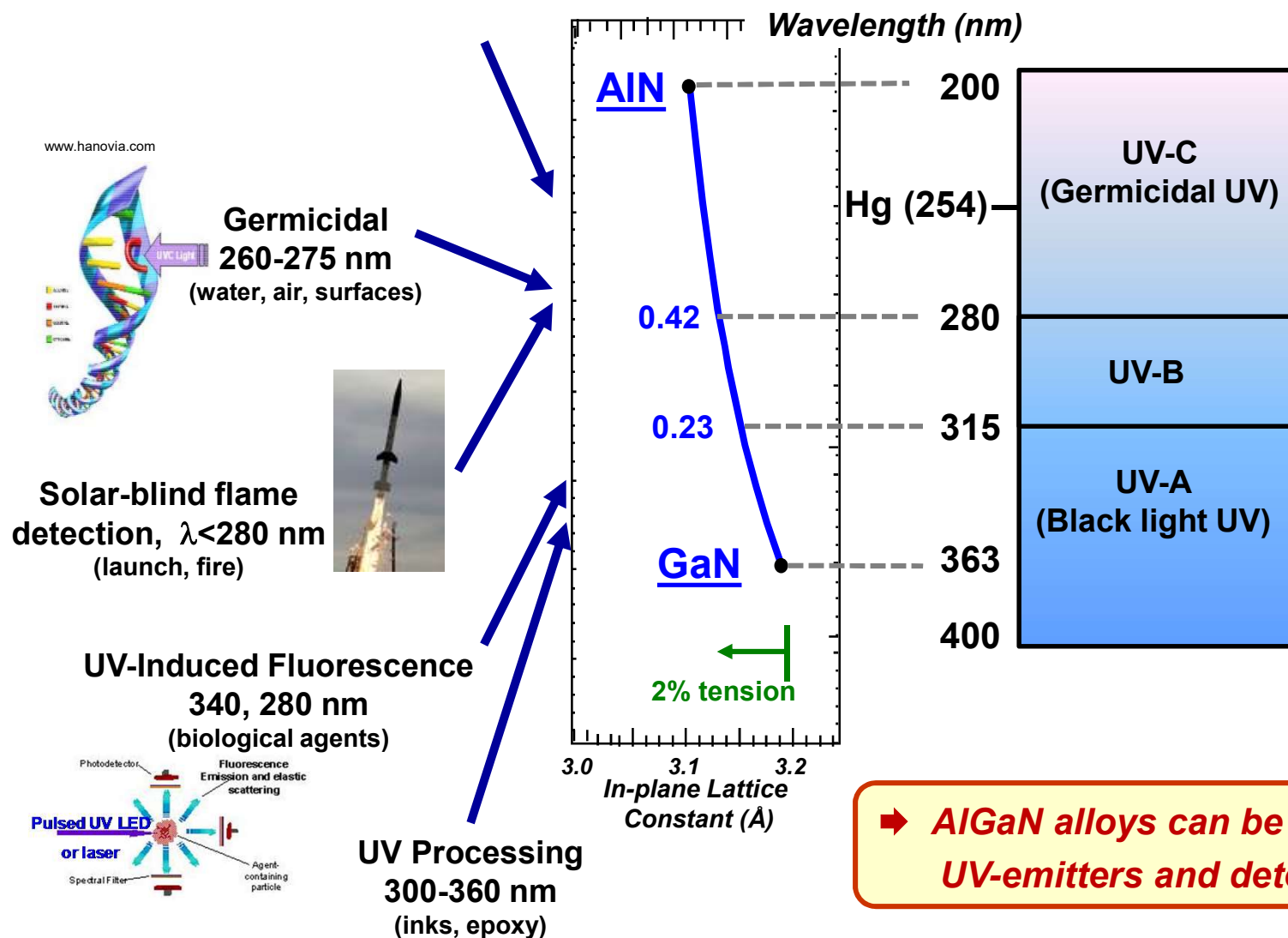
The growth of vertically conducting AlGaIn heterostructures on patterned GaIn substrates

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Sandia National Laboratories, Albuquerque, NM

- Introduction
- Growth of $\text{Al}_x\text{Ga}_{1-x}\text{N}$ ($X = 0.3$) on patterned N-GaN substrates
 - Pattern geometry and growth conditions
 - Threading dislocation density
- Summary

AlGaN Alloys Span UV-A, -B and -C Spectrum

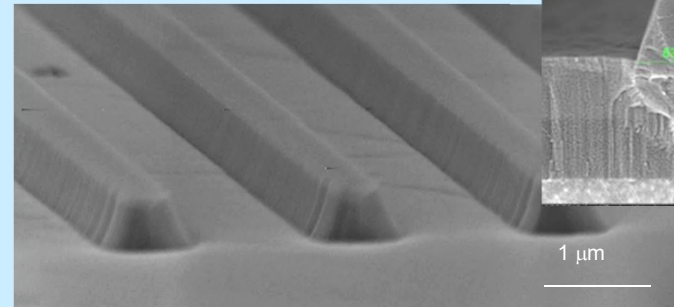
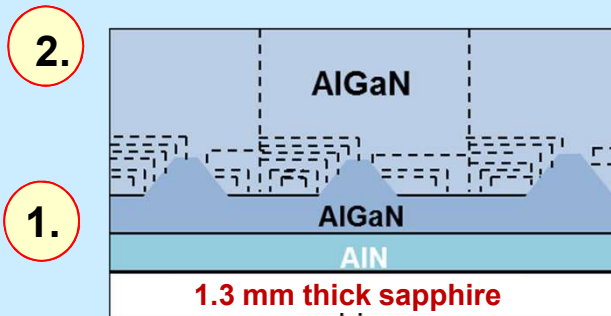


Sandia's AlGaN overgrowth of patterned templates

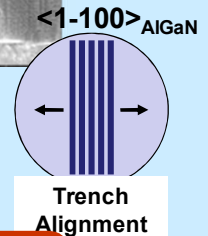
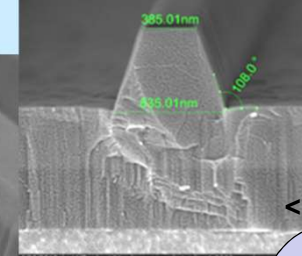
AlGaN Growth on Patterned Templates

Allerman et. al., JCG 2014

AlGaN with reduced dislocations

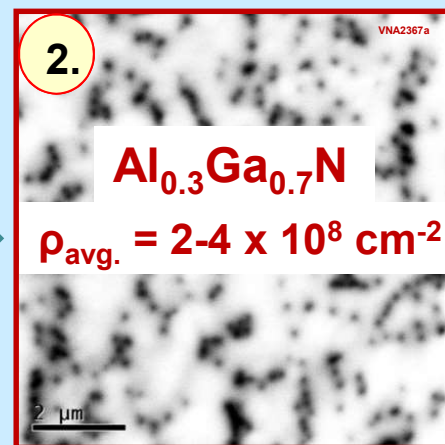
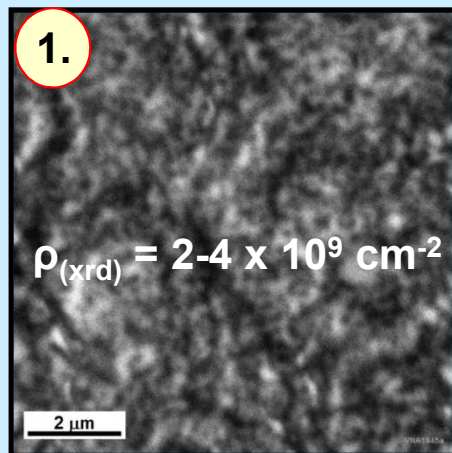


Mesa is 385nm at top!

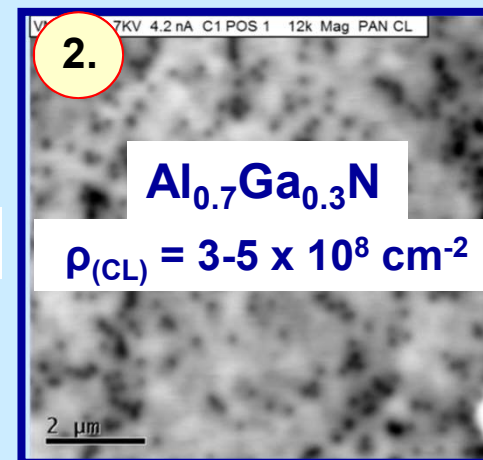


➔ Sub-micron features are key innovation for uniform reduction of dislocations

Cathodoluminescence (L. Alessi)

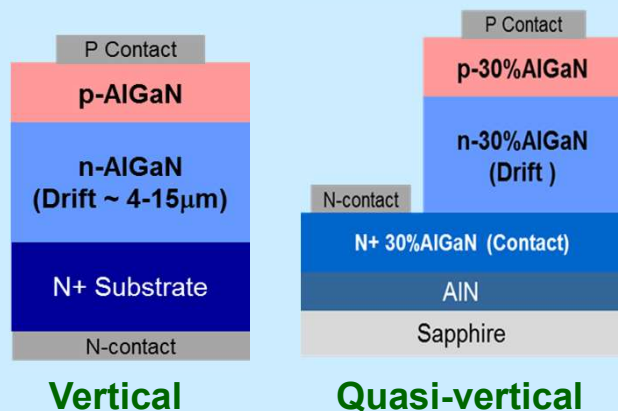


10-20X reduction



10-15x reduction

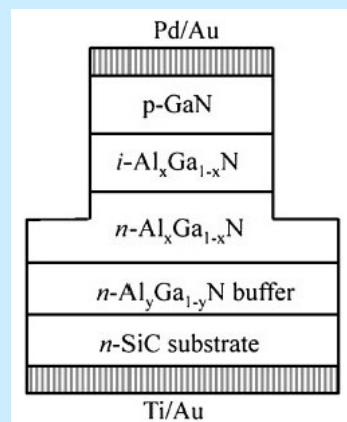
Low on-resistance requires vertical geometry diode



- **Quasi-vertical geometry useful for diode development**
 - Growth, process & JTE design
- **Quasi-vertical geometry results on high R_{on} due to current crowding under N-contact**
 - High resistive losses, low currents

➔ **Develop vertically conducting AlGaN diodes**

57%-AlGa PN diode on n-SiC



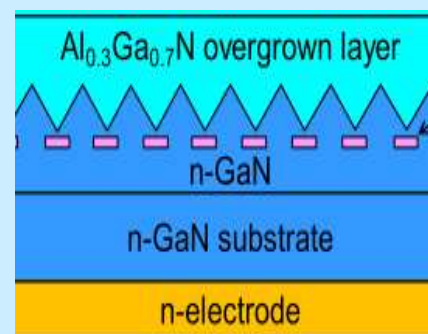
- 0.2 μm drift
- $V_{br} < 200\text{V}$

R_{on} : 1.5 mΩ-cm²

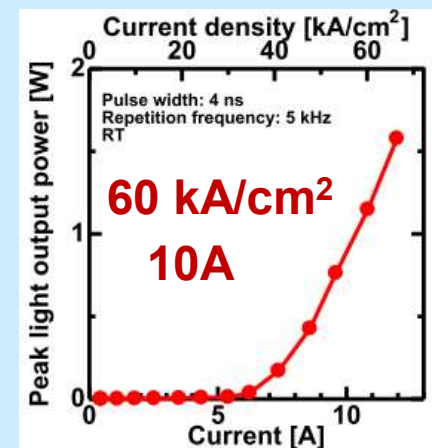
← n-SiC

Nishikawa Jpn J. App. Phys. 2007

338 nm laser diode on n-GaN Substrates

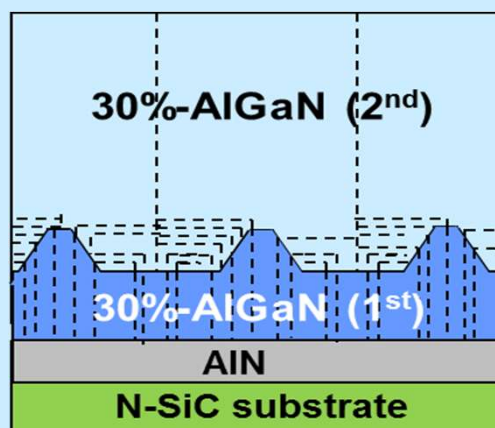


Taketomi, Jpn J Appl Phys 2016



$\text{Al}_x\text{Ga}_{1-x}\text{N}$ overgrowth on conductive substrates

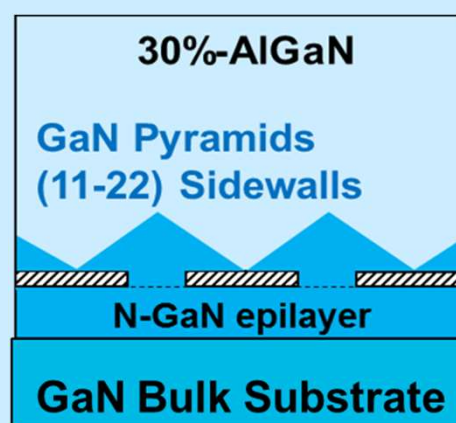
AlGaN on patterned n-SiC substrate



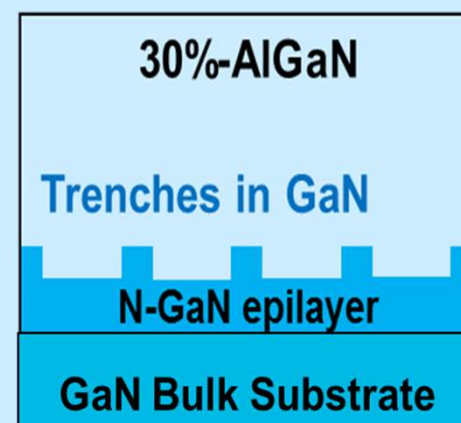
Sandia approach

- **Challenges:**
 - tensile strain from thermal mismatch (cracking)
- Requires conductive nucleation layer (replace AlN)

AlGaN regrowth on patterned n-type GaN substrates



Hamamatsu approach

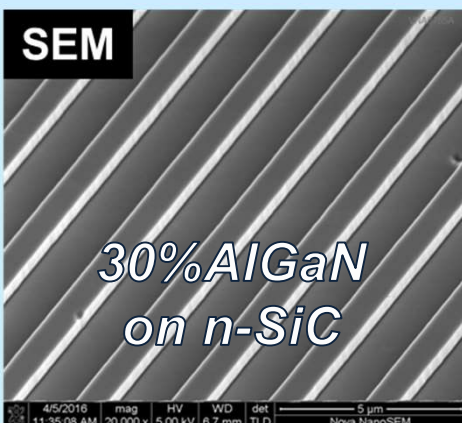
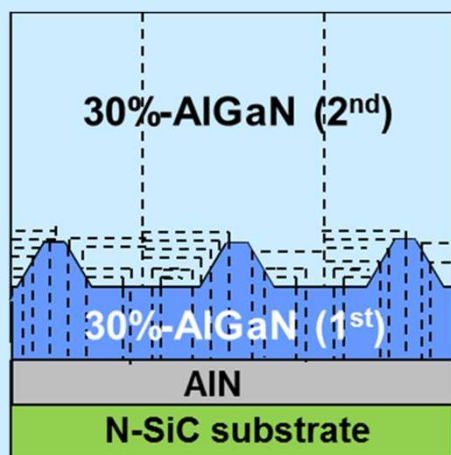


Sandia approach

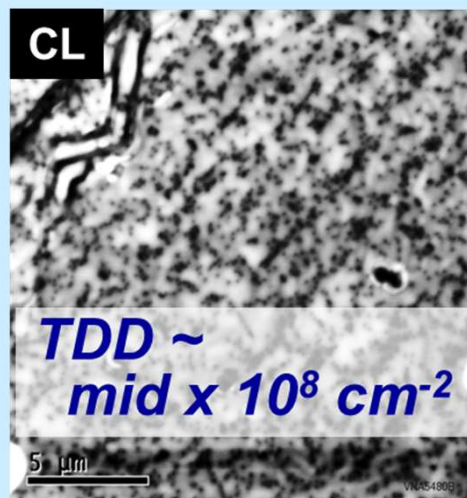
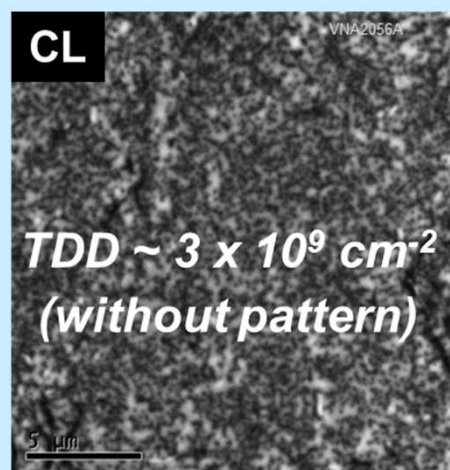
- Vertical UV-Laser diode demonstrated by Hamamatsu
- AlGaN growth on GaN trenches/pyramids is not straight forward

$\text{Al}_x\text{Ga}_{1-x}\text{N}$ ($x = 0.3$) overgrowth on n-type SiC substrates

Epi Structure

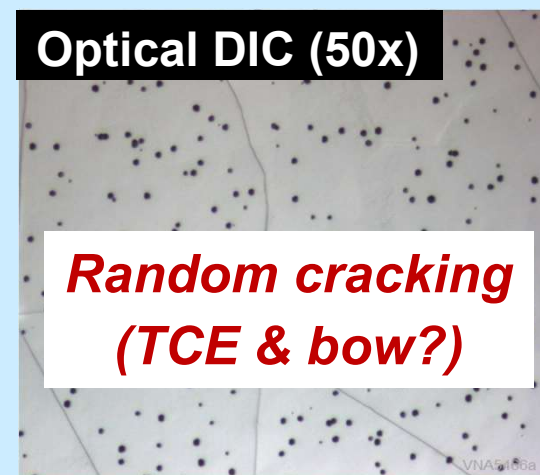


Defect Reduction



Surface Morphology

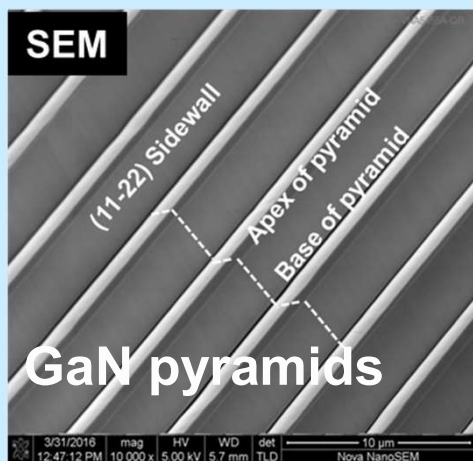
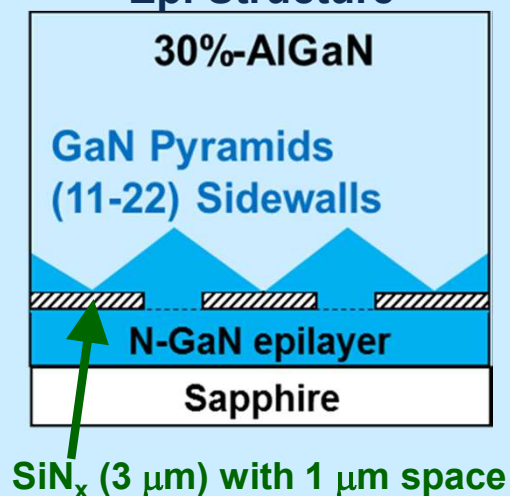
Optical DIC (50x)



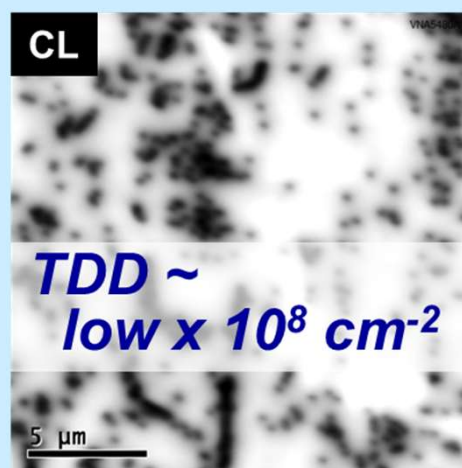
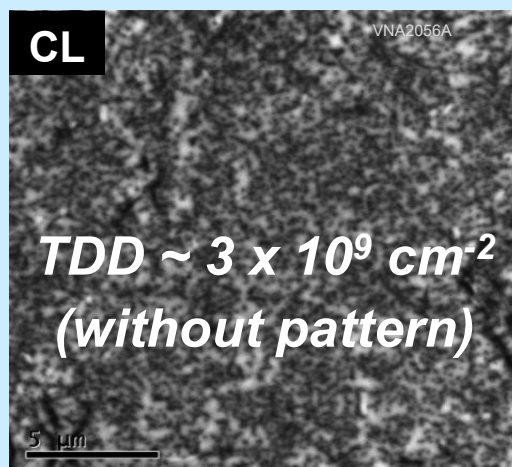
- ➔ TDD to mid $\times 10^8 \text{ cm}^{-2}$
- ➔ Random cracks in epi (Add compressive strain)
- ➔ Develop conductive nucleation layer

$\text{Al}_x\text{Ga}_{1-x}\text{N}$ ($x = 0.3$) overgrowth on n-type GaN substrates (Hamamatsu)

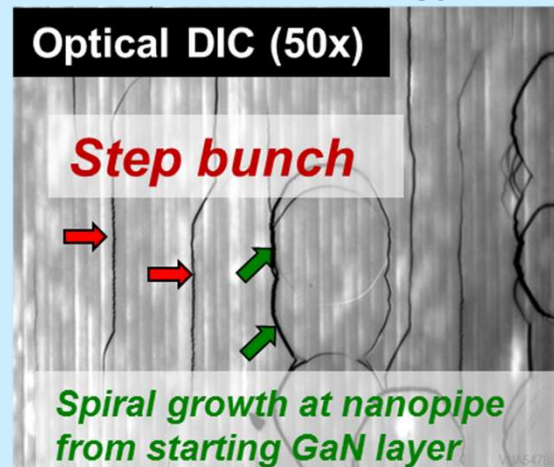
Epi Structure



Defect Reduction



Surface Morphology



- ➔ Well formed GaN pyramid
- ➔ TDD to low $\times 10^8 \text{ cm}^{-2}$
- ➔ AlGaIn overgrowth delamination from SiN_x

$\text{Al}_x\text{Ga}_{1-x}\text{N}$ ($x = 0.3$) overgrowth on n-type GaN substrates

30%-AlGaN

Trenches in GaN

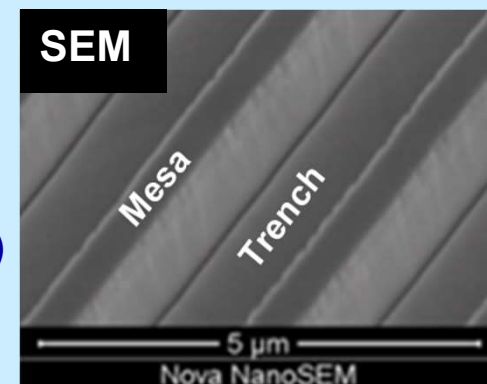
N-GaN epilayer

GaN Bulk Substrate

Patterned GaN substrate

- Investigated growth conditions:
 - ➔ V/III, Temperature,
- Geometry: $1\text{ }\mu\text{m}$ (mesa) x $1\text{ }\mu\text{m}$ (trench)
- Etch depth: 0.4 to $1.2\text{ }\mu\text{m}$

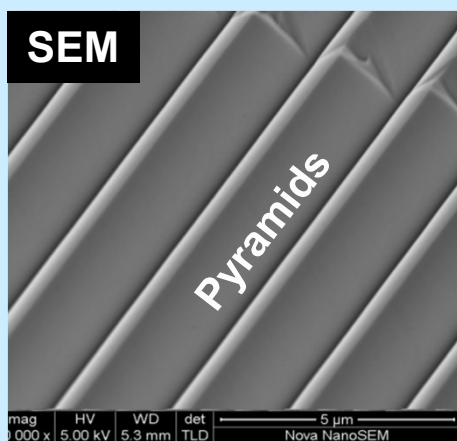
SEM



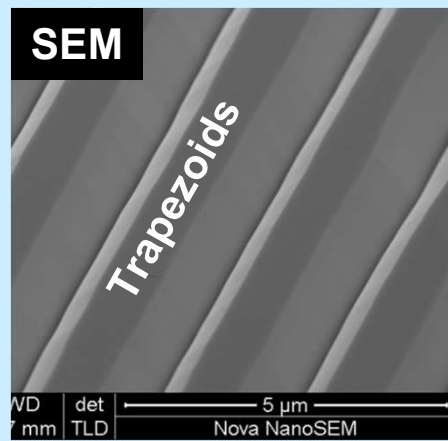
Growth initiation & AlGaN overgrowth

- Facet control during growth enables manipulation of threading dislocations & planar surfaces

SEM



SEM



Optical DIC (50x)

- Fully coalescence 45%AlGaN
- Particles
- Cracking

➔ *Overgrowth on GaN to $x = 0.45$*

Vertical $\text{Al}_{0.3}\text{Ga}_{0.7}\text{N}$ PN diode on a conducting GaN substrate

- Vertical PN in 30%AlGaN

- 30%AlGaN pn diode now grown on a conducting GaN substrate

- Drift region:

Thickness: $5.8\ \mu\text{m}$

N_o : $2-4 \times 10^{16}\ \text{cm}^{-3}$

- 30%AlGaN overgrowth:

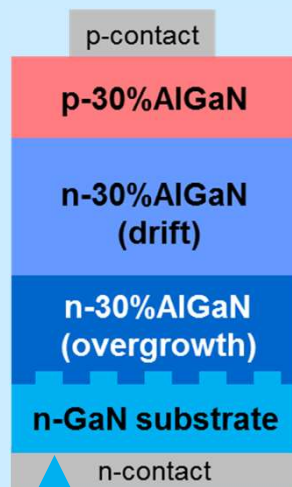
Thickness: $6\ \mu\text{m}$

N_o : $2-5 \times 10^{17}\ \text{cm}^{-3}$

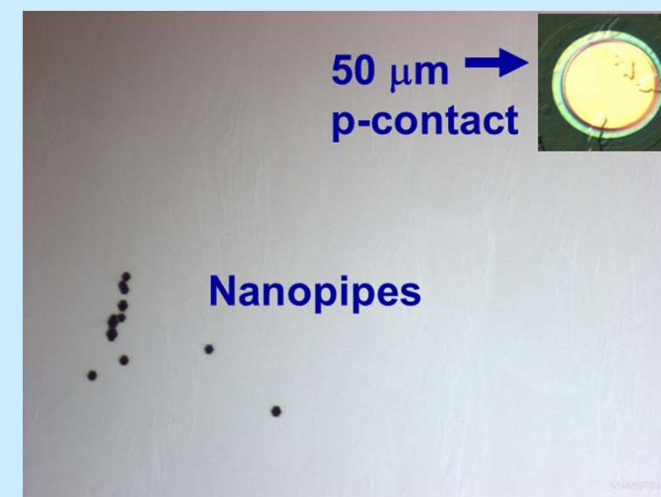
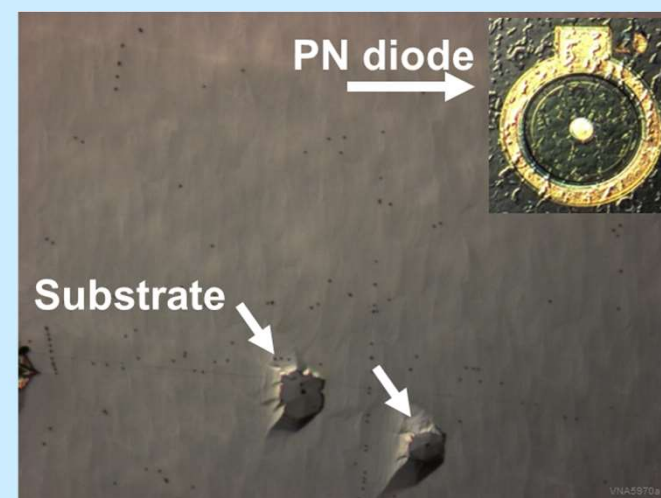
- Total epi: $12.4\ \mu\text{m}$

Little cracking

Smooth morphology



Patterned GaN substrate



- **Successful overgrowth of $\text{Al}_x\text{Ga}_{1-x}\text{N}$ on patterned N-GaN substrates to $X = 0.45$**
- **Overgrowth at higher compositions requires higher growth temperatures which decompose GaN substrate**
- **N-type SiC substrates are suitable for higher growth temperatures but epilayer cracking must be prevented**