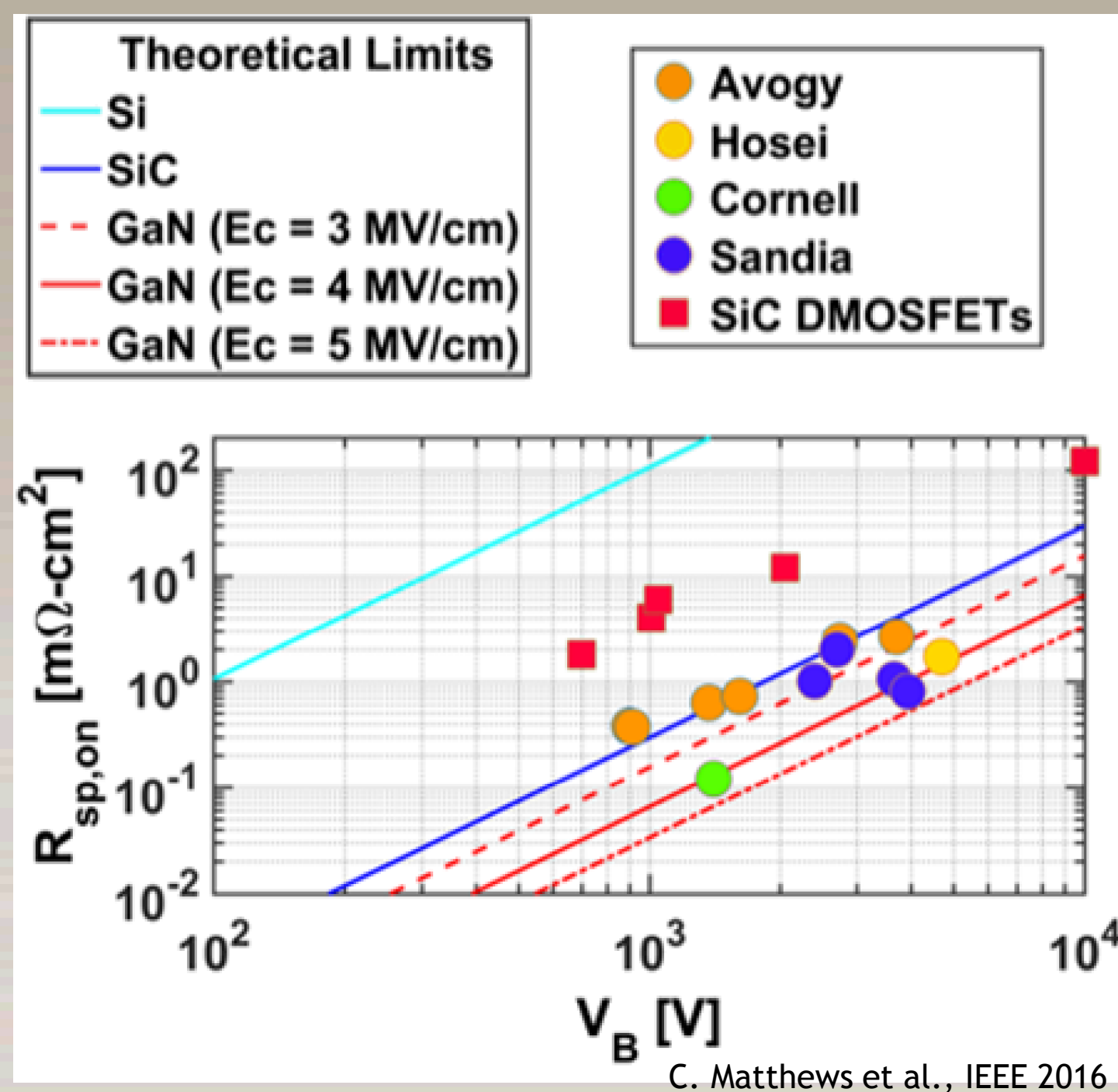


Switching Reliability of Vertical GaN PiN Diodes

O. Slobodyan^{*1}, S. Sandoval², J. Flicker¹, R. Kaplar¹, C. Matthews¹, M. van Heukelom¹, S. Atcity¹, O. Aktas³, and I. C. Kizilyalli⁴

Why Gallium Nitride?

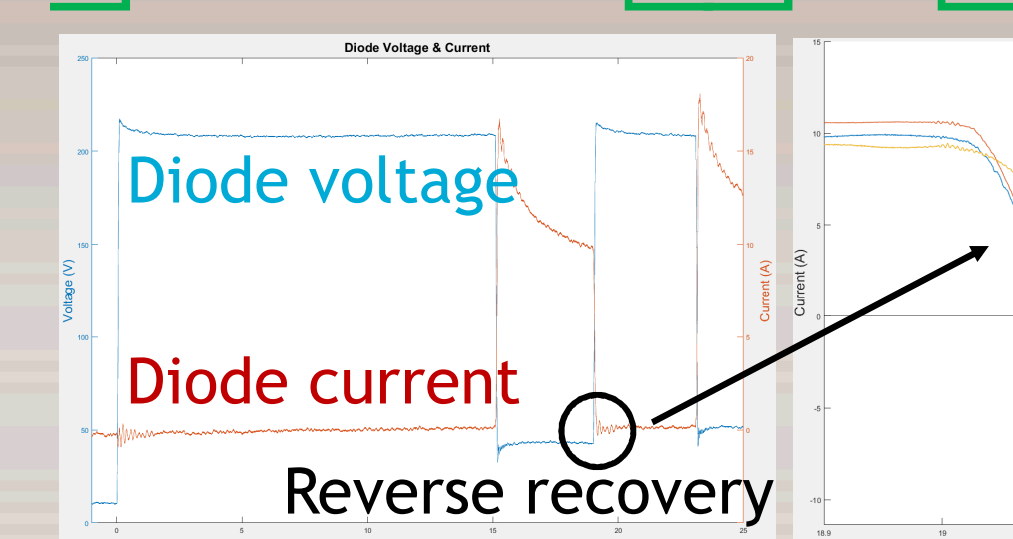
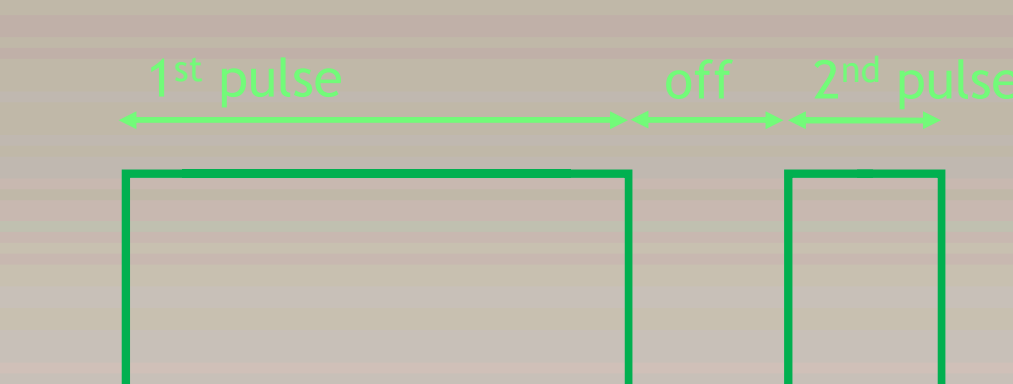


Better material →
Improved device
performance &
efficiency →
Reduced passive
component & cooling
demands →
Lower overall
system cost

Double-Pulse Test Circuit

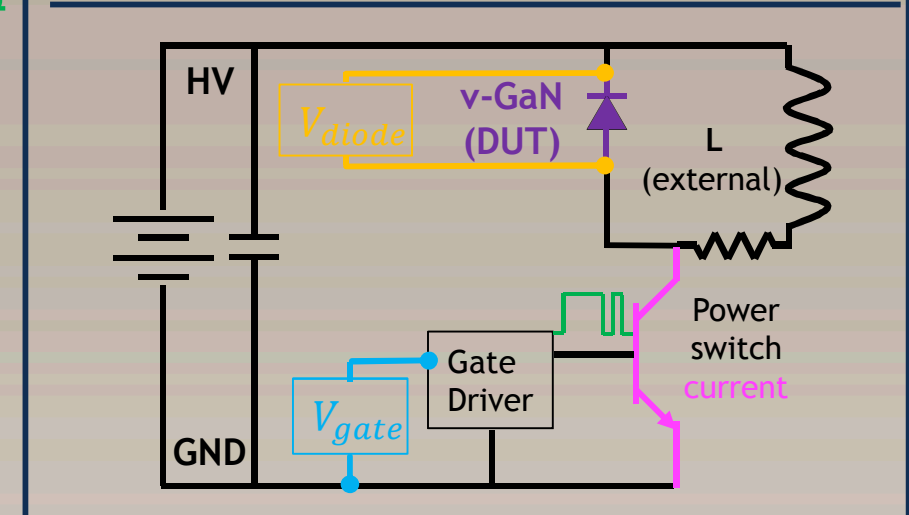
Double-pulse Switching

Characterization:

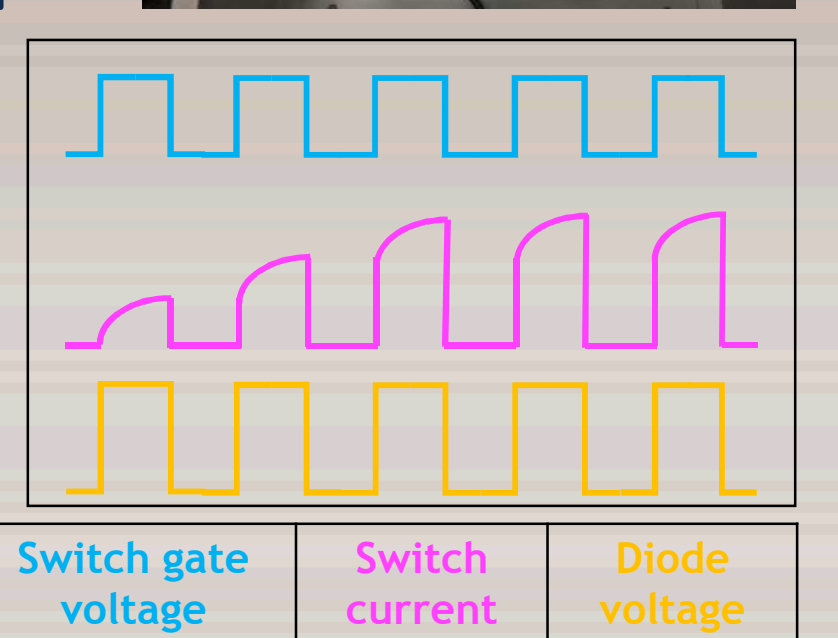


C. Matthews et al., IEEE 2016

DPTC Schematic

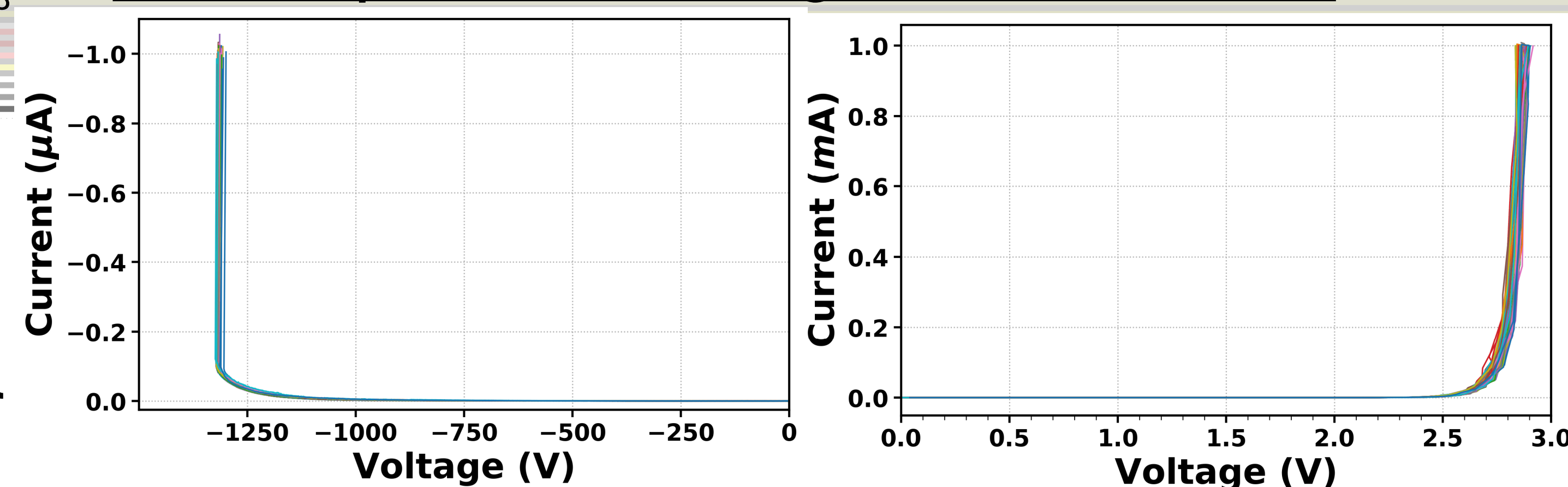


Stress Testing:

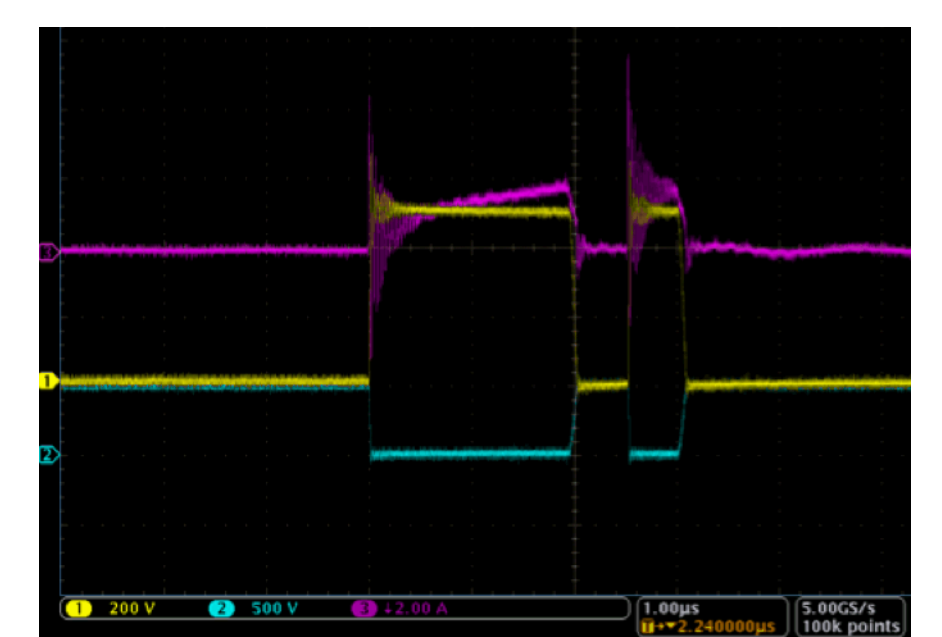
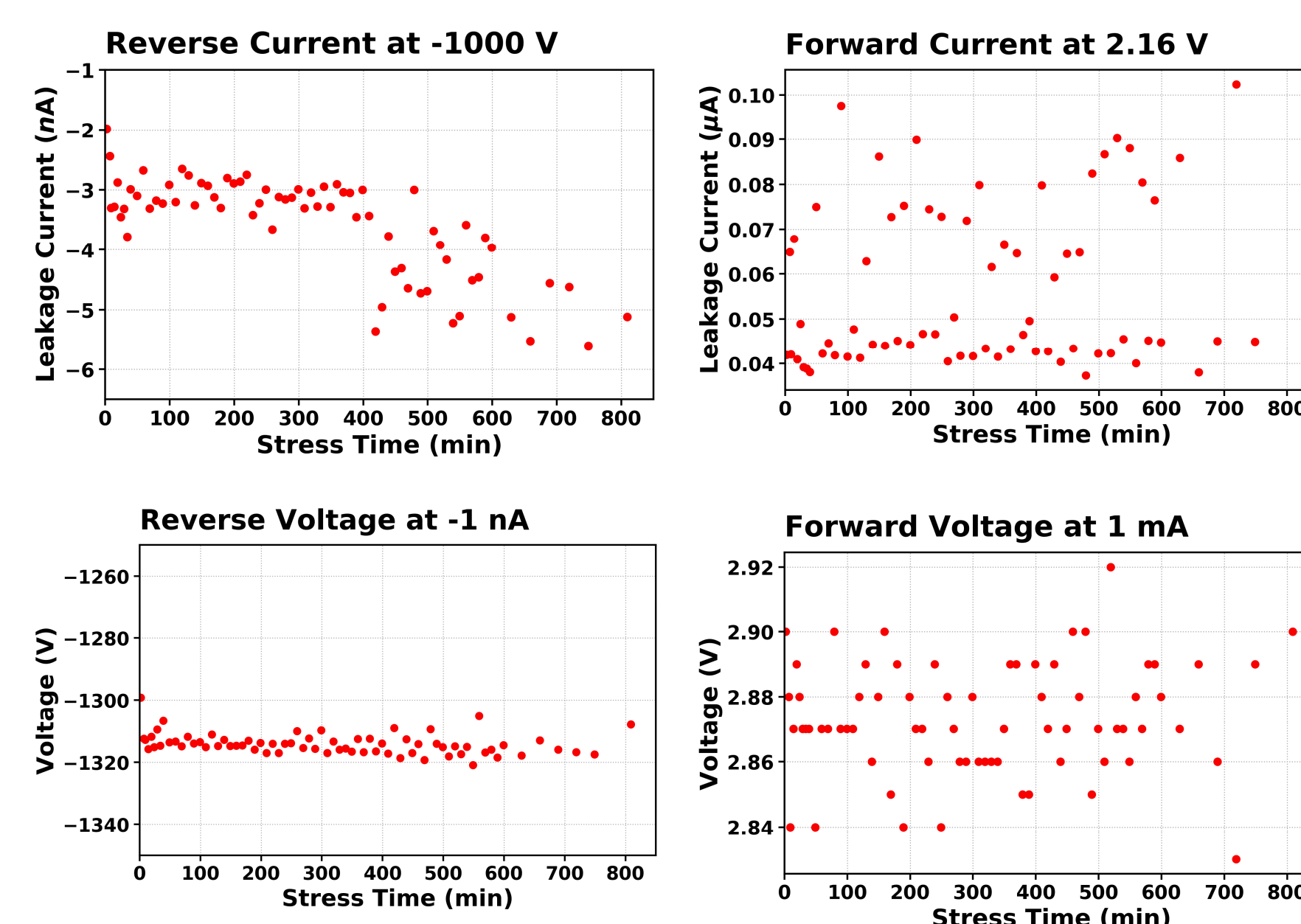


Stress Testing Results

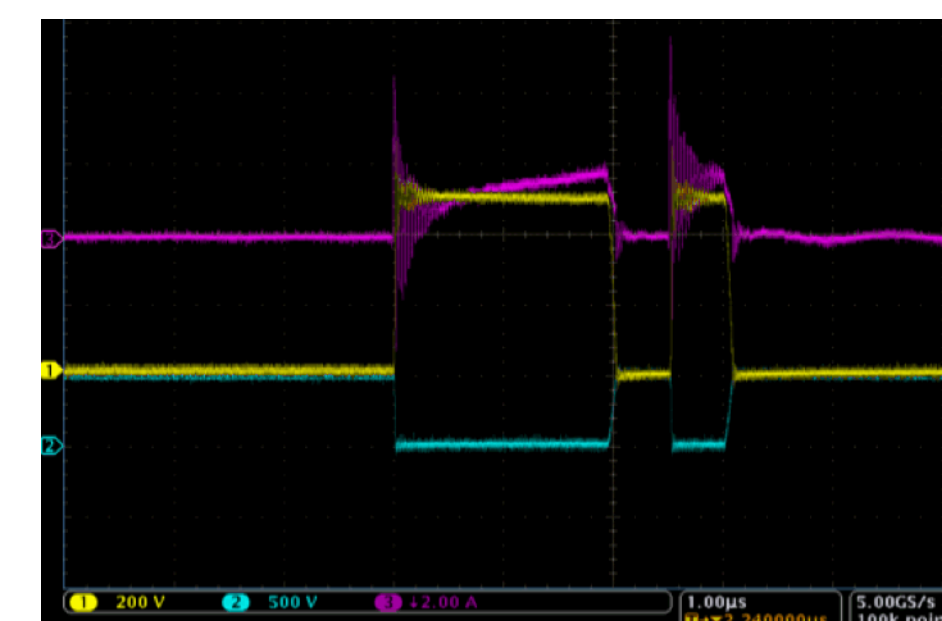
Double-pulse switching stress at -500 V



Packaged devices stable for 800 minutes of switching stress:

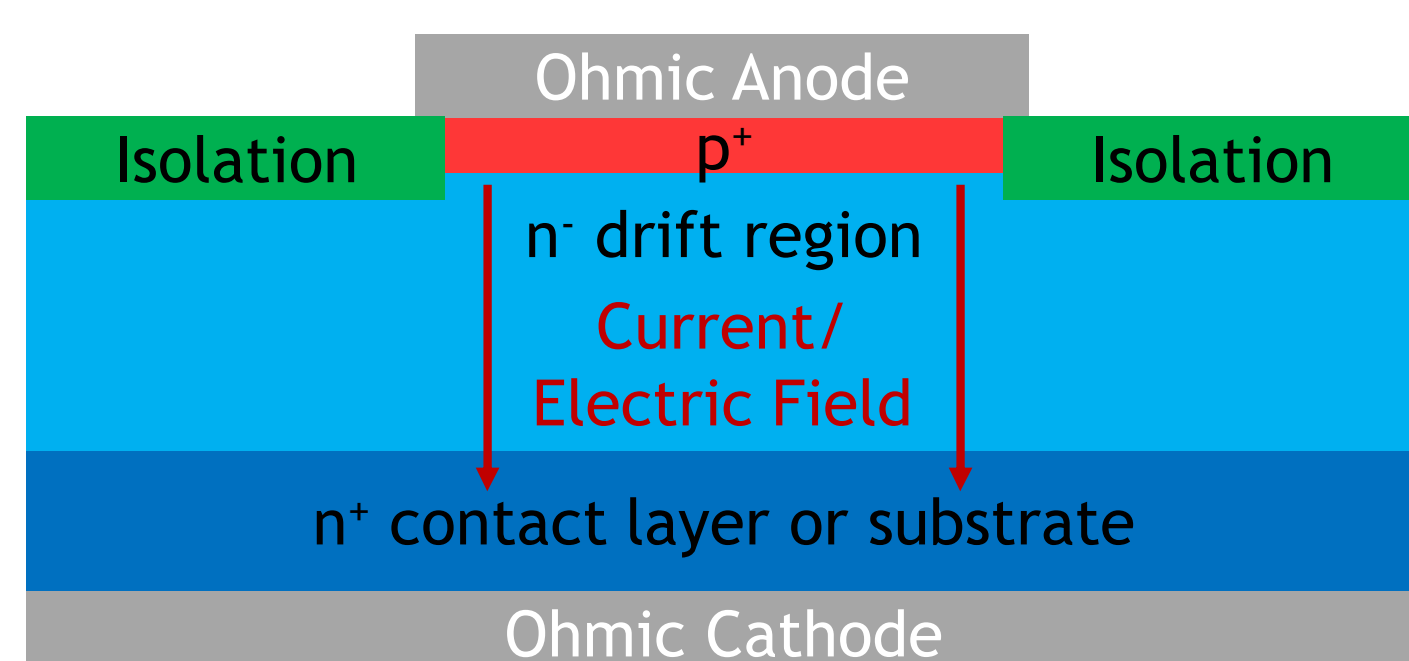


Initial Reverse-recovery



Final Reverse-recovery

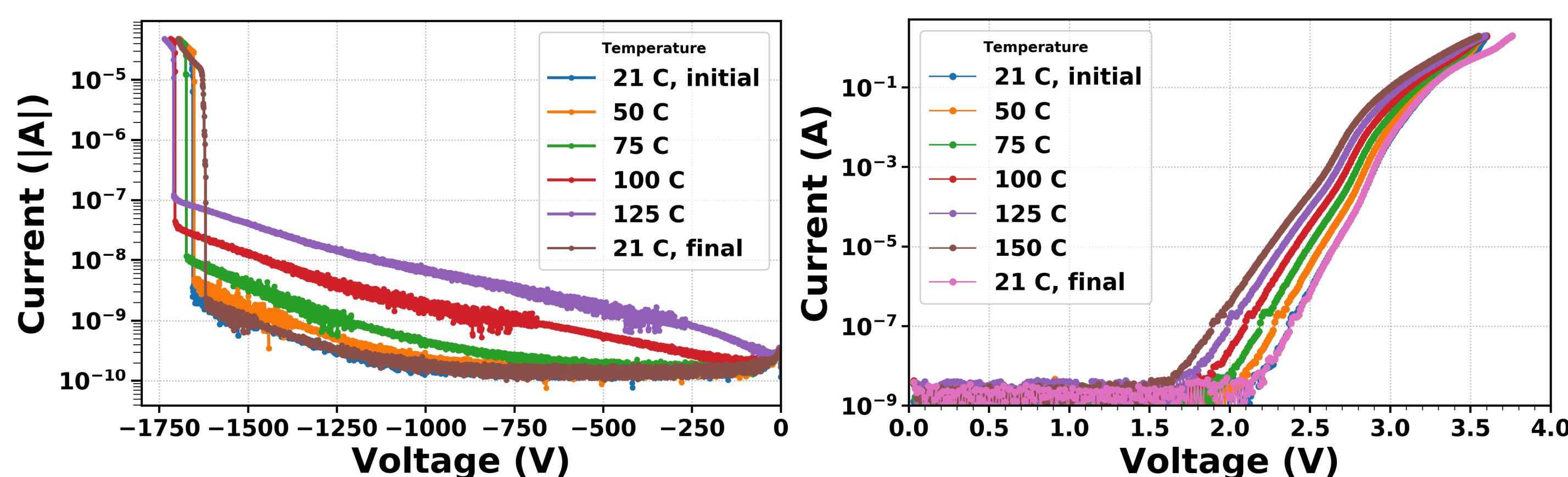
Vertical PiN Diode



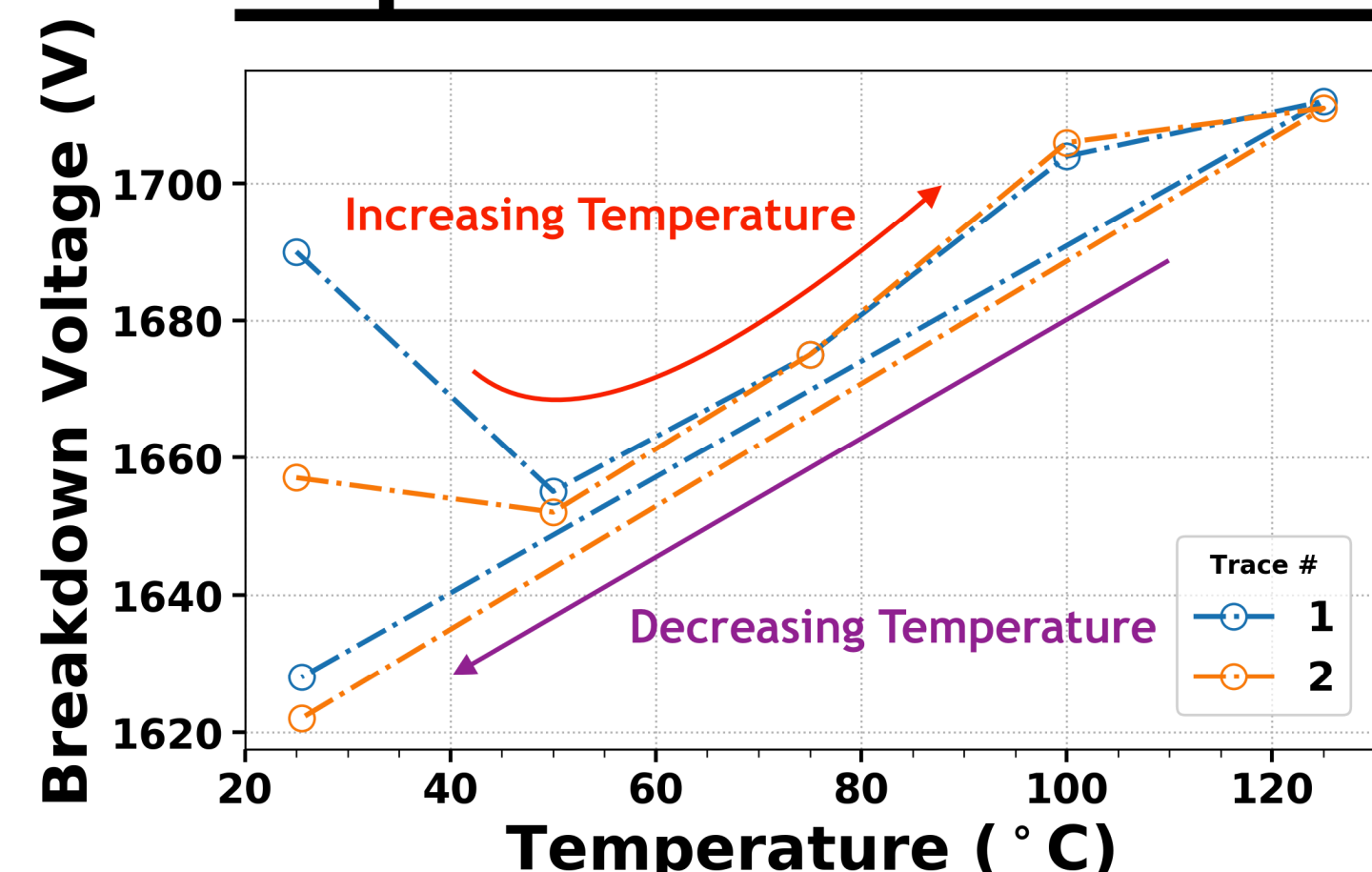
○ Vertical structure
offers superior
electric field control
➤ higher voltage and
current limits

**Superior material must translate to both
good device behavior and reliability**

Current-Voltage Characterization



Expected I-V wafer device performance



○ Positive breakdown
coefficient
➤ *avalanche process*
○ Breakdown hysteresis
➤ evidence of burn-
in effect

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