



# Hyper Temporal Sensor Accomplishments

SAND2017-XXXXX X

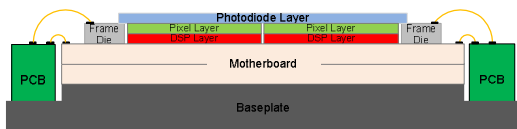
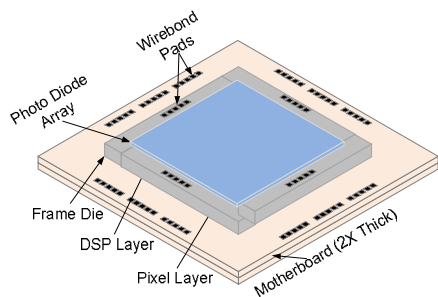


MIXED SIGNAL  
ASIC/SOC PRODUCTS

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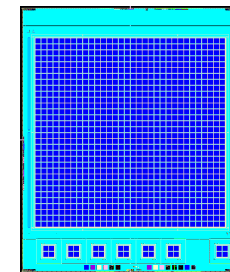
# Hyper Temporal Sensor

Develop a highly pixelated, high scan-rate, event driven FPA, using heterogeneous integration and design.



## FY17 Accomplishments:

- Espanola design and tapeout
- Quintisho fabrication completed
- Macho design and tapeout
- Relleno design and tapeout
- Lombardi design and tapeout
- Unnan design and tapeout
- FPGA Emulator design Phase I completed
- Testing of multiple short-loop lots completed
- Serrano design completed
- Tabasco design completed



*Macho*

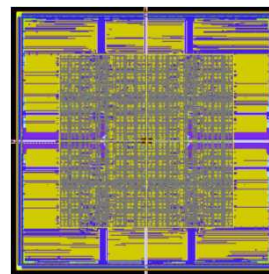
## Contributors

**Design/Layout:** Anna Burns, James Conner, Eric Edwards, Art Gonzalez, Rex Kay, Jeff McCasland, Russ Mikawa, Russ Miller, Brandon Mitchell, Eric Morgan, Moslema Sharif

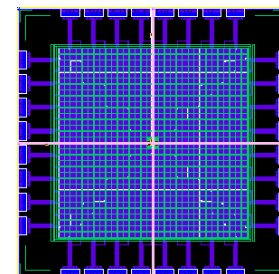
**Packaging:** Lu Fang

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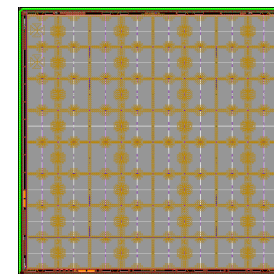
**PM:** James Levy



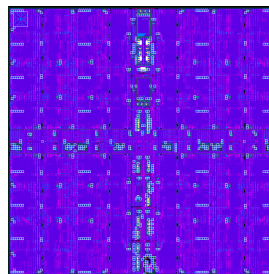
*Lombardi*



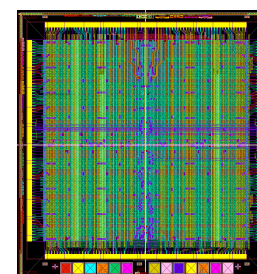
*Relleno*



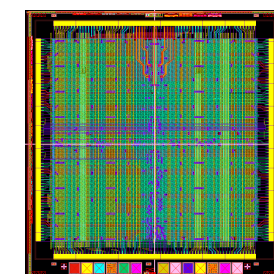
*Unnan*



*Espanola*



*Serrano*



*Tabasco*