

# Fabrication of Multilayer Glass Capacitors

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# Outline

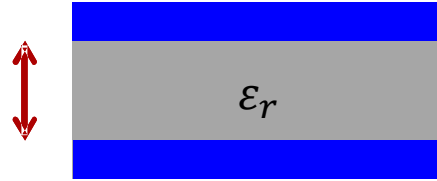


- Linear Dielectrics as Energy Storage Devices
- Glass as a Dielectric Material
- Alkali Free Glass
  - Properties of Thinned Glass
  - Multi-layer Glass Capacitor
- Conclusions

# Energy Density of Linear Dielectrics

Area (A)

thickness (t)

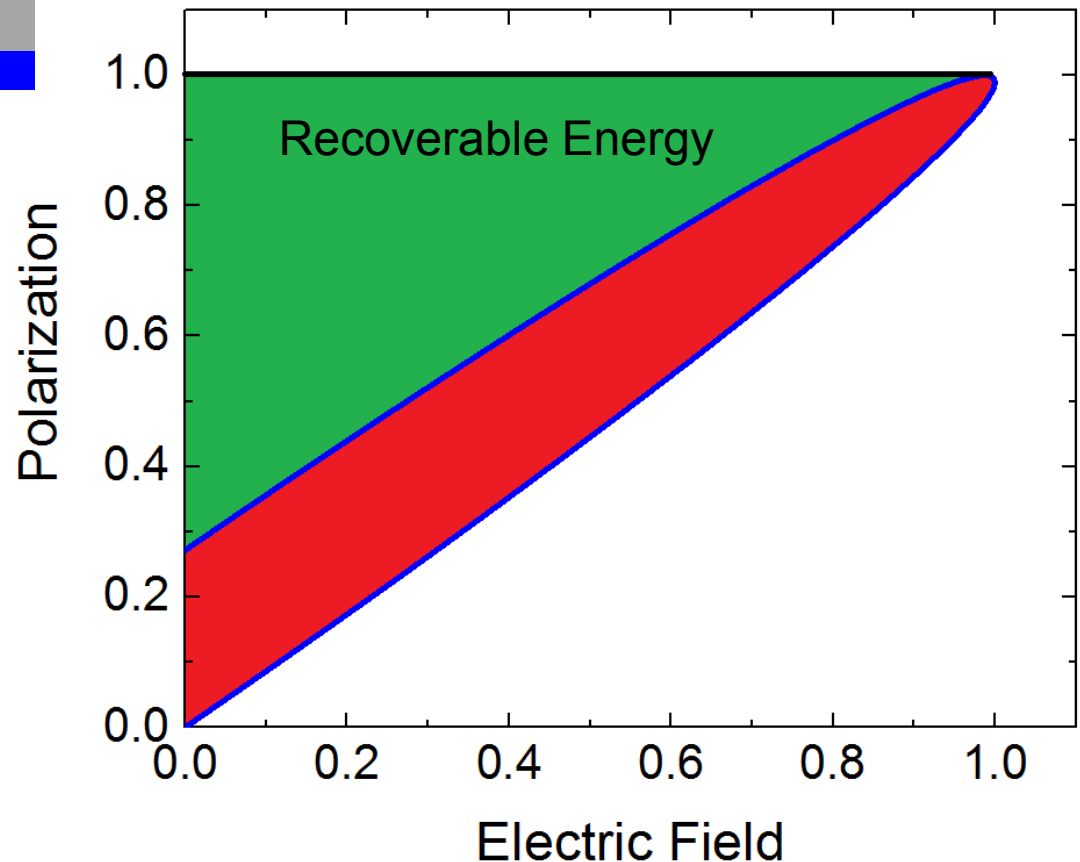


$$C = \epsilon_r \epsilon_0 \frac{A}{t}$$

$$W = \int P dE$$

$$W = \frac{1}{2} CV^2 = \frac{1}{2} \epsilon_r \epsilon_0 \frac{A}{t} V^2$$

$$U = \frac{\text{Energy}}{\text{Volume}} = \frac{1}{2} \epsilon_r \epsilon_0 E^2$$



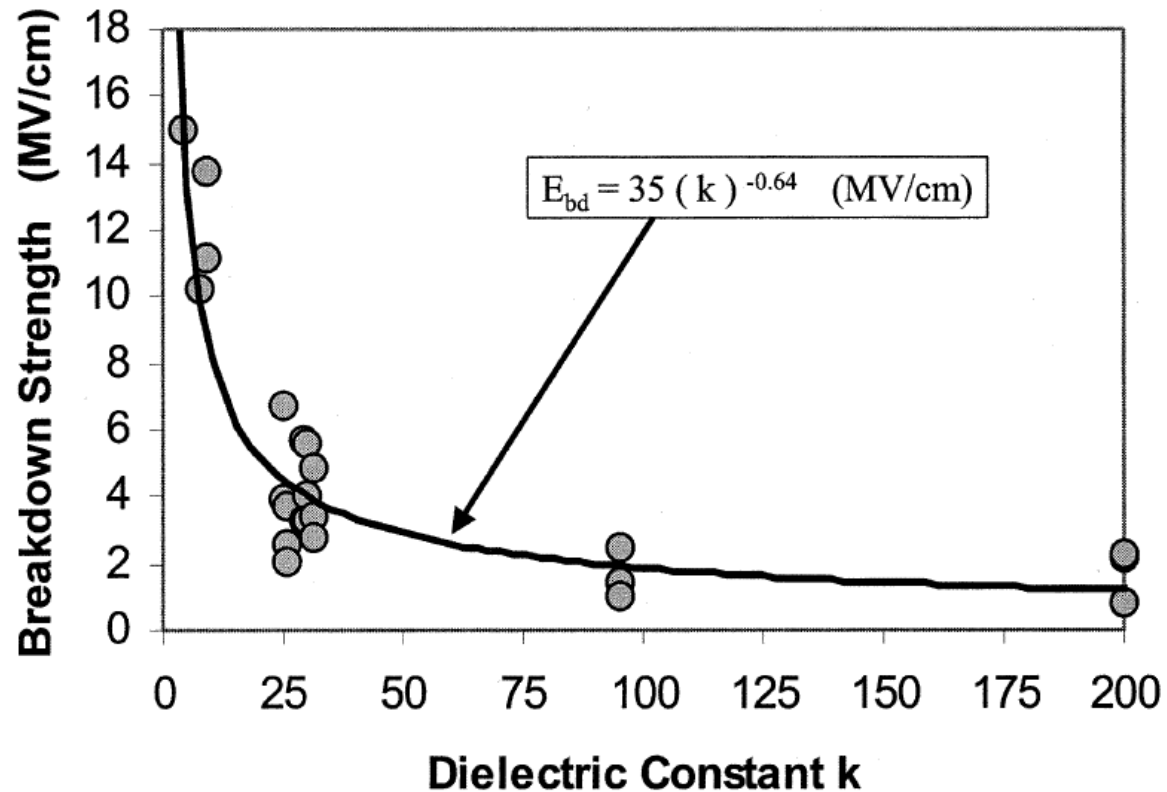
# Energy Density of Linear Dielectrics



Larger gains can be had by increasing breakdown strength

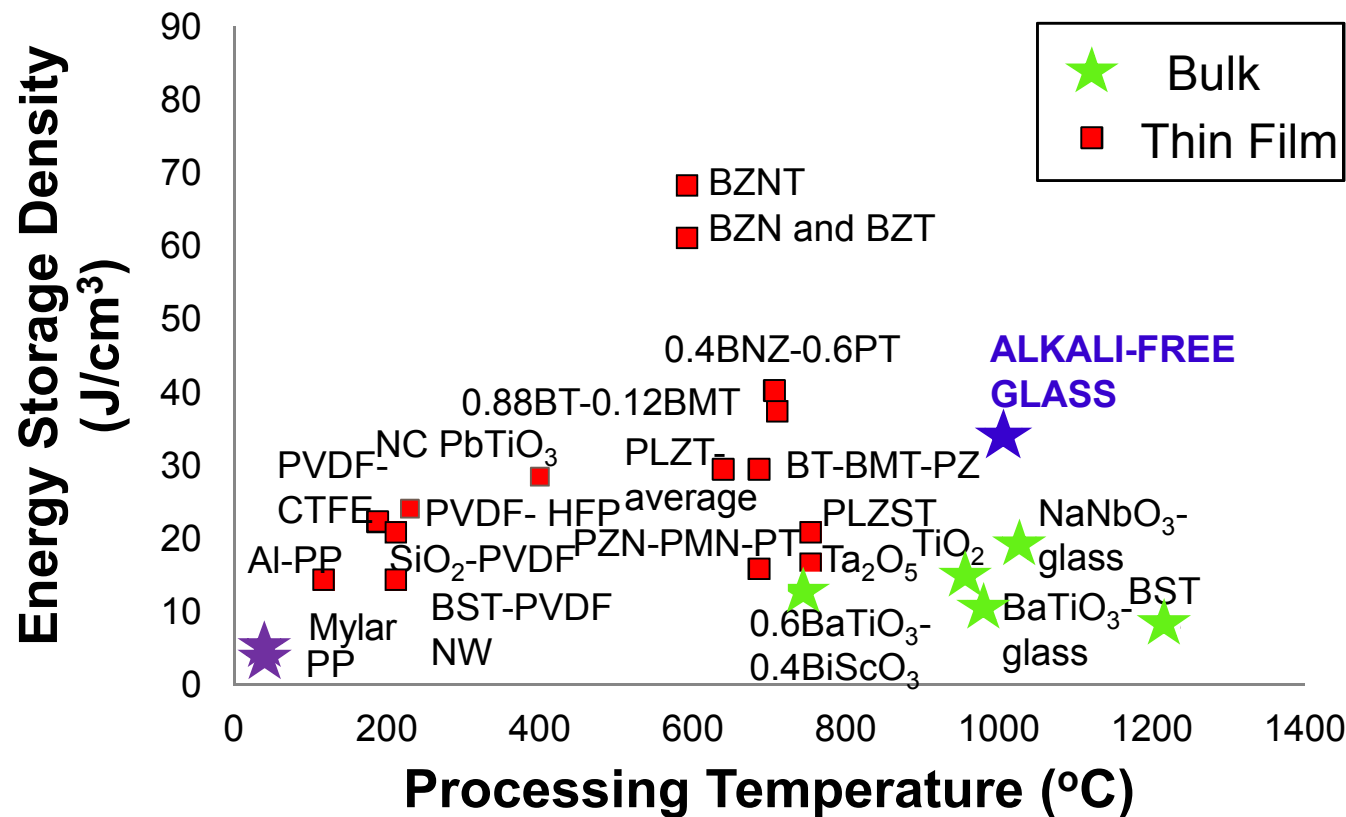
$$U = \frac{\text{Energy}}{\text{Volume}} = \frac{1}{2} \epsilon_r \epsilon_0 E^2$$

Limited value to tuning  $\epsilon_r$



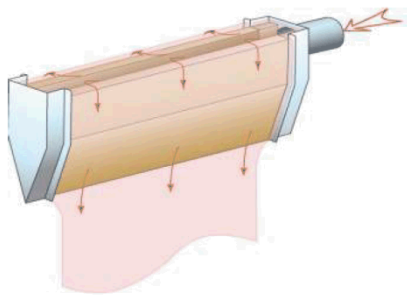
McPherson et al. IEEE TED, 2003

# Comparison of Capacitive Energy Storage Materials



- Alkali-free glass competitive with many emerging materials
- May have an advantage in manufacturing
- Packaged capacitors: 0.3-3 J/cc (depending on voltage rating)
  - Can we make 1 kV, 100 nF capacitors?

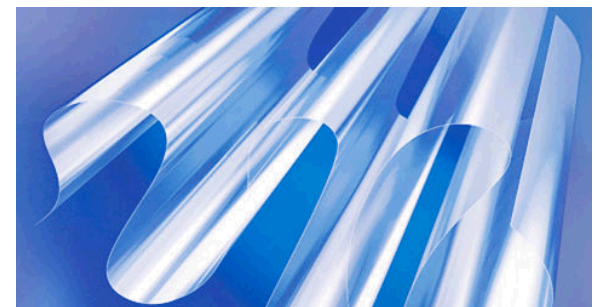
# Alkali-Free Glasses



- “Overflow drawn down process”

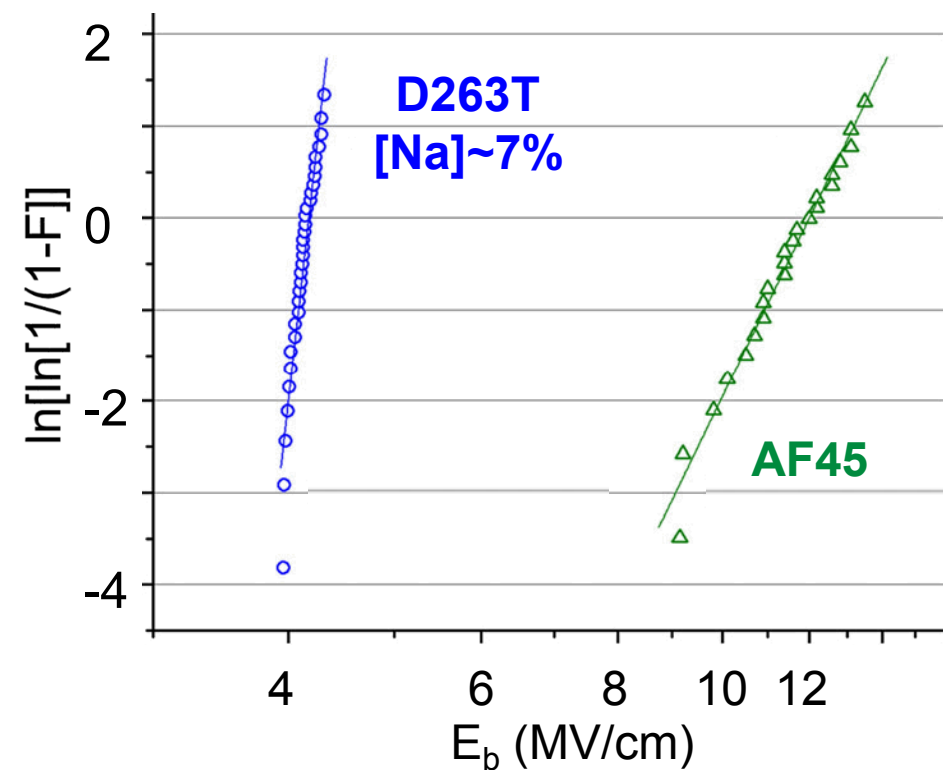


Parameter	Value
Density (g/cm <sup>3</sup> )	2.3-2.5
Young's Modulus (GPa)	73-75
$\epsilon_r$	5-6
$\tan \delta$	0.001
$\rho$ ( $\Omega \cdot \text{cm}$ @ 250 °C)	$>10^{12}$
Strain Point	650-700 °C

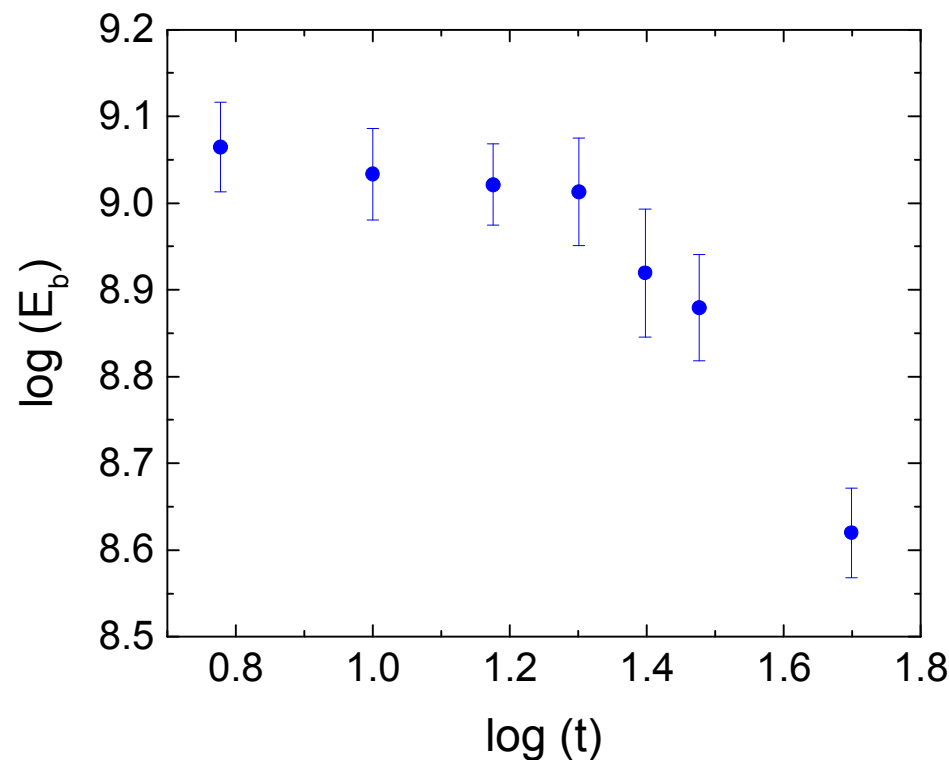


- Sold by many vendors world wide
- Boro-alumino-silicate glass category
- Sold in thicknesses  $\sim 100 - 200 \mu\text{m}$
- $[\text{Na}] < 350 \text{ ppm}$  (typical)

# Breakdown Strength of Alkali-free Glass

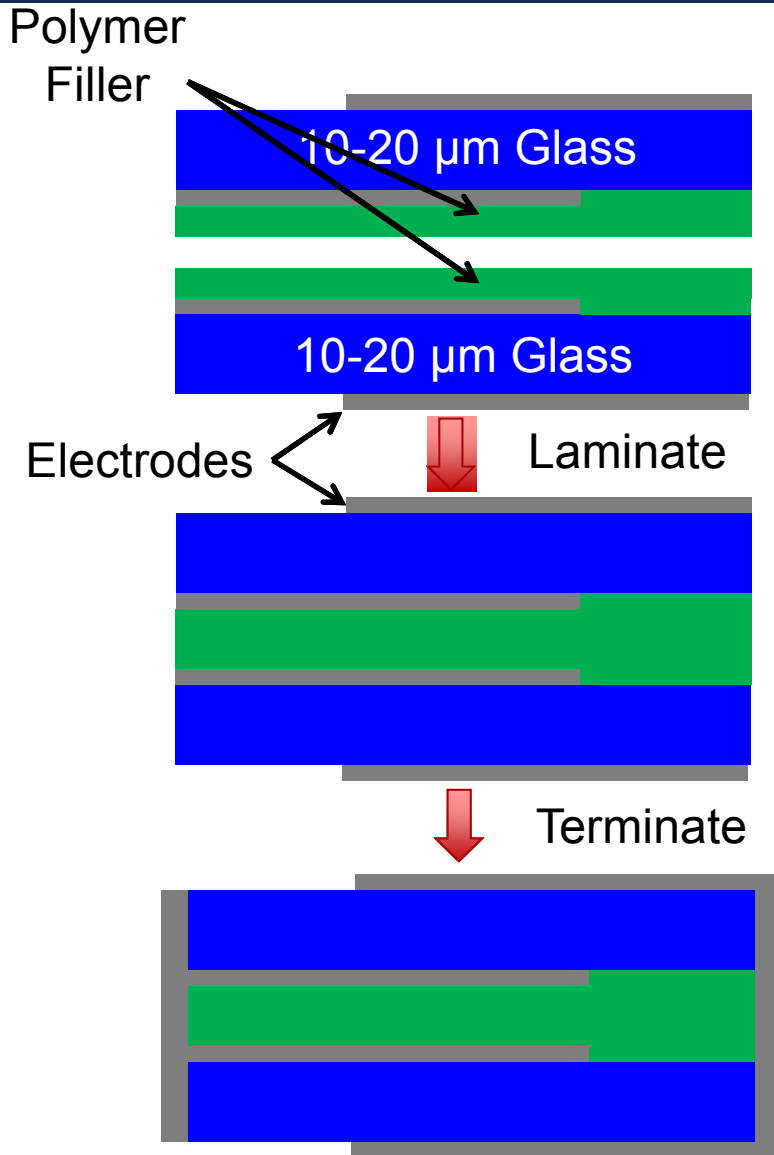


Parameter	D263T	AF45
$t$ ( $\mu\text{m}$ )	30	19
$E_b$ (MV/cm)	4.2	12
$\beta$	47.9	10.7

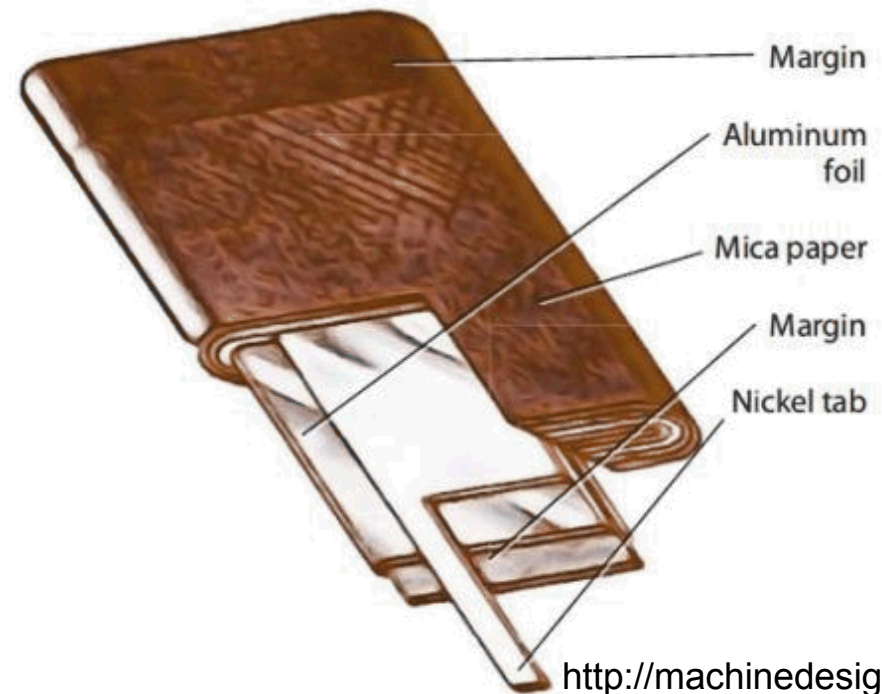


- Schott AF45 glass etched via sonicating in HF
- For  $t < 20 \mu\text{m}$ ,  $E_b > 10 \text{ MV/cm}$
- $U_{\text{dielectric}} \sim 35 \text{ J/cc}$

# Approach for Bonding Metallized Glass



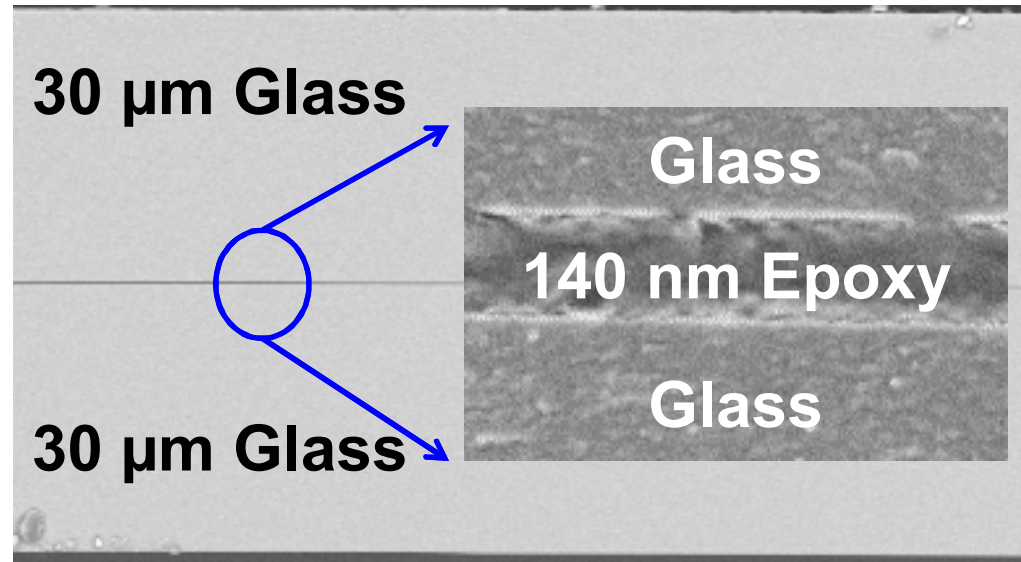
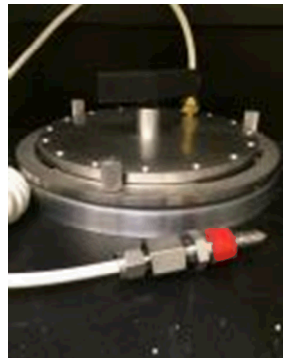
- Thinned glass is manufactured "layer by layer"
- Need approach to physically and electrically connect multiple layers to form final capacitor
- Bonding Approach – laminate using high breakdown strength Epon 828/Jeffamine T403
  - Analogies to epoxy impregnated mica-paper capacitor construction



<http://machinedesign.com>

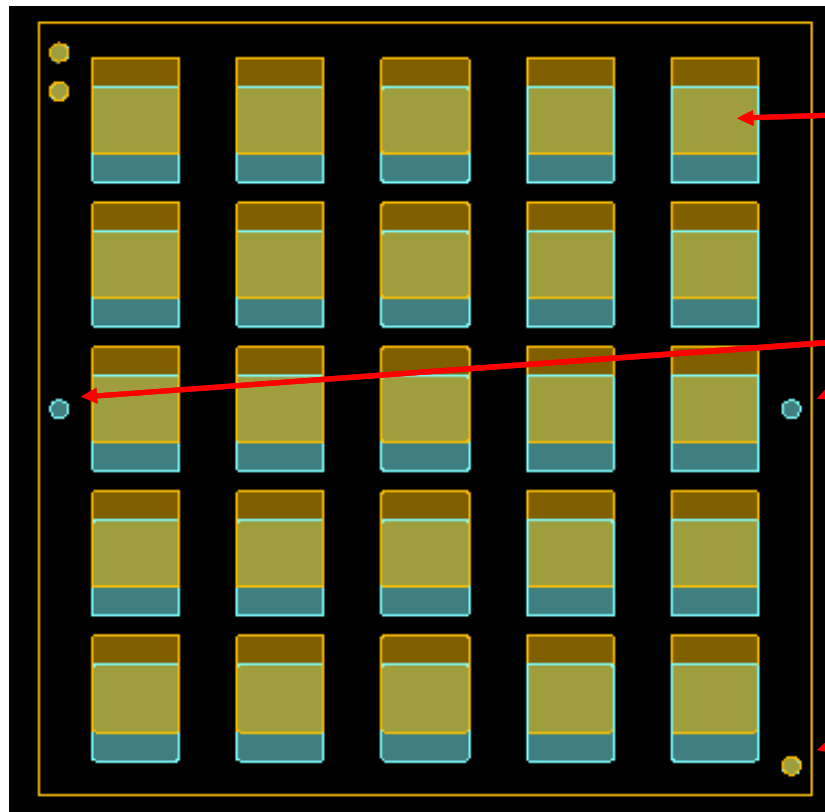


# Demonstration of Bonding under Pressure



- Epoxy cured under uniaxial pressure of 4.2 kPa
- Uniform thickness of  $138 \pm 4$  nm across 1" test piece

# Aligning Electrodes



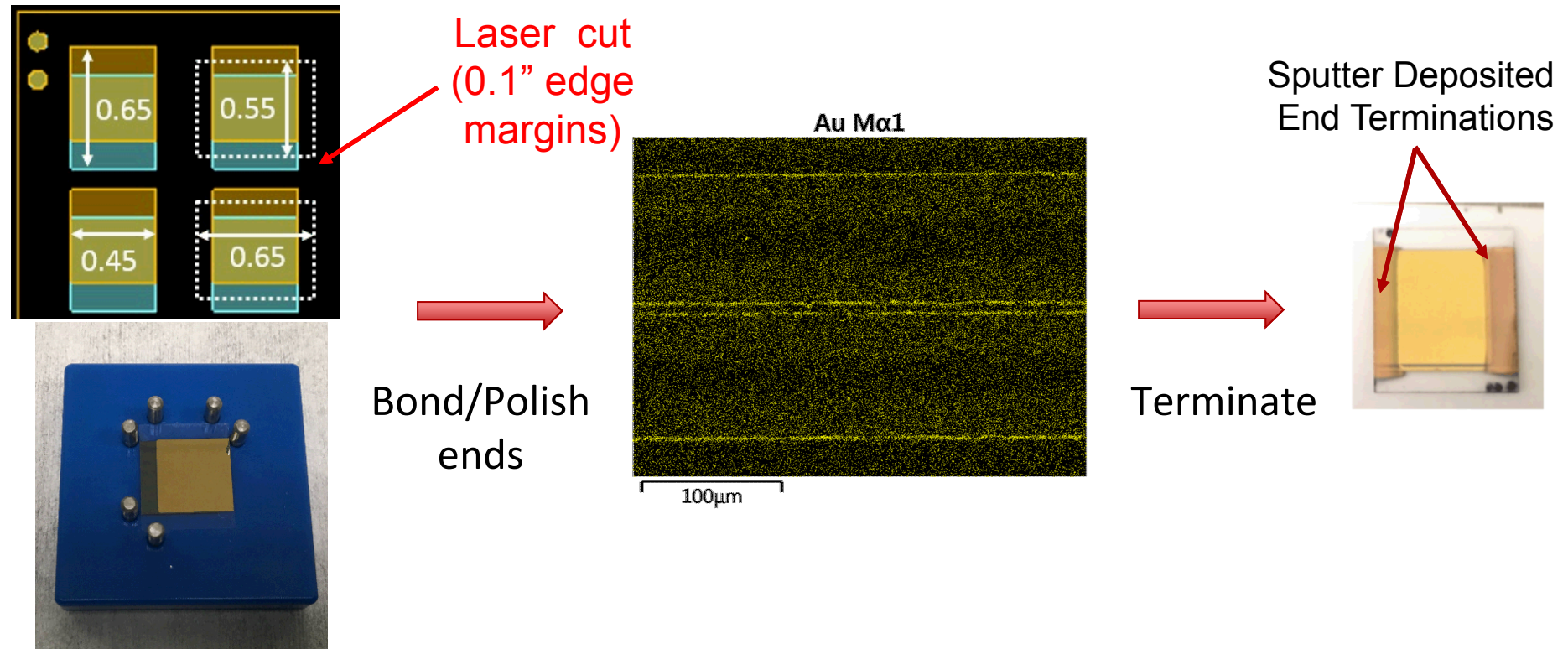
Metallized Electrodes  
0.45" x 0.5" (0.15" offset)  
Active area 0.35"x0.45"

Reference Marks for Laser Machining  
Cut out for Alignment Fixture

Reference Marks for Aligning  
Shadow Mask  
for Top/Bottom Electrodes

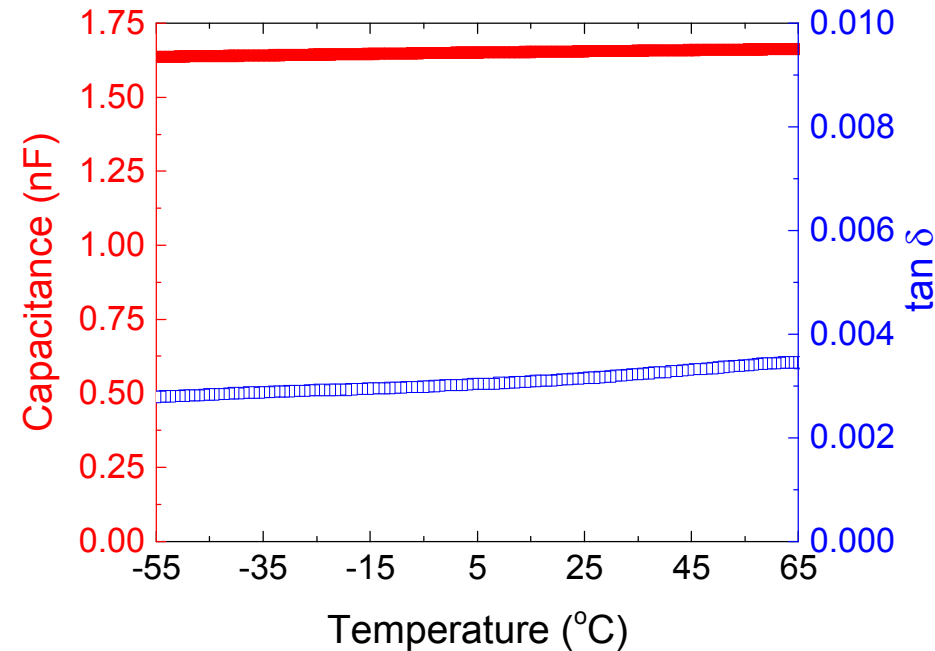
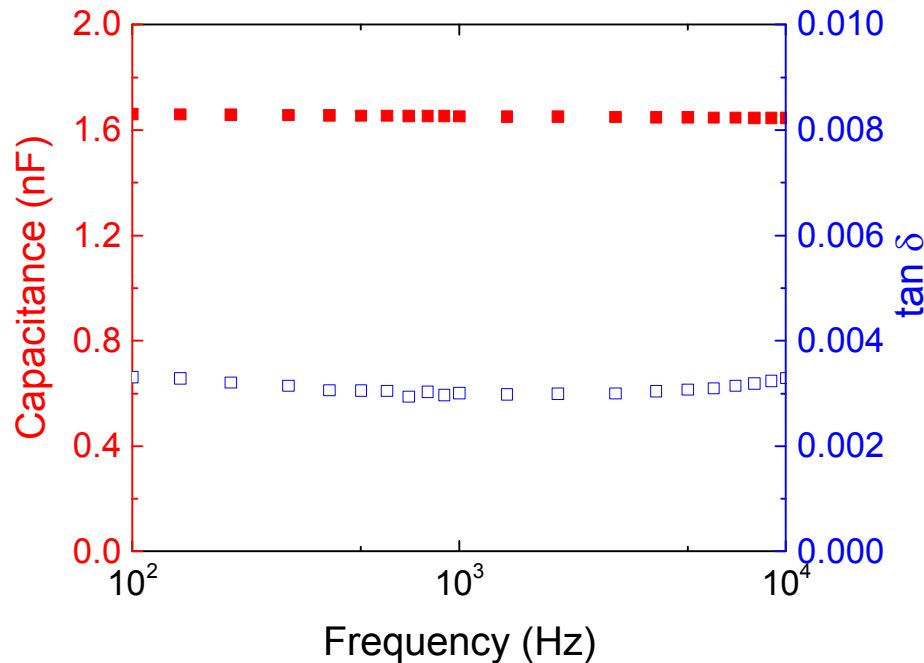
- Alignment between subsequent layers is crucial for minimizing dead volume/maximizing energy density
- Shadow mask designed for metallizing glass – providing alignment markings
- Glass sheets subsequently cut

# Fabricating Capacitor using 3D Printed Fixture



- Active area of capacitor is 0.35"x0.45" (Total area 0.55"x0.65")
  - 160 pF per layer for 30 µm thick glass
  - Have process to thin to 10 µm (500 pF per layer)
  - Long term goal is 5 µm (990 pF per layer)

# Electrical Properties of 10 layer Capacitor



- 1.65 nF (indicates all layers electrically active)
- Shows better than  $\pm 1\%$  stability over  $-55\text{ }^{\circ}\text{C}$  to  $+65\text{ }^{\circ}\text{C}$  (exceeds X4A classification)
- $R = 2.81 \times 10^{13}\ \Omega$  @  $65\text{ }^{\circ}\text{C}$  ( $RC = 4.7 \times 10^4\text{ s}$ )
- $V_{\text{Breakdown}} = 9.74\text{ kV}$  (dielectric – 2.8 J/cc, package 0.4 J/cc)  $\Rightarrow$  need more samples for Weibull analysis
- 150x increase in area vs. Lee report:  $\frac{E_{b1}}{E_{b2}} = \left( \frac{\text{Area}_2}{\text{Area}_1} \right)^{1/\beta} \Rightarrow V_B(\text{MLGC}) \sim 12\text{ kV}$



- Successfully demonstrate route to make solid state multi-layer glass capacitors
- Temperature range of operation determined by bonding material (Epon 828/Jeffamine T403 -55 °C to + 65 °C)
- Breakdown data suggests area scaling of Weibull data applies
  - Need to proof test glass sheets prior to incorporation to maximize energy density
  - Working on “mass production” to demonstrate Weibull statistics on packaged capacitors