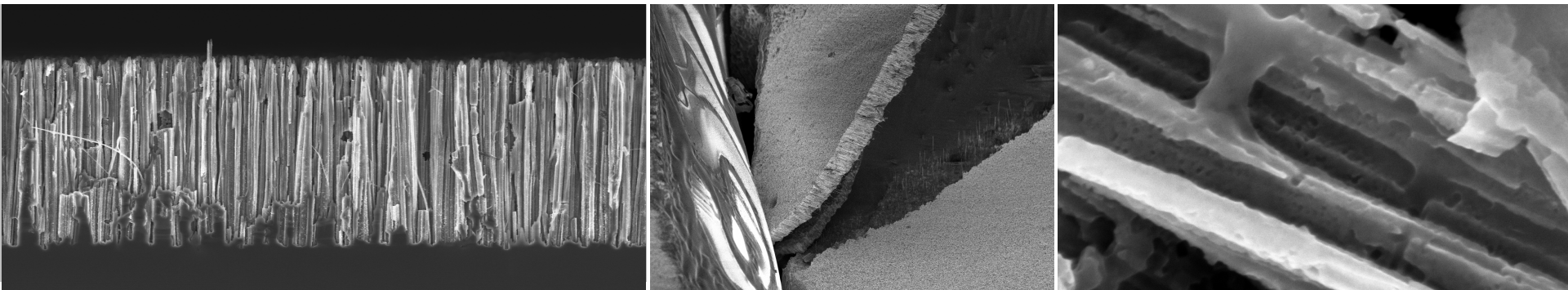


Exceptional service in the national interest



Chemically Etched Silicon Nanowires As Anodes for Lithium-Ion Batteries

Hannah Height

Overview

- Motivation and background
- Silicon nanowire manufacturing methods
- Experimental setup and procedure
- Results and SEM imaging
- Conclusions
- Future work

Motivation

- Aerospace applications
 - Lighter battery packs



- Electric vehicles
 - Longer driving distances



- Personal Electronics



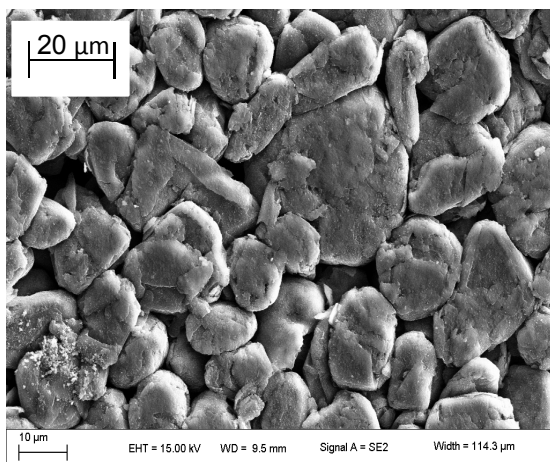
Current Technology

■ Current technology

Graphite

$\text{LiC}_6 \rightarrow 372 \text{ mAh/g}$

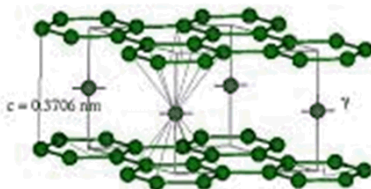
Expansion: $\sim 10\%$



Courtesy of: Chelsea Snyder

● Carbon

● Lithium



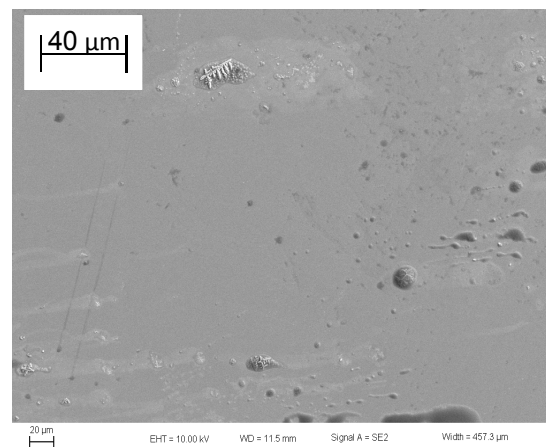
Kganyago, K. R.; Ngoepe, P. E. *Phys. Rev. B* **2003**, 68, 16.

■ Future technology

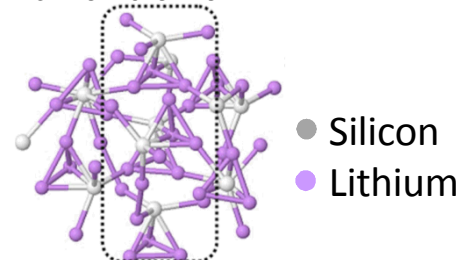
Silicon

$\text{Li}_{15}\text{Si}_4 \rightarrow 3,578 \text{ mAh/g}$

Expansion: $\sim 270\%$

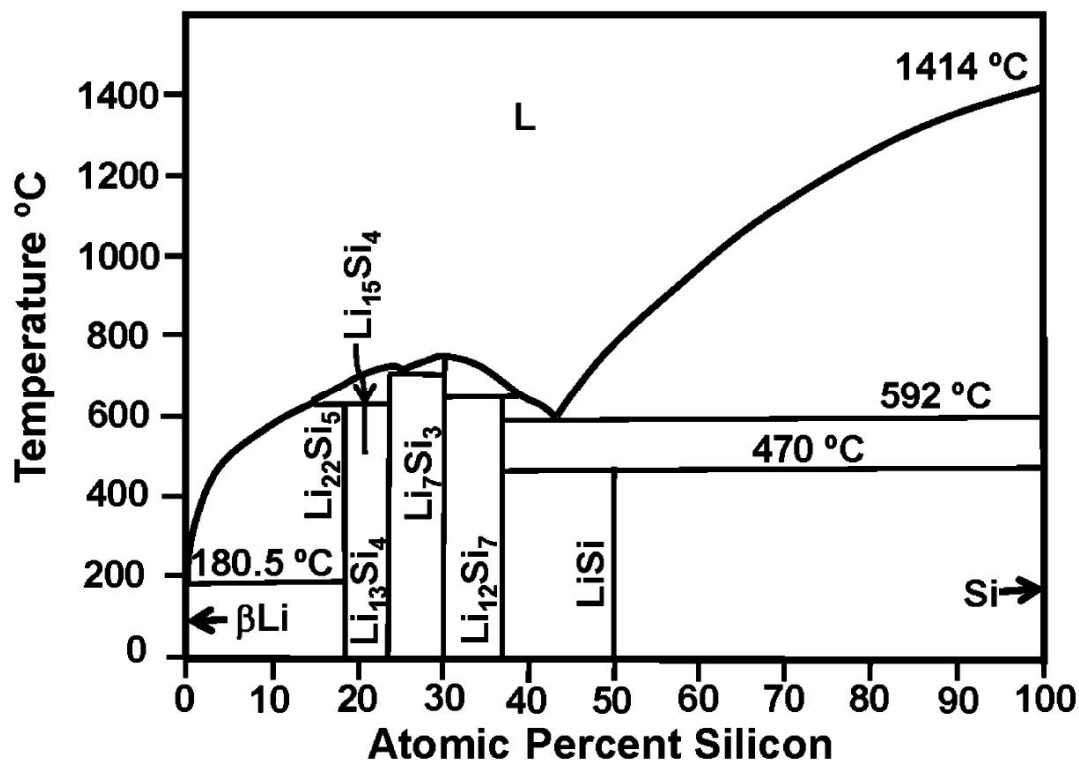


Courtesy of: Bonnie Mckenzie

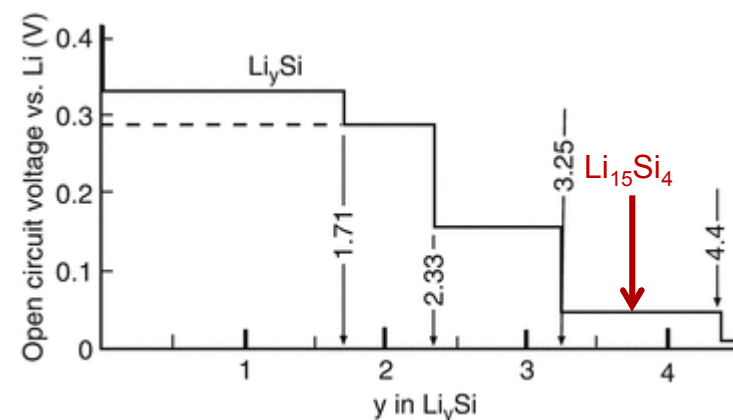


Kubota, Y.; Escano, M. C. S.; Nakanishi, H.; Kasai, H. *J. Appl. Phys.* **2007**, 102, 6.

Lithiation of Silicon

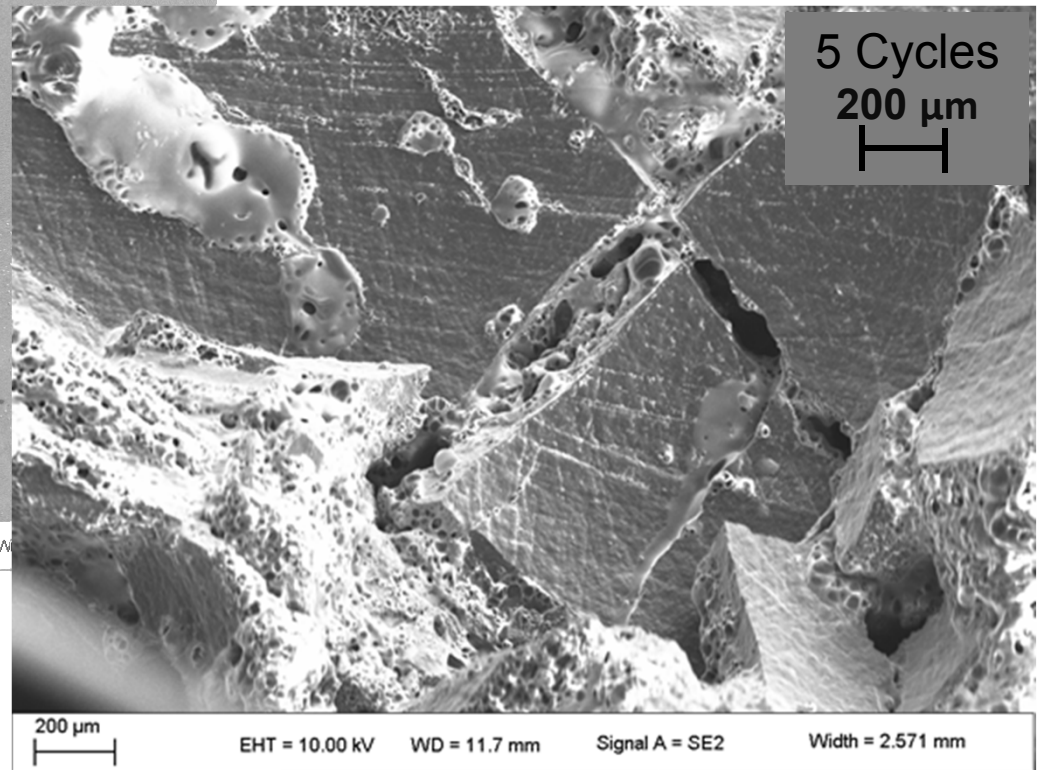
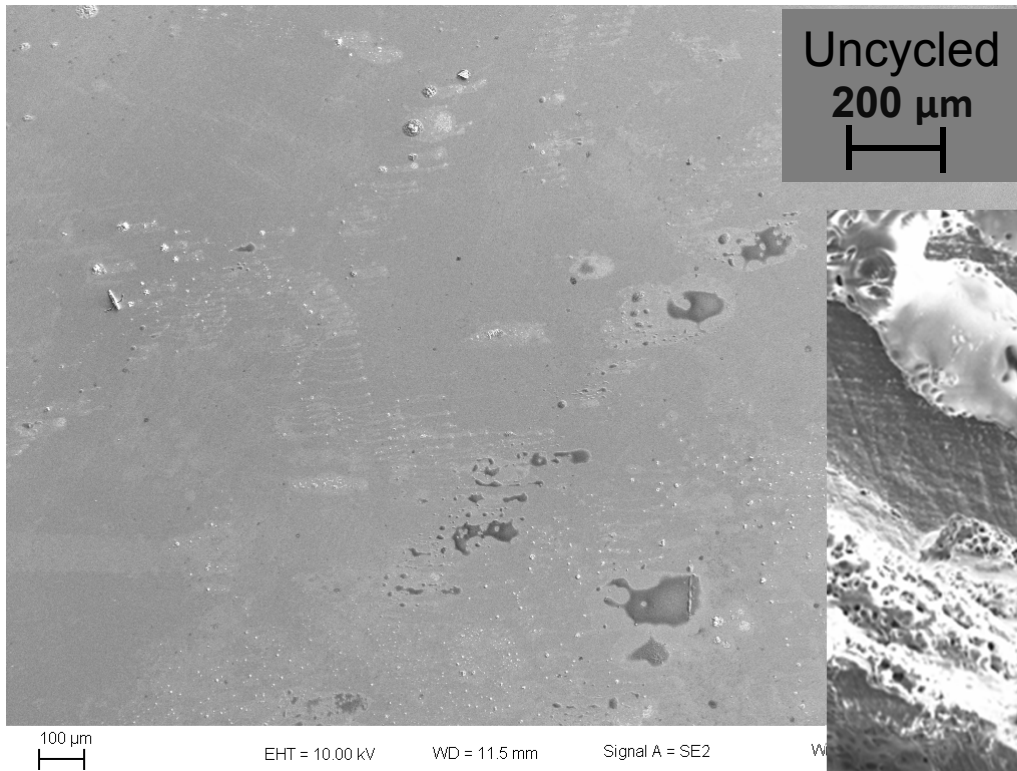


Phase	Capacity (mAh/g)
LiSi	954
Li ₇ Si ₃	2226
Li ₁₂ Si ₇	1636
Li ₁₃ Si ₄	3101
Li ₁₅ Si ₄	3578
Li ₂₂ Si ₅	4198



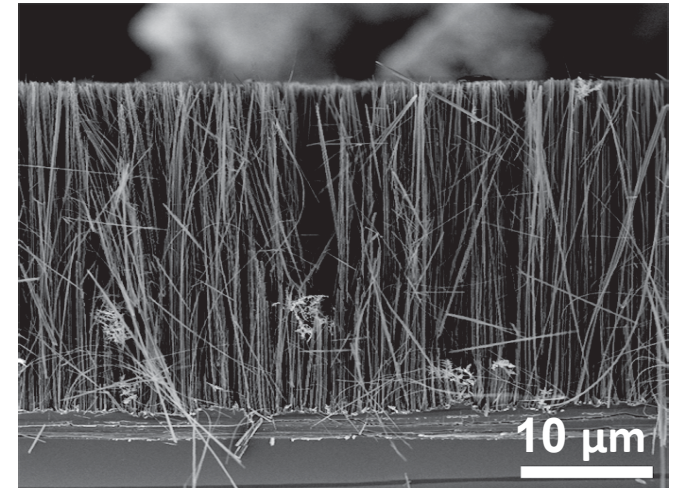
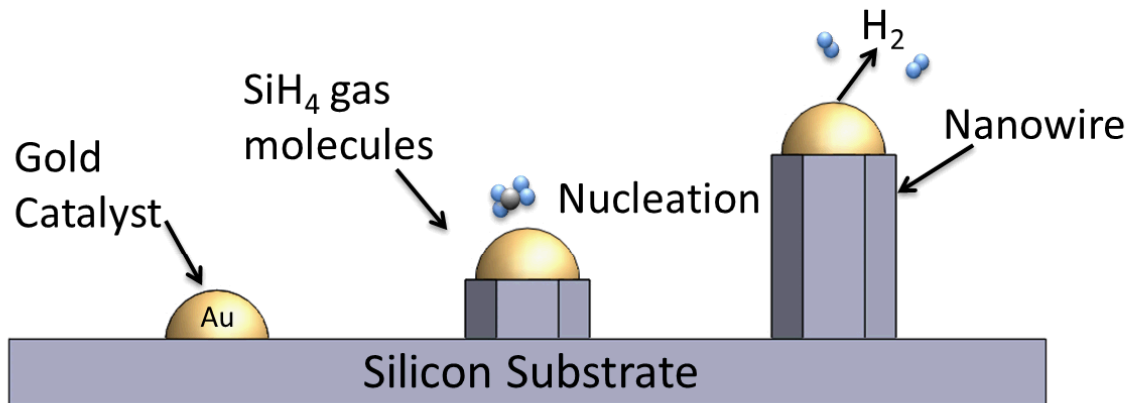
- Li₁₅Si₄ forms at room temperature and low voltages
- Crystallization from amorphous phase causes fracturing

Challenges

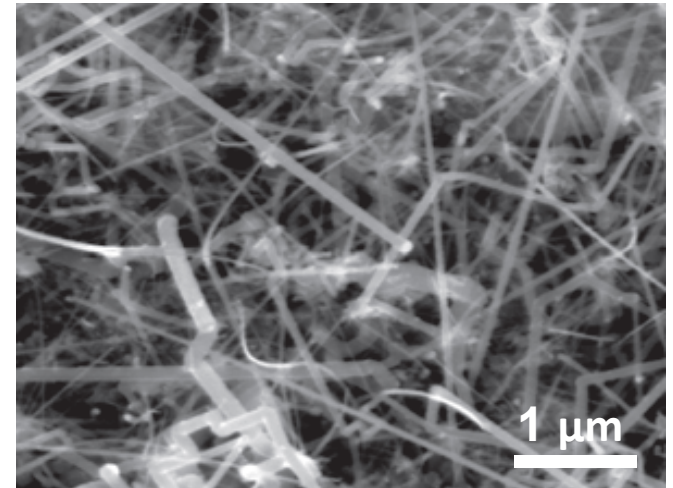


- Silicon expands ~270% upon lithiation
- Rapid loss of capacity

Vapor-Liquid-Solid Mechanism

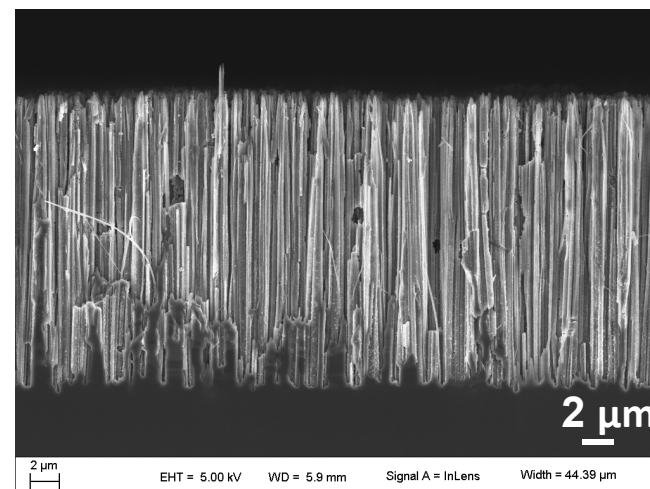
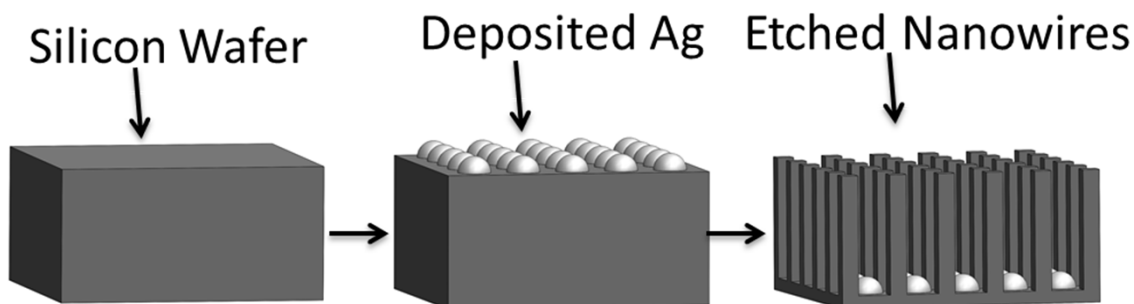


Hochbaum, A. I.; Chen, R.; Delgado, R. D.; Liang, W.; Garnett, E. C.; Najarian, M.; Majumdar, A.; Yang, P. *Nature* **2008**, 451, 163.

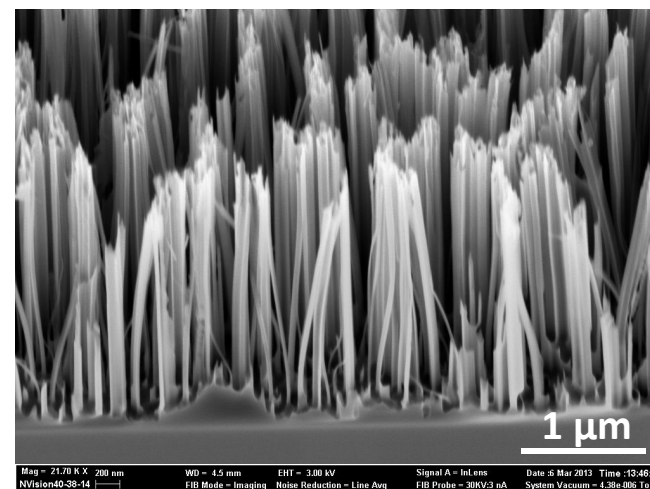


Chan, C. K.; Peng, H. L.; Liu, G.; McIlwrath, K.; Zhang, X. F.; Huggins, R. A.; Cui, Y. *Nat. Nanotechnol.* **2008**, 3, 31.

Metal Assisted Chemical Etched



Courtesy of: Bonnie Mckenzie



Smith, Z. R.; Smith, R. L.; Collins, S. D. *Electrochim. Acta* **2013**, 92, 139.

Manufacturing Comparison

Vapor-Liquid-Solid (VLS) Mechanism

Advantages:	Disadvantages:
1. Single crystalline Cheaper than vapor-liquid-solid method Dislocation free	High temperature Clean room
2. More easily implemented into production facility	Expensive Catalyst

Metal Assisted Chemical Etching

Advantages:	Disadvantages:
3. More mechanically sound than grown nanowires Single crystalline Fast process Room temperature Affordable catalyst	Uses hydrofluoric acid Silicon wafer needed Loss of material

Experimental Set up

- Half-cell configuration

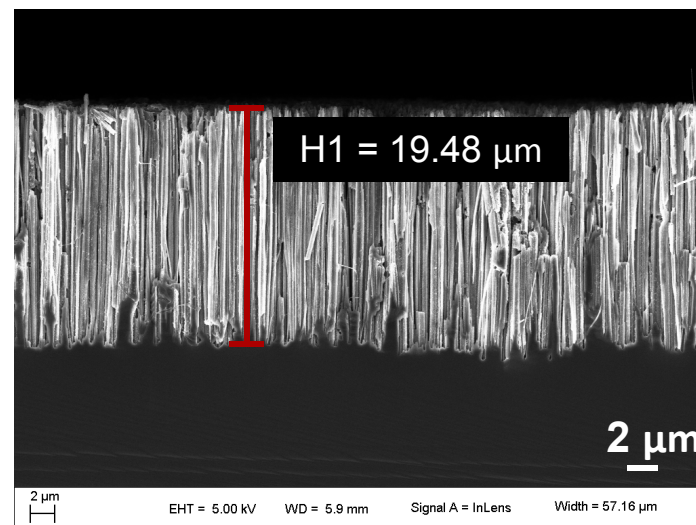
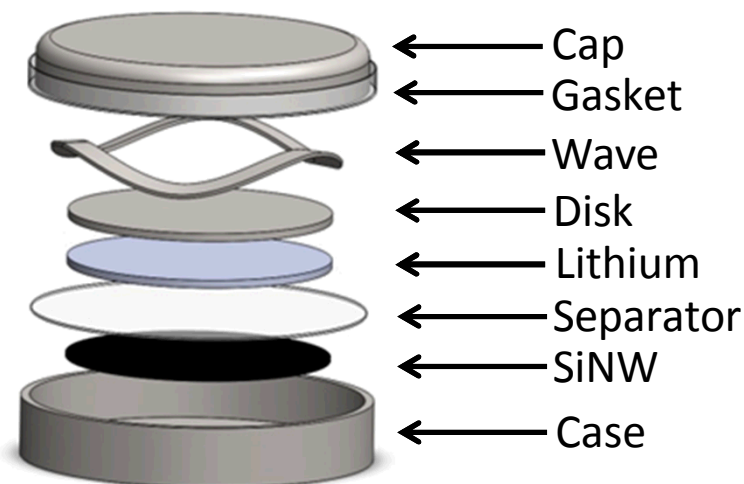
- Cyclic voltammetry
- Chrono-voltammetry (charge/discharge)
- SEM/EDS

Working Electrode: Silicon

Counter Electrode: Lithium metal

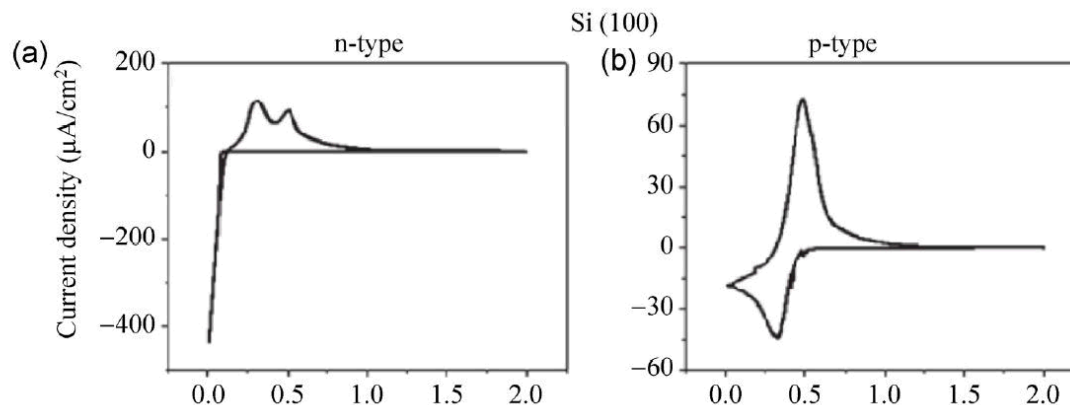
Electrolyte: 3:7 EC:EMC + 1.2M LiPF₆

- Capacity determined by mass of nanowires (30% surface coverage)
- Actual capacity used for long cycling
- All tests performed in controlled environment (~25°C)
- No carbon conductive phase in bulk



Courtesy of: Bonnie Mckenzie

Cyclic Voltammetry Results

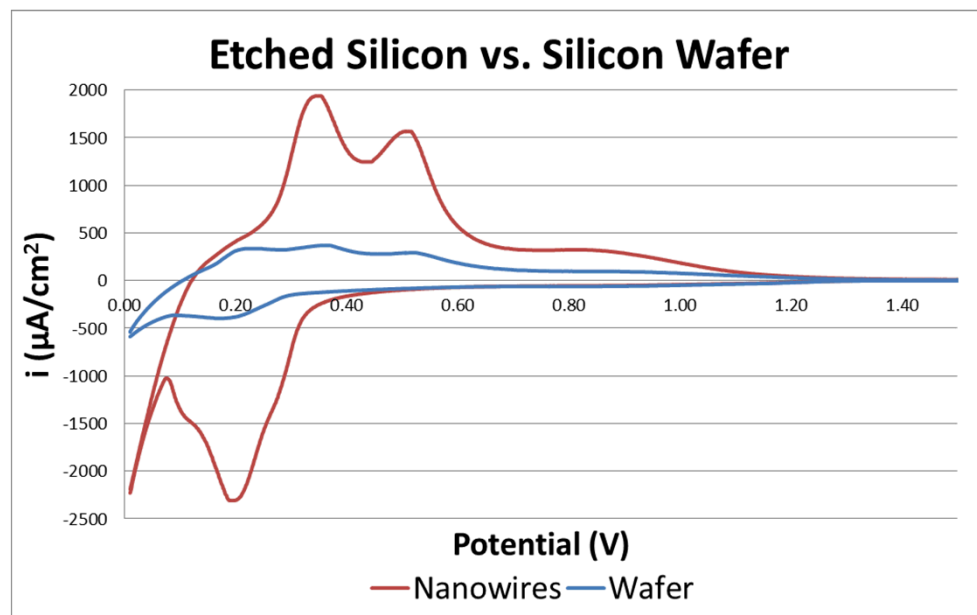


Long, B. R.; Chan, M. K. Y.; Greeley, J. P.; Gewirth, A. A. *J. Phys. Chem. C* **2011**, *115*, 18916.

Working Electrodes: n-type P doped Si
p-type B doped Si

Counter Electrode: Li ribbon

Scan Rate: 200 $\mu\text{V}/\text{s}$



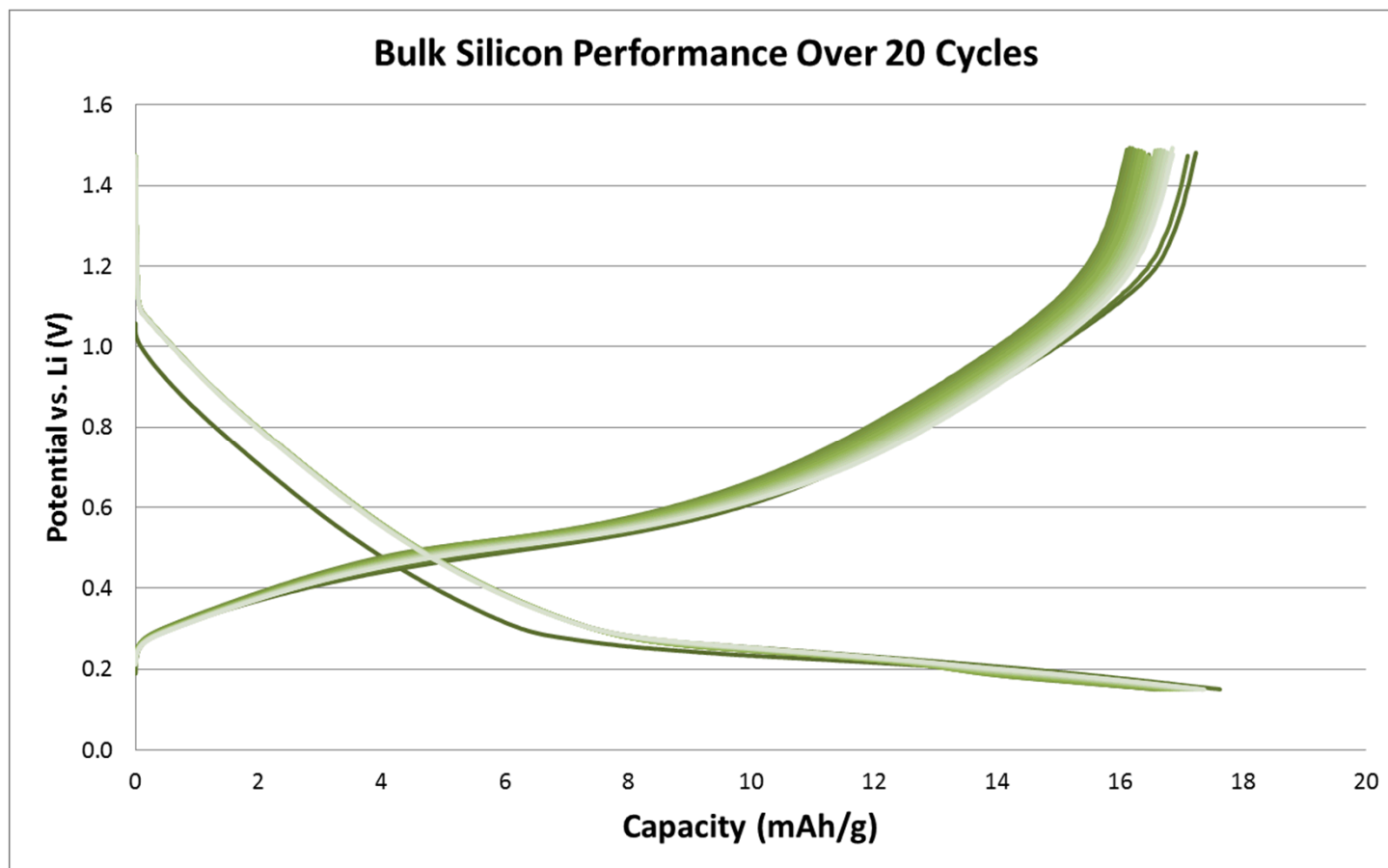
Working Electrodes: p-type P doped Si
Silicon Nanowires

Counter Electrode: Li metal

Scan Rate: 25 $\mu\text{V}/\text{s}$

Temperature: 25°C

Chrono-voltammetry Results



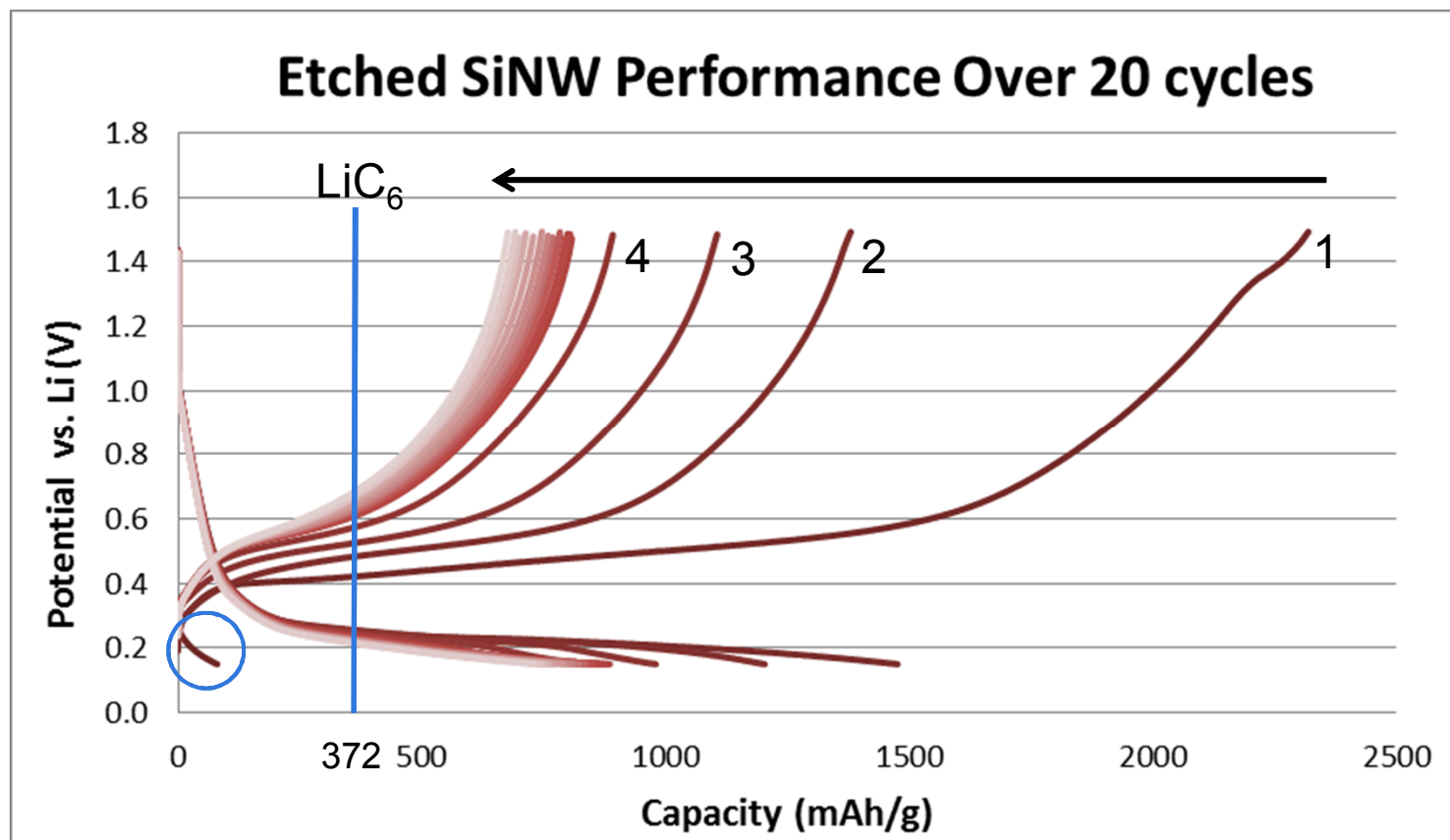
Temperature: 25°C

Rate: C/10

Mass: 0.0965 g

- Only penetrating 0.48% of wafer
- ~3.15 μm out of 650 μm

Chrono-voltammetry Results



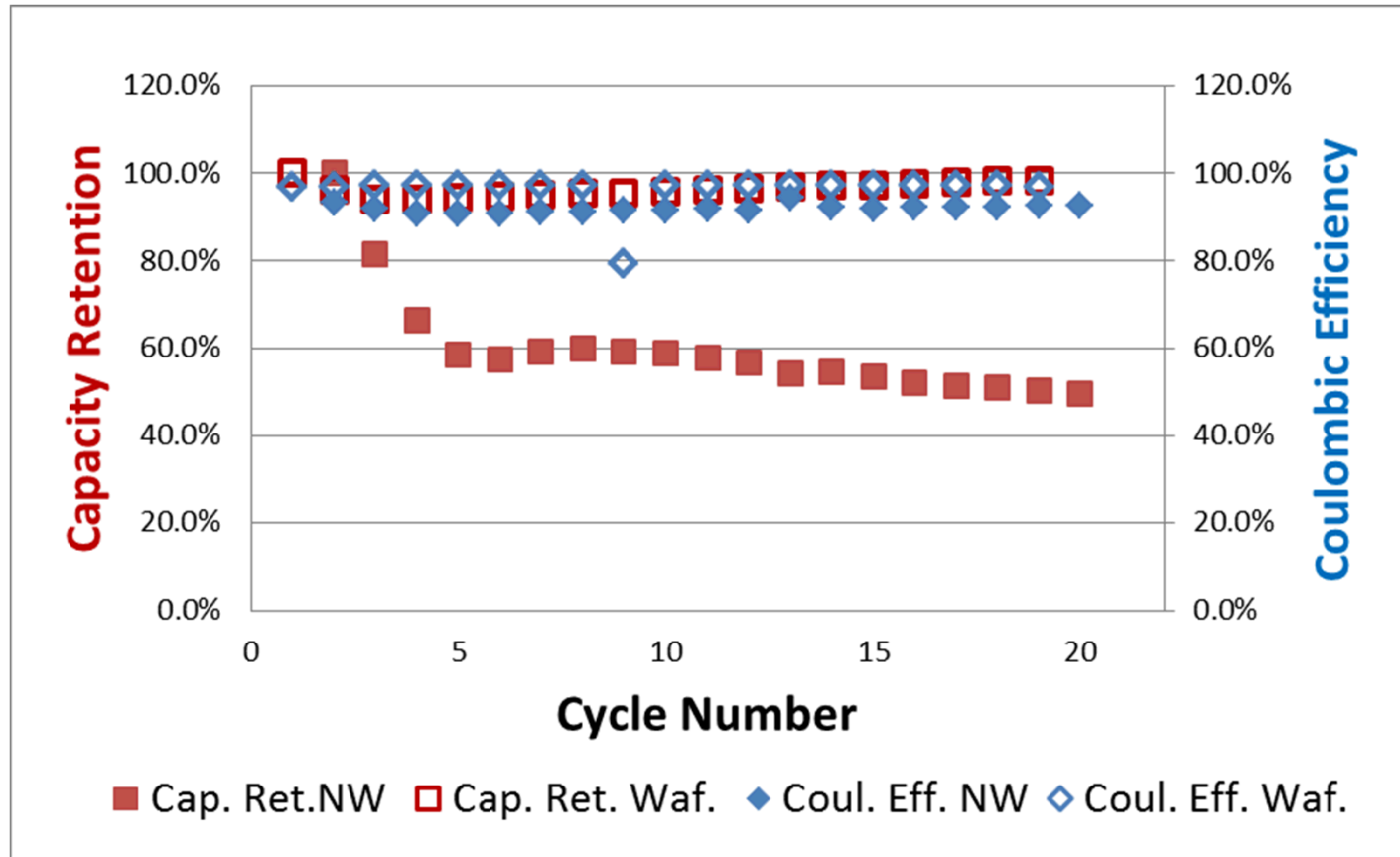
Temperature: 25°C

Rate: C/10

Mass: 0.00469 g

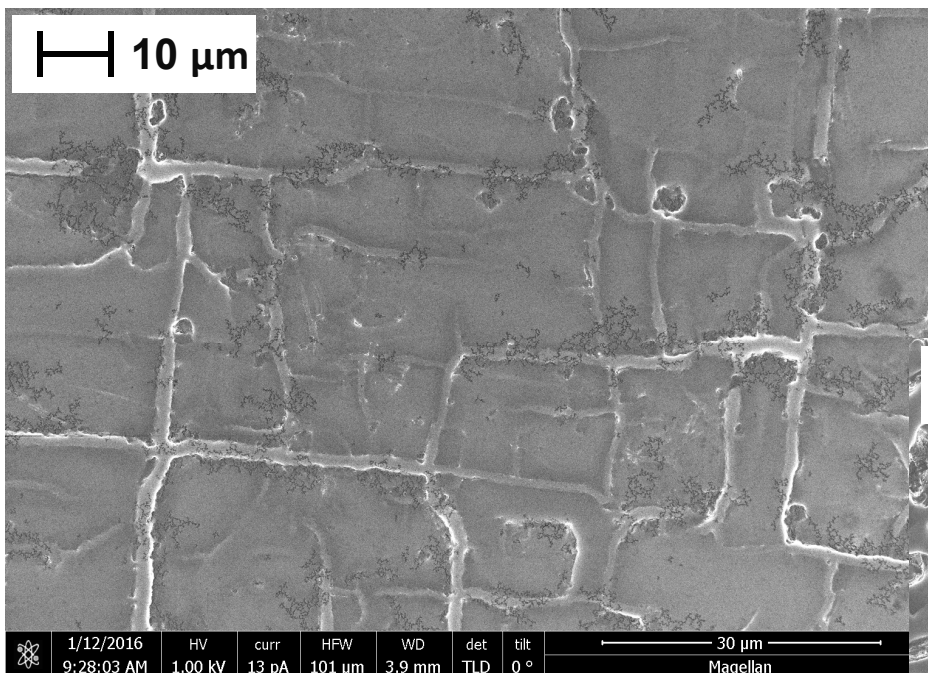
- Discharge = lithiation of silicon
- After 3 formation cycles at C/100
- C rate based on data from formation cycles

Capacity Retention and Coulombic Efficiency

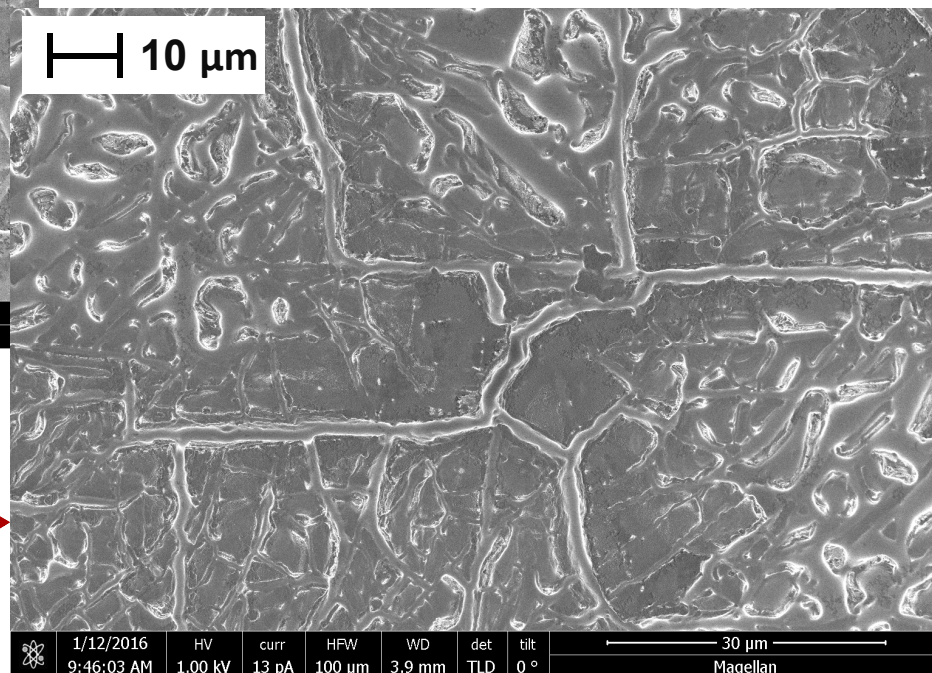


- Low coulombic efficiency for nanowires (consistent with literature)

Bulk Silicon Wafers

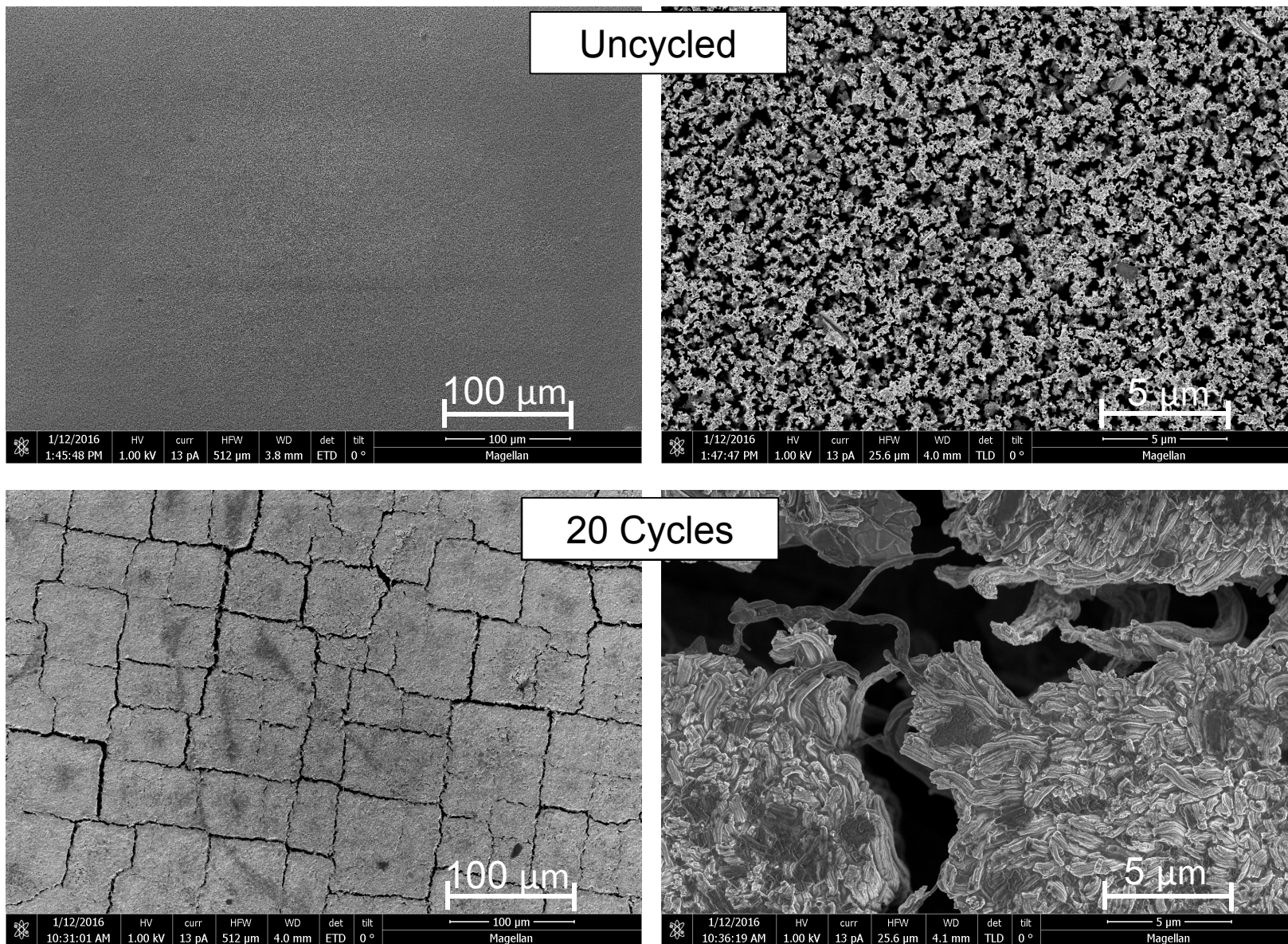


Bulk Silicon Wafer
10 cycles at C/10
Room Temperature (~25°C)
3:7 EC:EMC + 1.2M LiPF₆

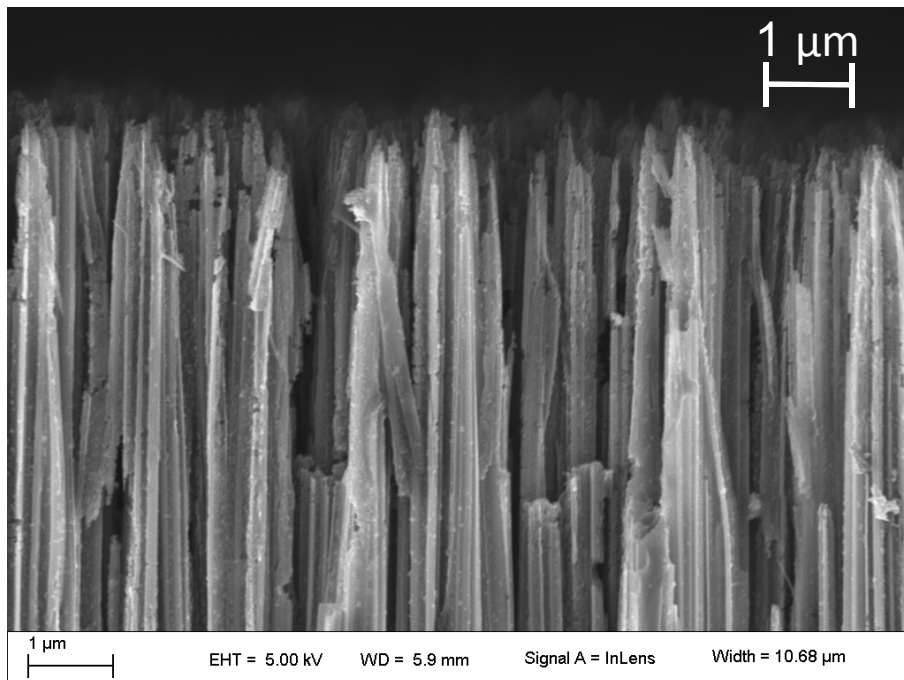


Bulk Silicon Wafer
20 cycles at C/10
Room Temperature (~25°C)
3:7 EC:EMC + 1.2M LiPF₆

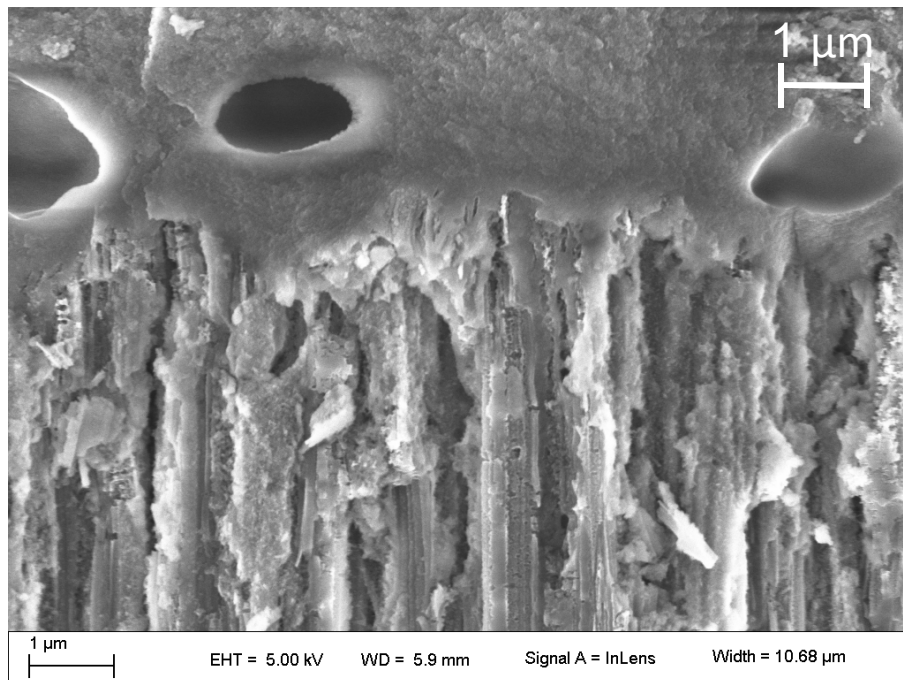
Etched Silicon



SEM Imaging of Silicon Nanowires



Uncycled



Cycled

Conditions:
RT = 25°C
WE: Si NW
CE: Li metal

Conclusions

- Etched silicon nanowires are capable of achieving capacities twice as large as graphite anodes even after 20 cycles.
- Mechanically sound, stayed relatively intact during cycling
- Low coulombic efficiency
- 49.5% capacity retained after 20 cycles

Future Work

- Identify capacity dependence on etch time and concentration
- Run silicon nanowire anodes in full cell
- Run anodes for more cycles (50+ cycles)

Acknowledgements

Christopher Apblett

Jonathan Coleman

Scott D. Collins

Kyle Fenton

Brian Perdue

Bonnie Mckenzie

Chelsea Snyder

Richard Grant

Backup Slides

Silicon Nanowires		
Cycle	Coulombic Efficiency	Capacity Retention
1	-	-
2	93.5%	100.0%
3	91.9%	81.5%
4	91.0%	66.4%
5	90.7%	58.5%
6	90.9%	57.5%
7	91.2%	59.4%
8	91.2%	60.0%
9	91.8%	59.4%
10	91.6%	58.9%
11	91.8%	57.8%
12	91.7%	56.7%
13	94.4%	54.5%
14	92.2%	54.8%
15	92.1%	53.6%
16	92.4%	52.3%
17	92.3%	51.5%
18	92.4%	51.2%
19	92.7%	50.5%
20	92.6%	49.5%

Silicon Wafer		
Cycle	Coulombic Efficiency	Capacity Retention
1	-	-
2	97.0%	100.0%
3	97.1%	96.2%
4	97.3%	94.0%
5	97.3%	94.2%
6	97.3%	94.5%
7	97.2%	94.8%
8	97.3%	95.1%
9	97.2%	95.4%
10	79.4%	95.7%
11	97.2%	96.0%
12	97.2%	96.3%
13	97.2%	96.7%
14	97.2%	97.0%
15	97.2%	97.2%
16	97.3%	97.5%
17	97.2%	97.8%
18	97.2%	98.2%
19	97.2%	98.4%
20	97.1%	98.6%

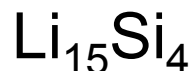
Theoretical Capacity Calculations

$$q = \frac{nF}{3600 \frac{C}{Ah} * M}$$

n = # of electrons

F = 96487 Coulombs/mol

M = 28.09 g/mol for Si



$$q = \frac{(15)(96487 \frac{C}{mol})}{(3600 \frac{C}{Ah}) * (4 * 28.09 \frac{g}{mol})} = 3.578 \frac{Ah}{g}$$