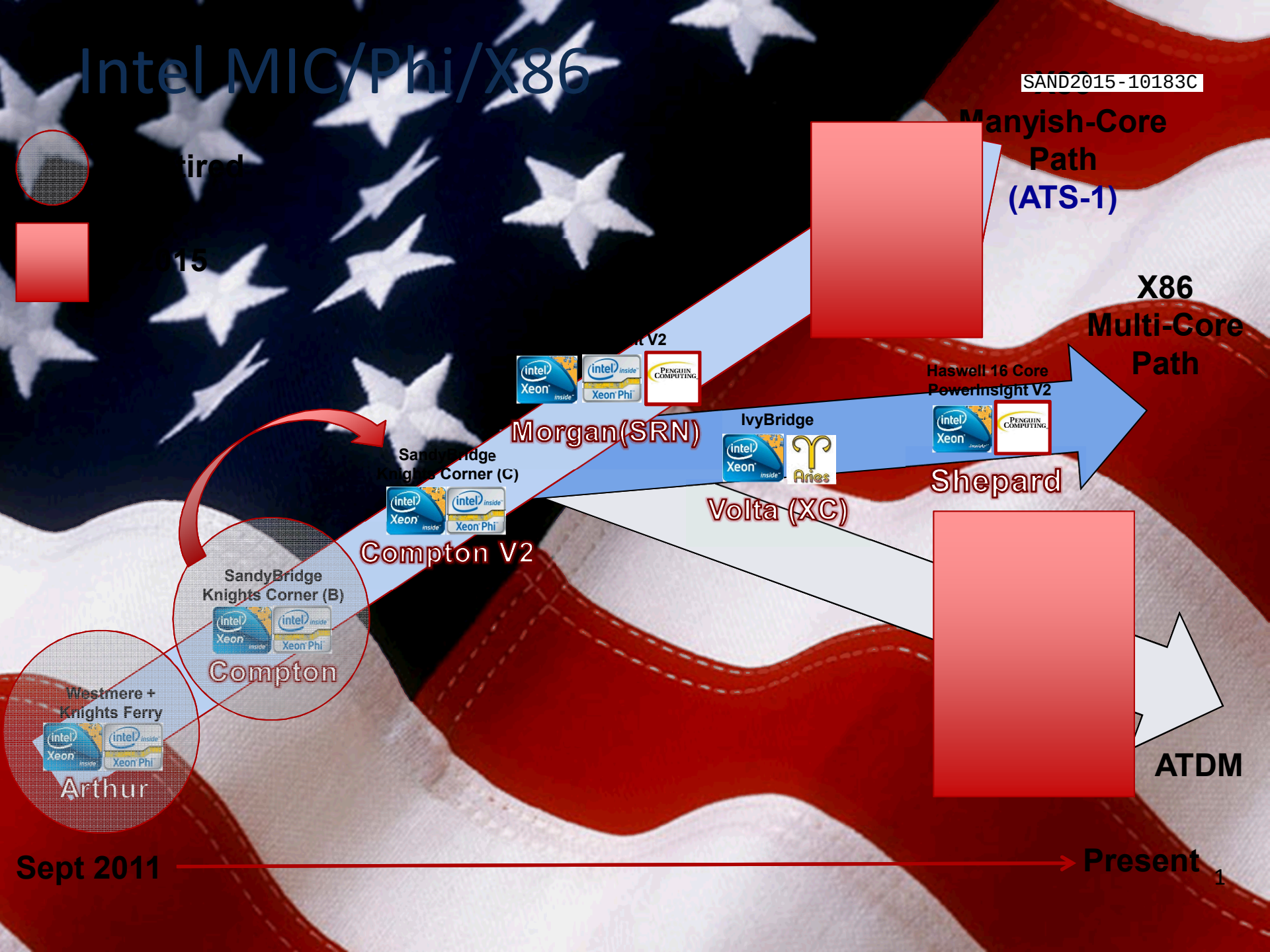
Intel MIC/Phi/X86

SAND2015-10183C



Shiller and Hansen (ATDM)

- Robert Shiller, Lars Hansen (and Barack Obama) 2013 Nobel Prize in Economics
- 2 systems
  - 1 Open Network
  - 1 Restricted Network
- 4 Dual socket Haswell nodes
  - 16 core sockets
  - 512 GB DDR4 2133 MHz
- 2 with 1 each NVIDIA K80 GPU
- Mellanox FDR IB
- 120 TB IO Appliance
  - Optimized for IOPs

Robert Shiller

Lars Hansen

NVIDIA
IBM/NVIDIA

ator

Tightly Integrated Accelerator (ATS-2)

SandyBridge + K20 + K40 + K80

Shannon V3

SandyBridge + Kepler K20

Shannon

Interlagos + Kepler K20X

Curie (XK7) V2

Power 8

ESP

er 7 + FPGA

Watson

ATDM

Interlagos + Fermi 2090x

Curie (XK6)

Sept 2011 → Present

White and Ride

White (OHPC)

Helen White

- First women in America to earn [illegible] (Creek)
- Tuletta (1 head, 9 compute)
  - 2x 10 core P8 processors
    - 3.42 GHz
    - 16 Channels
    - DDR3 @ 1600 MHz
  - K40 GPUs (3 none, [illegible])
- Firestone (8 compute)
  - 2x 8 core P8 processors
    - 3.625 GHz
    - 8 Channels
    - DDR4
  - K80 GPUs (8 with 2)
    - Currently PCI
  - CAPI
    - Coherent Accelerated Processor Interface

ide (SRN)

Sally Ride

- First American women in space (1983)
- Tuletta (1 head, 4 compute)
  - 2x 10 core P8 processors
    - 3.42 GHz
    - 16 Channels (check?)
    - DDR3 @ 1600 MHz
  - K40 GPUs (1 none, 4 with 2)
- Firestone (1 head, 10 compute)
  - 2x 8 core P8 processors
    - 3.625 GHz
    - 8 Channels
    - DDR4
  - K80 GPUs (1 none, 10 with 2)
    - Currently PCI
  - CAPI
    - Coherent Accelerated Processor Interface

Firestone is first of HPC targeted line from IBM