

# Resistive Memory for Space Applications

**D. R. Hughart, J. L. Pacheco, A. J. Lohn, P. R. Mickel, E. Bielejec,  
G. Vizkelethy, B. L. Doyle, S. L. Wolfley, P. E. Dodd,  
M. R. Shaneyfelt, M. L. McLain, and M. J. Marinella**

***Sandia National Laboratories***

***This work was supported Sandia's Laboratory Directed Research and  
Development***



Sandia National Laboratories is a multi-program laboratory managed and operated by Sandia Corporation,  
a wholly owned subsidiary of Lockheed Martin Corporation, for the U.S. Department of Energy's  
National Nuclear Security Administration under contract DE-AC04-94AL85000.



# Outline

- **Introduction**
- **Ionization and Displacement Damage**
  - **Separate and identify mechanisms**
- **Isolate conductive filament**
  - **Microbeam**
  - **Nanoimplanter**



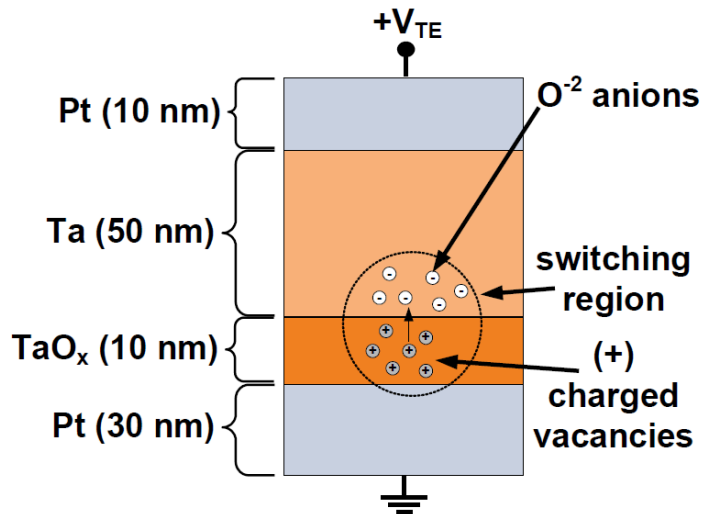
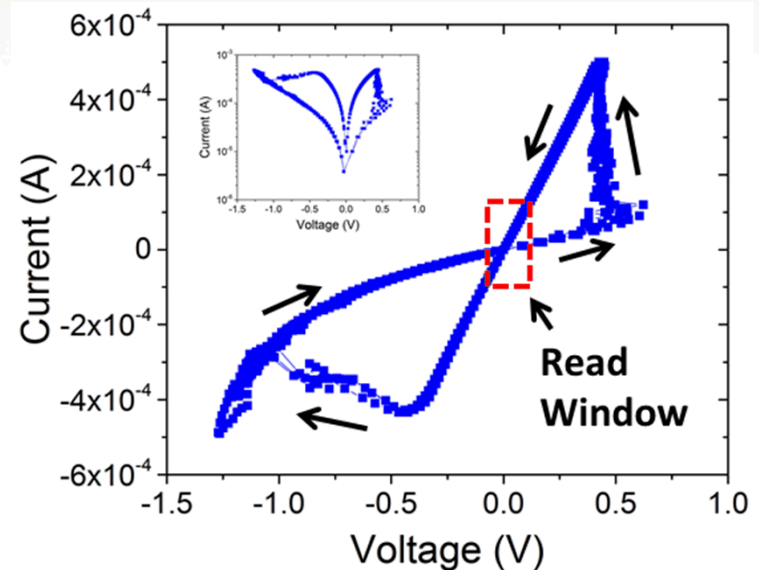
# Why Memristors?

- **Current nonvolatile memory (NVM) technologies like Flash are expected to be increasingly limited by scaling**
- **Resistive RAM (ReRAM) is a strong candidate to replace Flash with many promising performance metrics**
  - Scalability, endurance, speed, low power
  - Promising initial radiation studies
- **State of the art is rapidly advancing**
  - Panasonic has a commercial product
    - “Industry-leading low power operation (less than 4 $\mu$ W in low-speed active mode”
  - HP plans DIMM by 2016, later “The Machine”
    - “High-speed rewriting, 5 times faster than conventional flash-based MCU”



# Memristor I-V Characteristics

- **Resistive RAM (ReRAM)** stores state as a function of resistance
- **Applied current and voltage can change resistance state**
  - **Hysteresis loop**
- **Low voltages can read state**



- **Resistive switching**
  - **Oxygen vacancies**
- **TaO<sub>x</sub>**
  - **Oxygen anions**

# Displacement Damage vs. Ionization

- Different damage mechanisms investigated using various beams

Displacement  
damage

800 keV Ta

28 MeV Si

70 keV e<sup>-</sup>

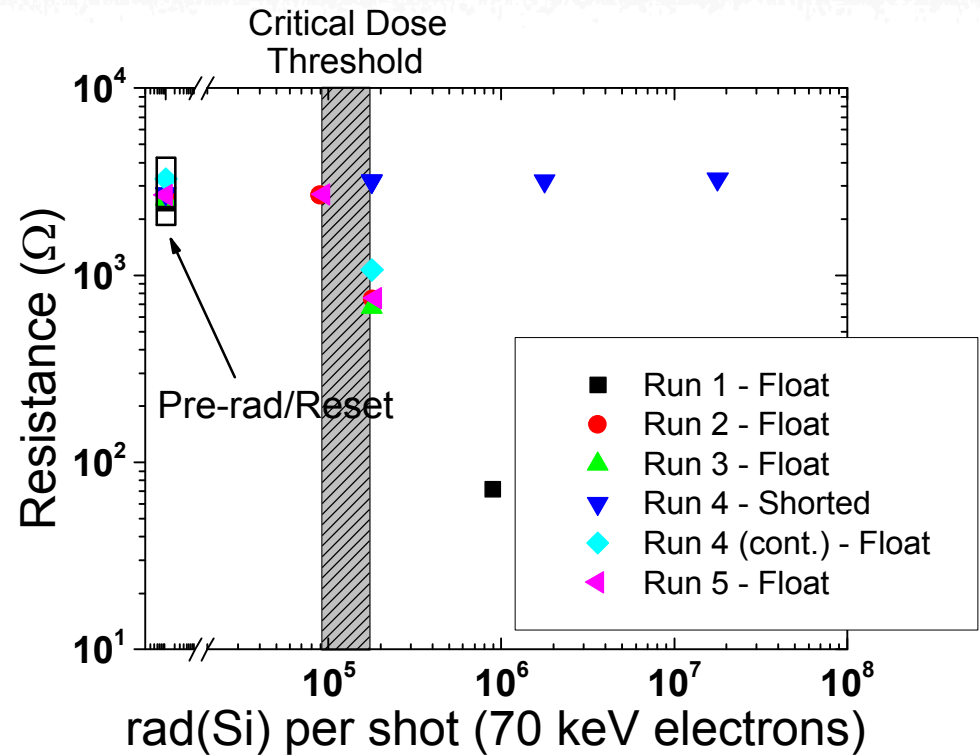
Ionization

- Different circuit configurations
  - Floating and shorted



# 70 keV Electrons (Ionization) - TaO<sub>x</sub>

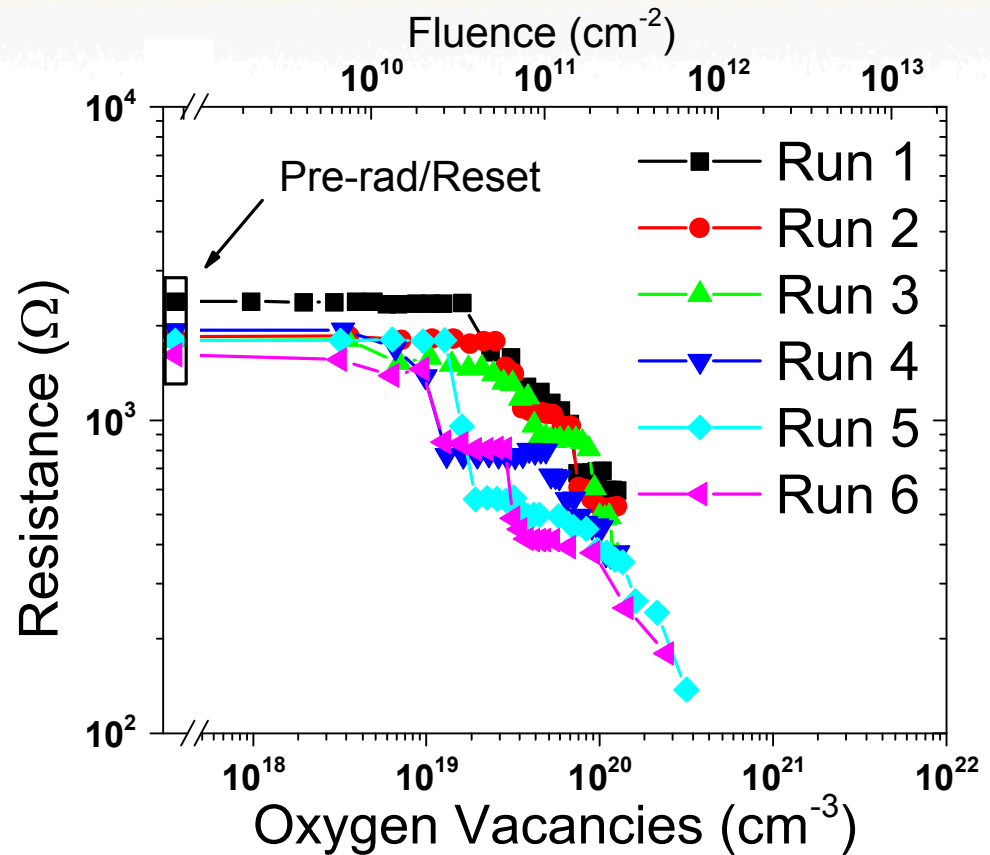
- **Ionization**
  - Threshold 100-200 krad(Si) per shot
- When pins are **shorted** no changes occur for doses up to 18 Mrad(Si)
- Resistance change varies with dose per shot





# Displacement Damage Effects

- **800 keV Ta**
  - Gradual resistance degradation
- **Creation of oxygen vacancies**
  - Fluence  $> 10^{10} \text{ cm}^{-2}$
  - $V_o > 10^{19} \text{ cm}^{-3}$
- **Fluence for a single device**
  - What size is the sensitive area?



# Ionization and Displacement Damage Initial Study Summary

- **Separate displacement damage and ionization effects**
  - Gradual resistance decrease above  $\sim 10^{19} \text{ cm}^{-3}$  oxygen vacancies
  - Abrupt and consistent changes at rad(Si) per shot threshold
- **Potential mitigation strategies**
  - Displacement damage: Repeated cycling may restore degraded  $R_{\text{OFF}}$
  - Ionization: Devices that aren't floating are less susceptible
  - If devices are floating often, apply small voltages periodically

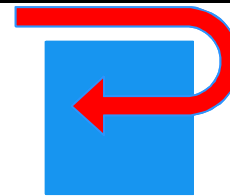




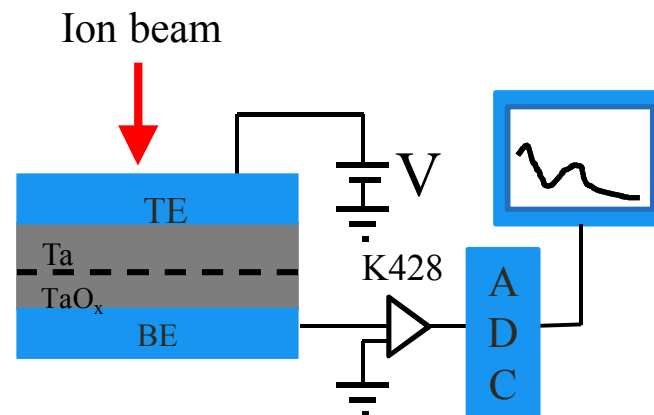
# Microbeam Raster Scan

- **Target smaller regions of the oxide to look for sensitive regions**
  - Spatial mapping of potential conduction channels
- **800 keV Si beam rastered across the device**
  - Targeted area  $\sim 1\ \mu\text{m} \times 2\ \mu\text{m}$  (device is  $10\ \mu\text{m} \times 10\ \mu\text{m}$ )
  - Resistance recorded each time beam moves (50 mV)

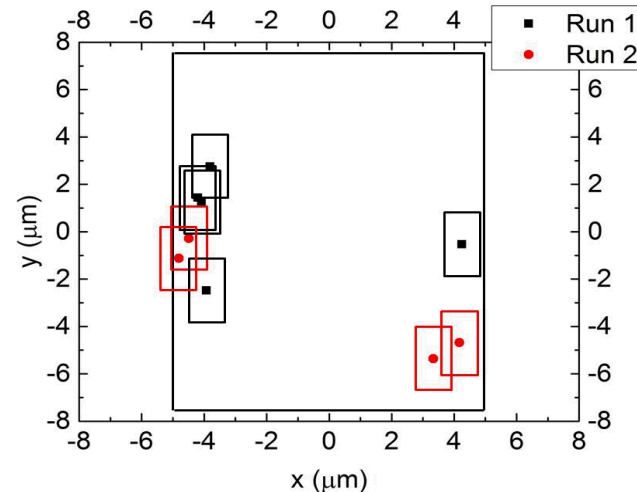
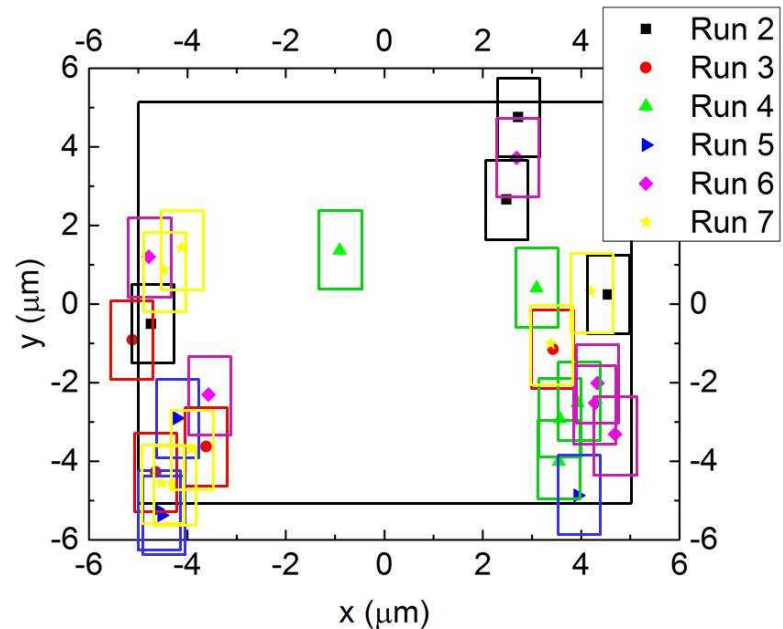
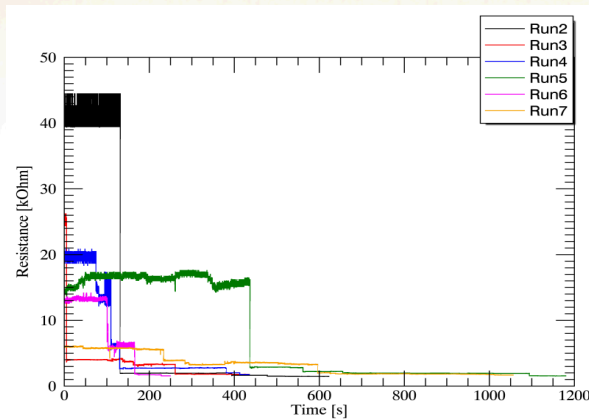
## Scan Ion Beam over device



## In-situ monitoring of resistance



# Spatial Mapping



- There are multiple distinct sensitive areas
- Changes in resistance tend to happen on the edges
  - More defects formed on perimeter during forming
  - Stronger electric field

Beam targeted center with no effect



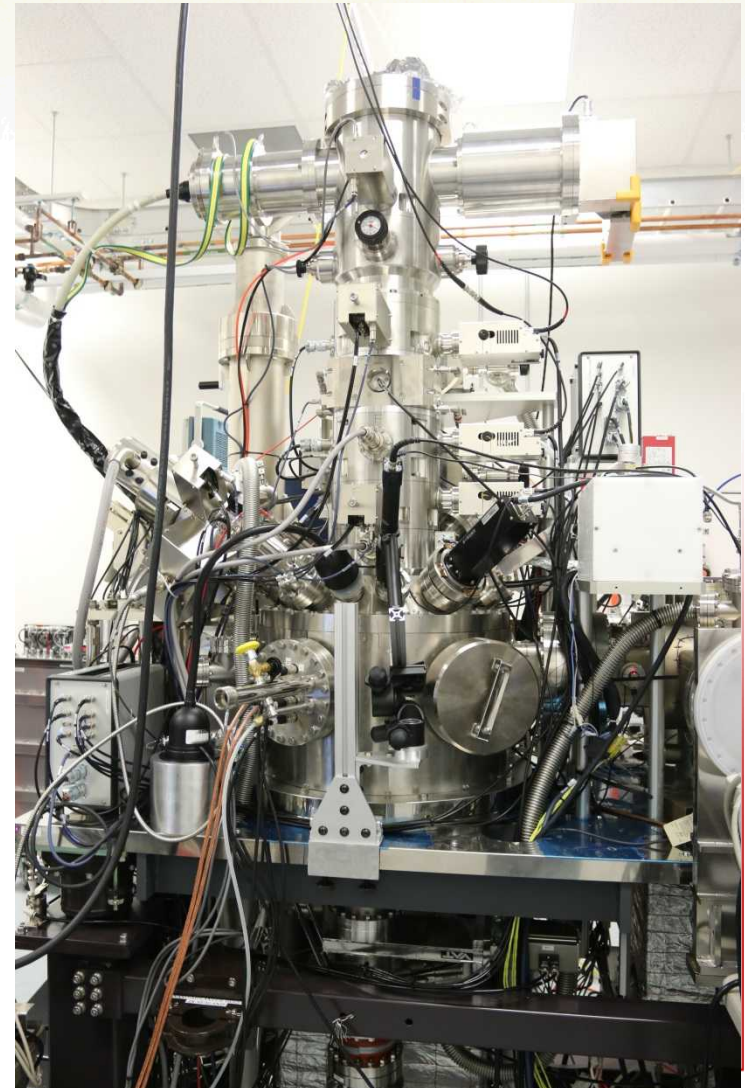
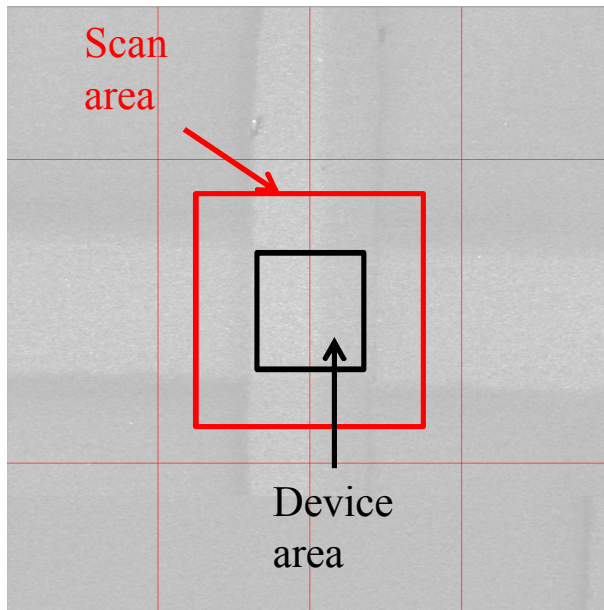
# Microbeam Summary

- **Multiple distinct sensitive regions**
- **Device is most sensitive on perimeter**
  - **Likely more defects on the perimeter**
  - **Appears insensitive in center of device**
  - **Sensitive area is not the entire oxide region**
- **Targeting likely not precise enough**
  - **Takes many scans to get changes**
  - **It sure would be great to have more precise targeting...**



# Nano-scale Ion Implantation

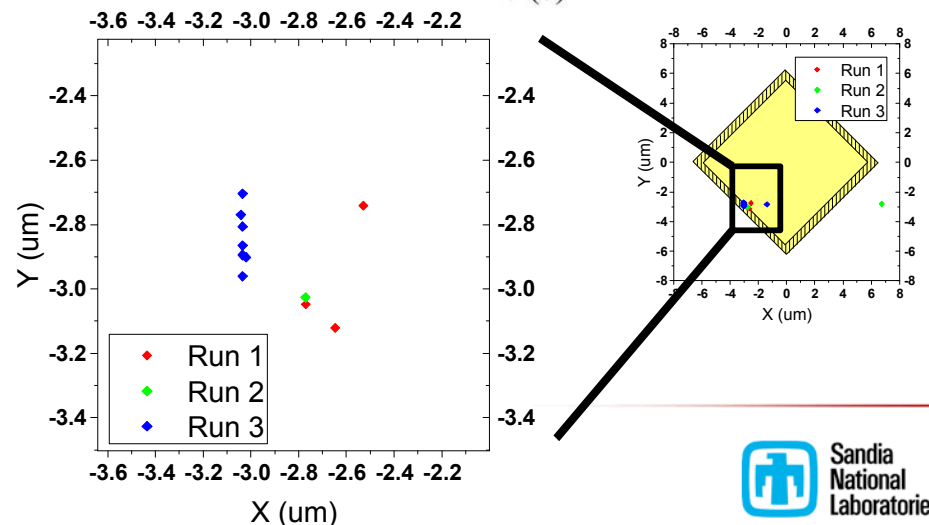
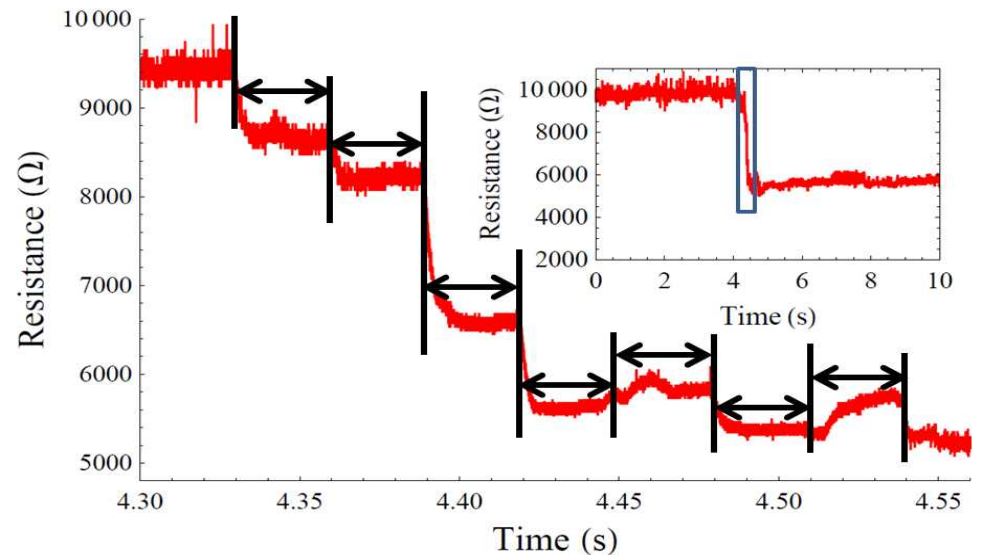
- NanoImplanter (nI)
- 15  $\mu\text{m}$  by 15  $\mu\text{m}$  scan area
  - 200 keV Si<sup>++</sup>
  - Beam spot size ~40 nm





# Size of a Sensitive Area

- Events equally spaced apart
  - 30 ms (one scan length)
  - Part of one region
- Estimate filament in Y
  - Symmetric in X?
- 300 nm
  - 120 nm critical region?
- Filament size affected by non-radiation factors
  - Operating conditions



# Summary and Conclusions

- **Characterized ionization and displacement damage sensitivity**
- **Spatially mapped multiple conduction paths, or potential conduction paths**
  - **Sensitive areas exist preferentially on the perimeter**
  - **Forming method and operating conditions may impact sensitive area**
- **Large portion of the active area is insensitive to displacement damage**
  - **Scaling implications**
- **Capability to locate and characterize sensitive regions with high precision**

