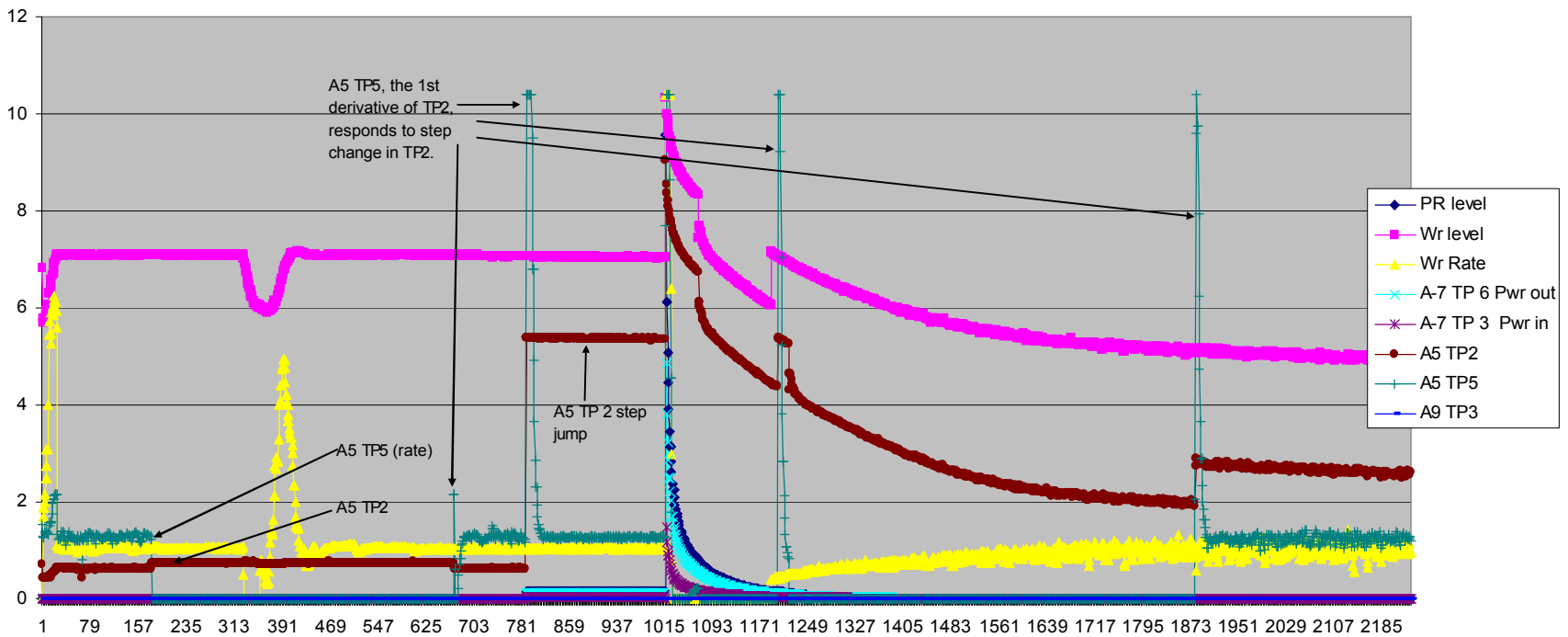


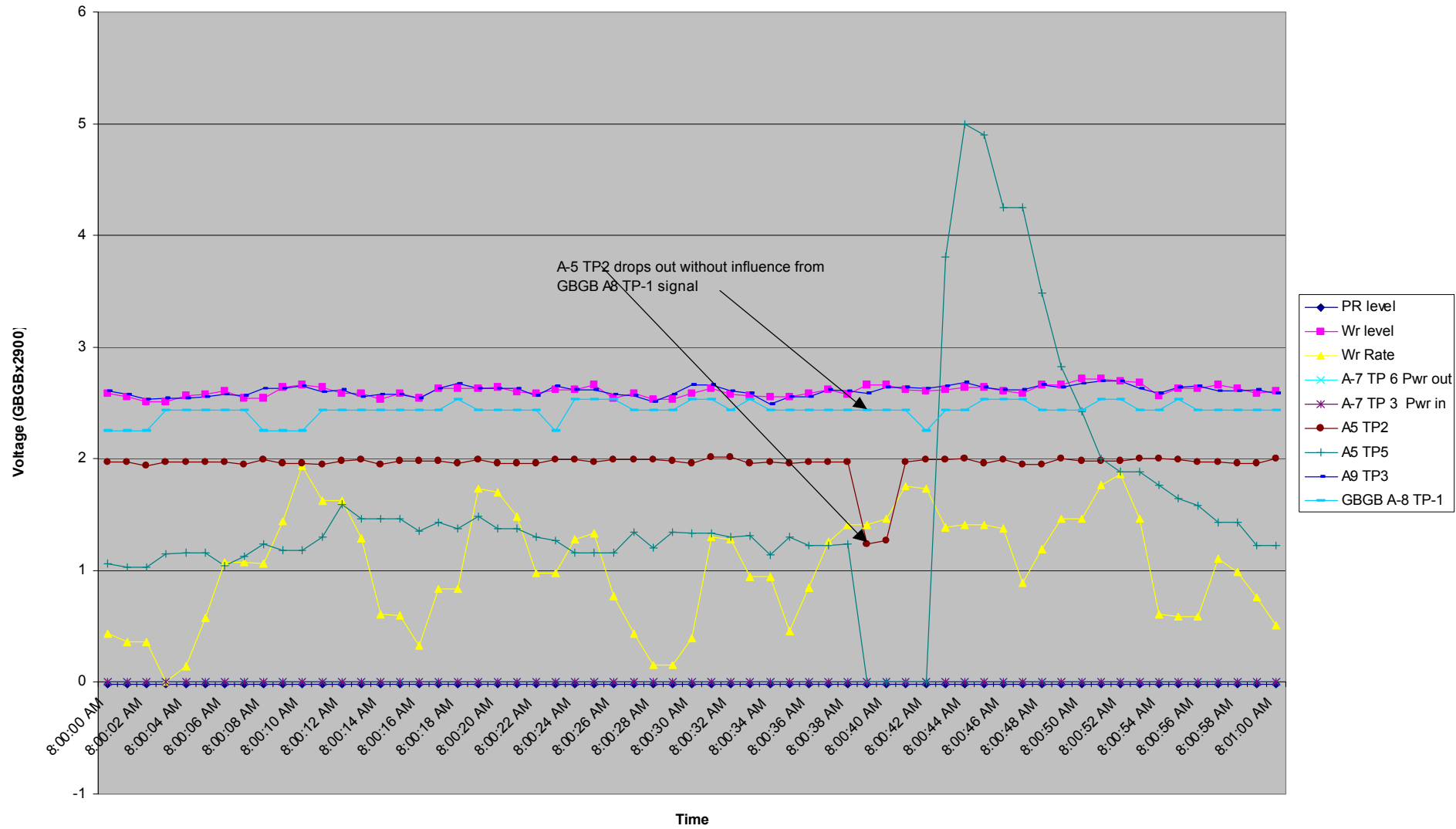
Intermittent Linear Power

- This chronicles troubleshooting efforts at the ACRR reactor to locate the loss of the power linear signal.

Saved data



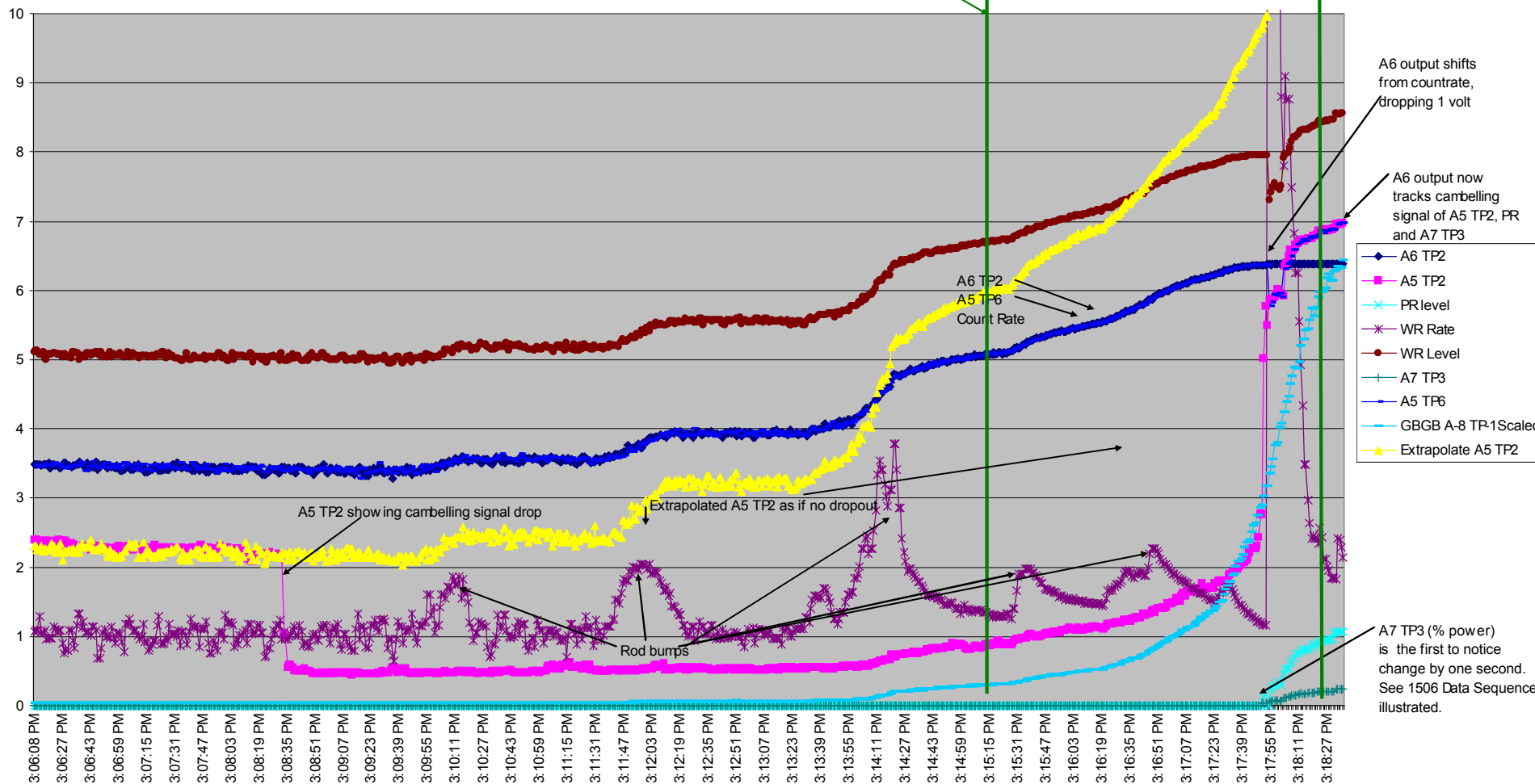
Facility startup Aug 10

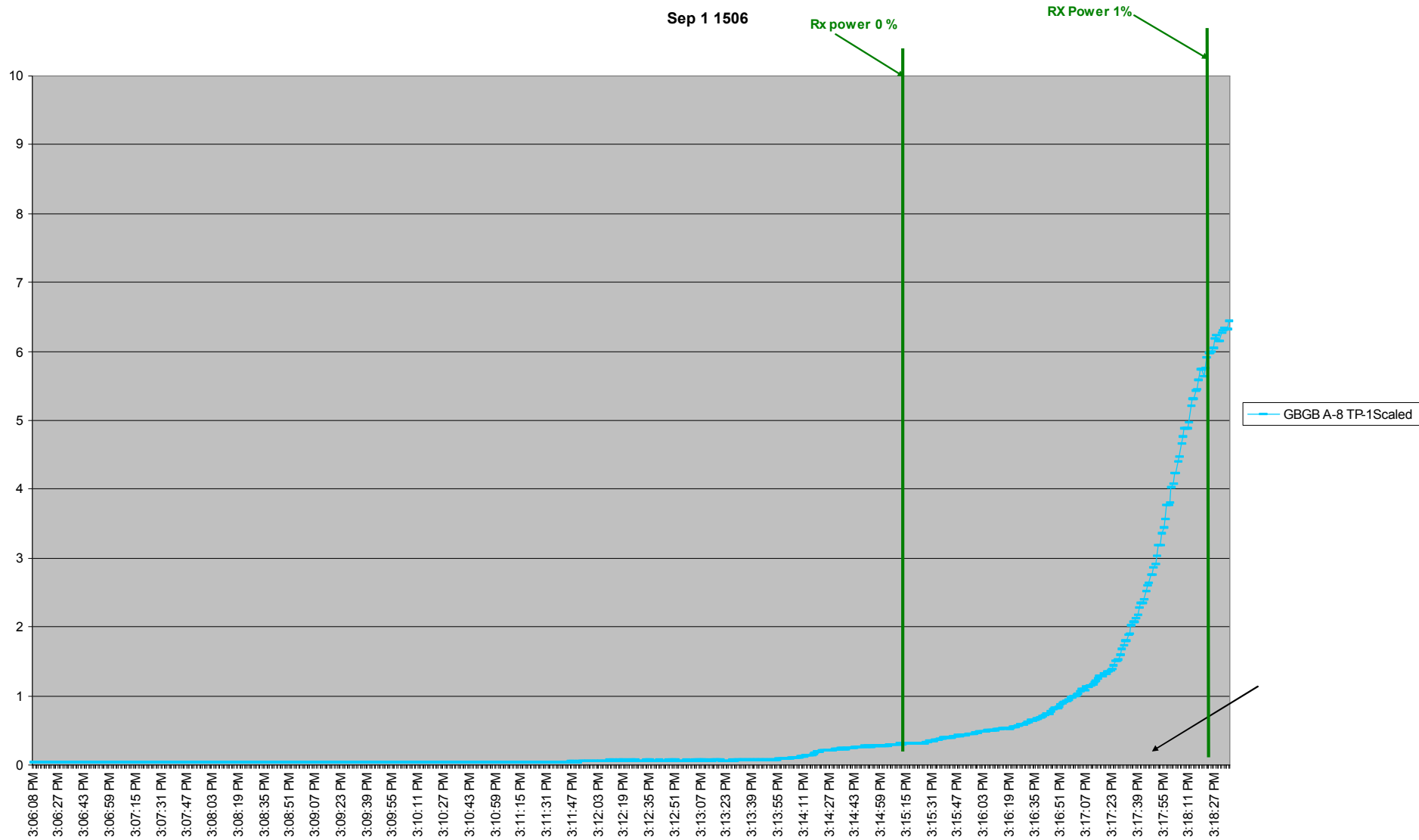


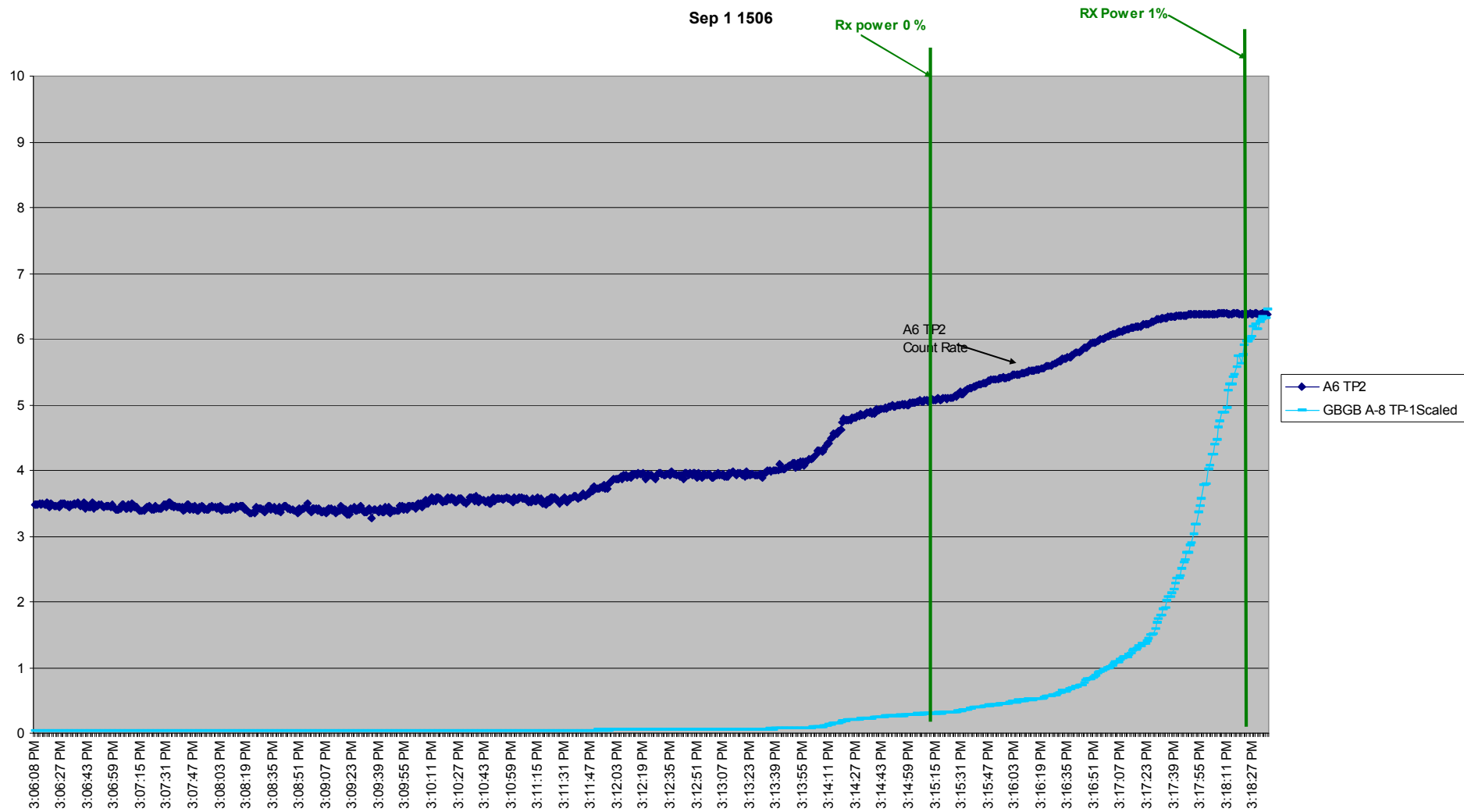
Sep 1 1506

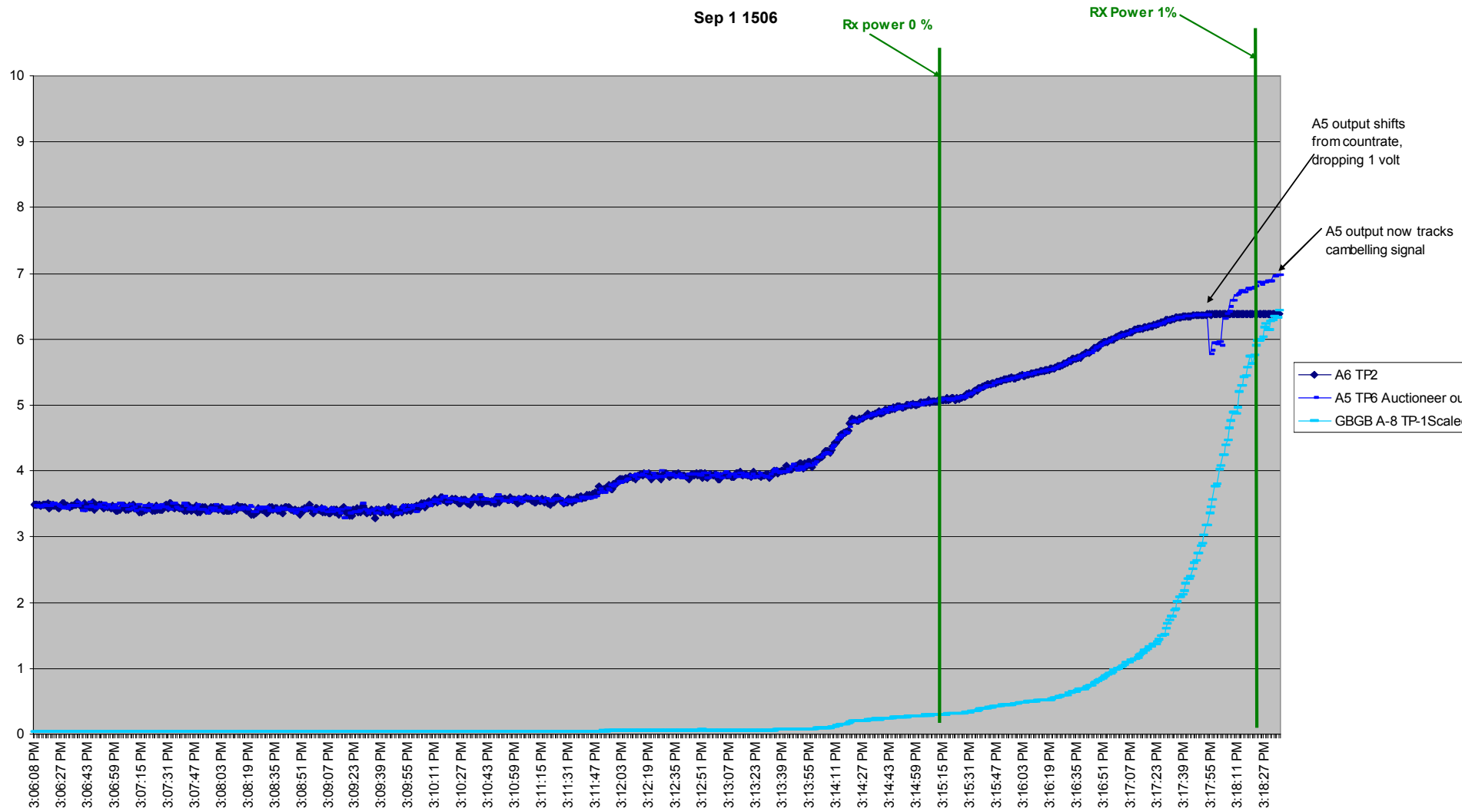
Rx power 0 %

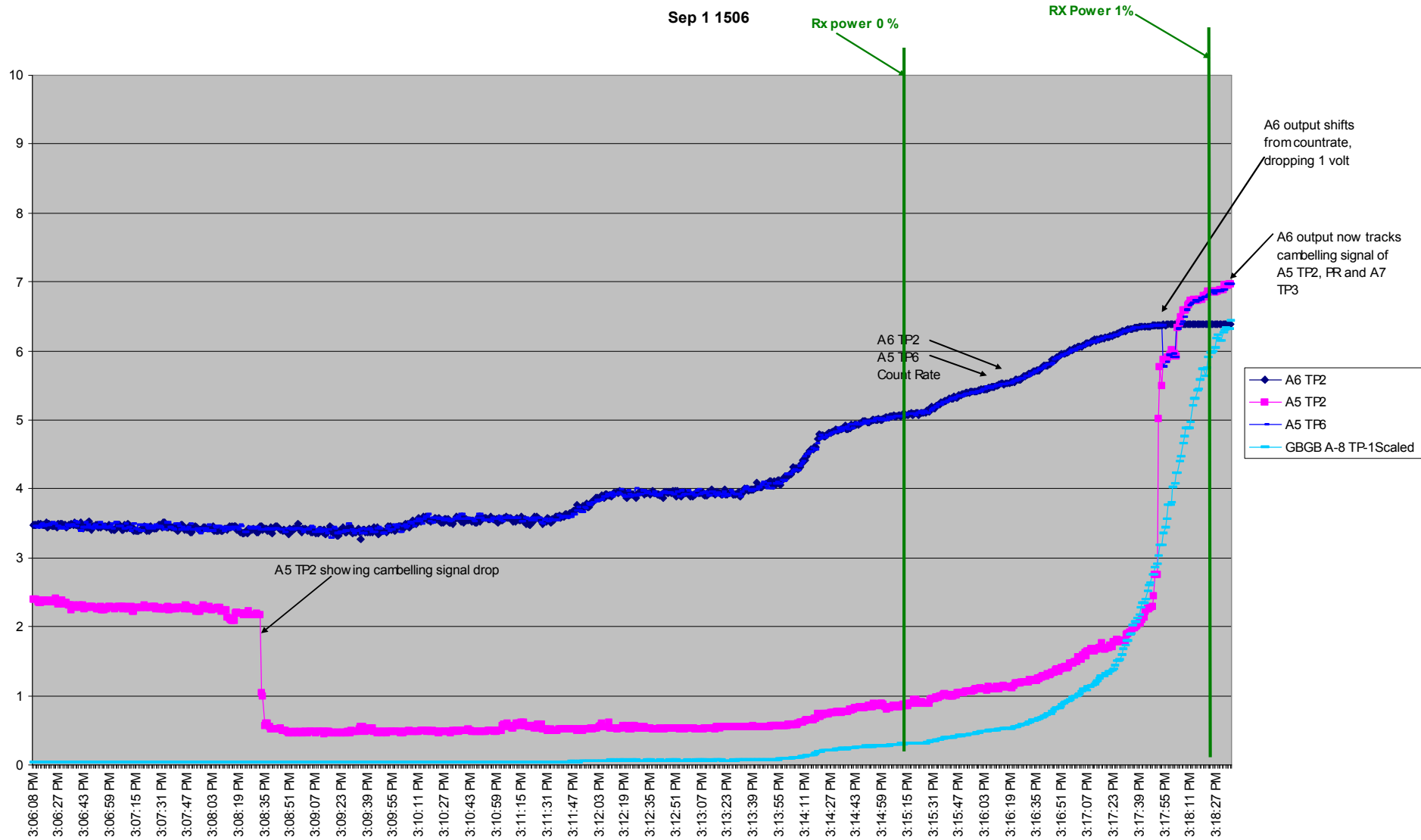
RX Power 1%

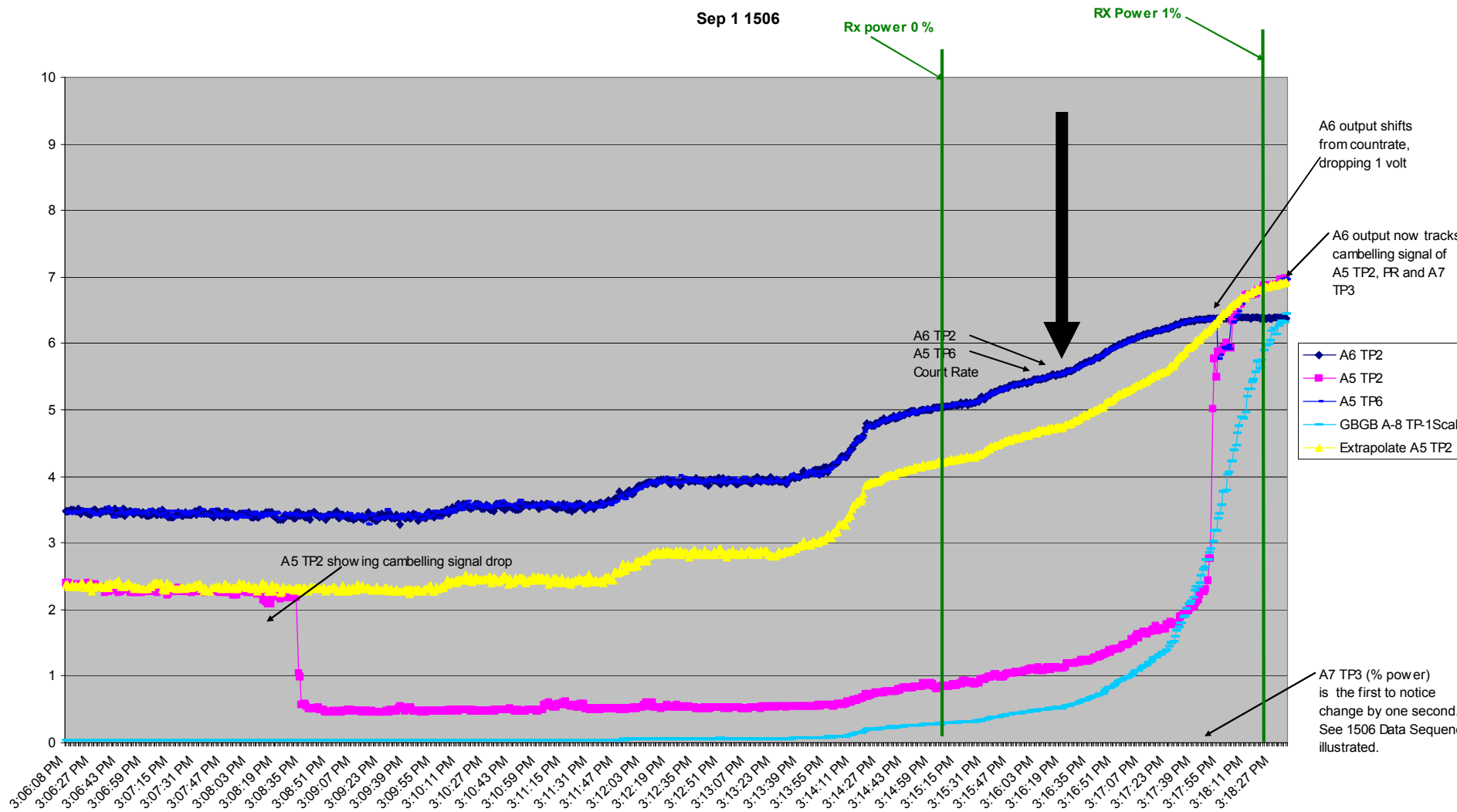


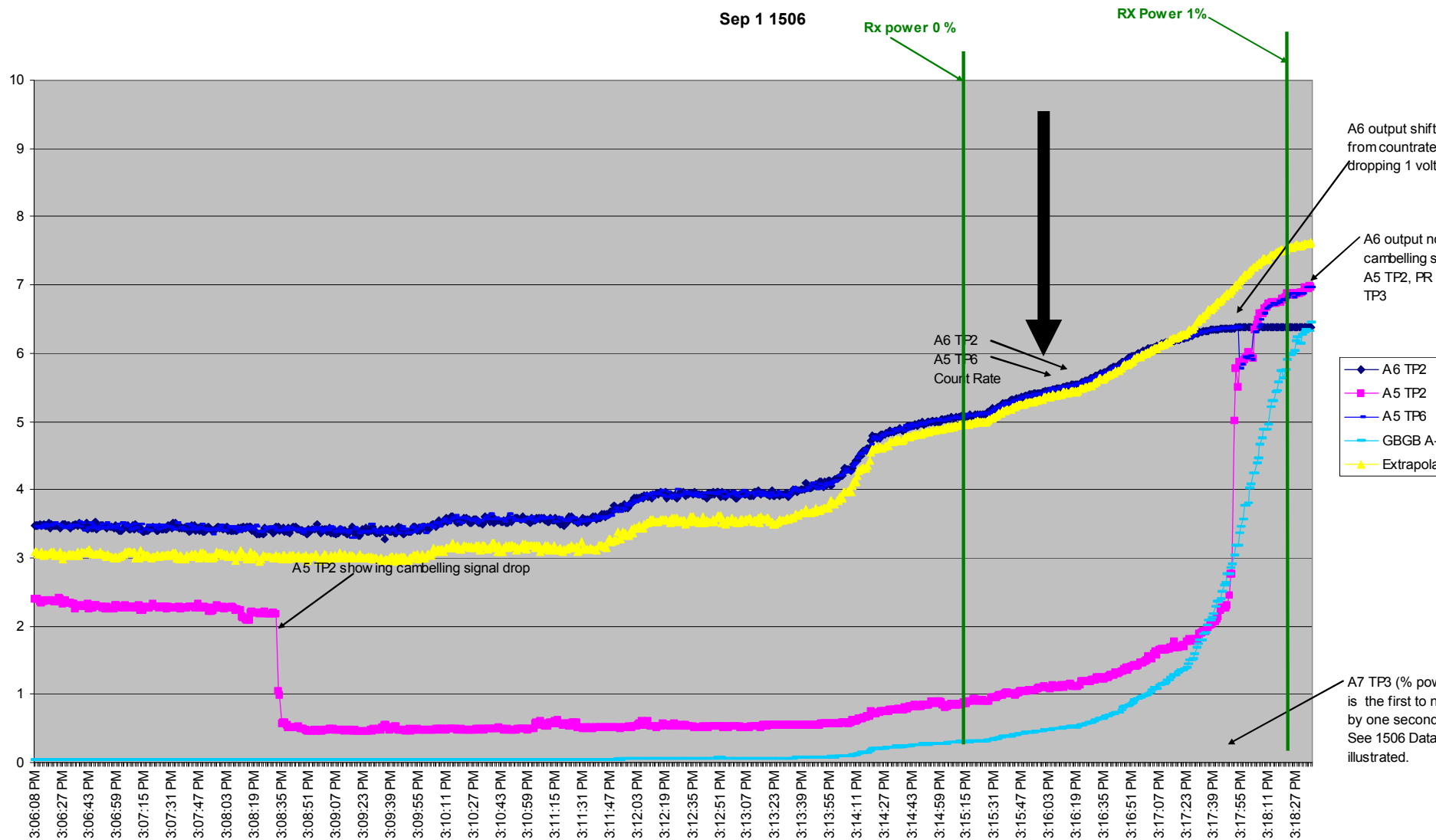


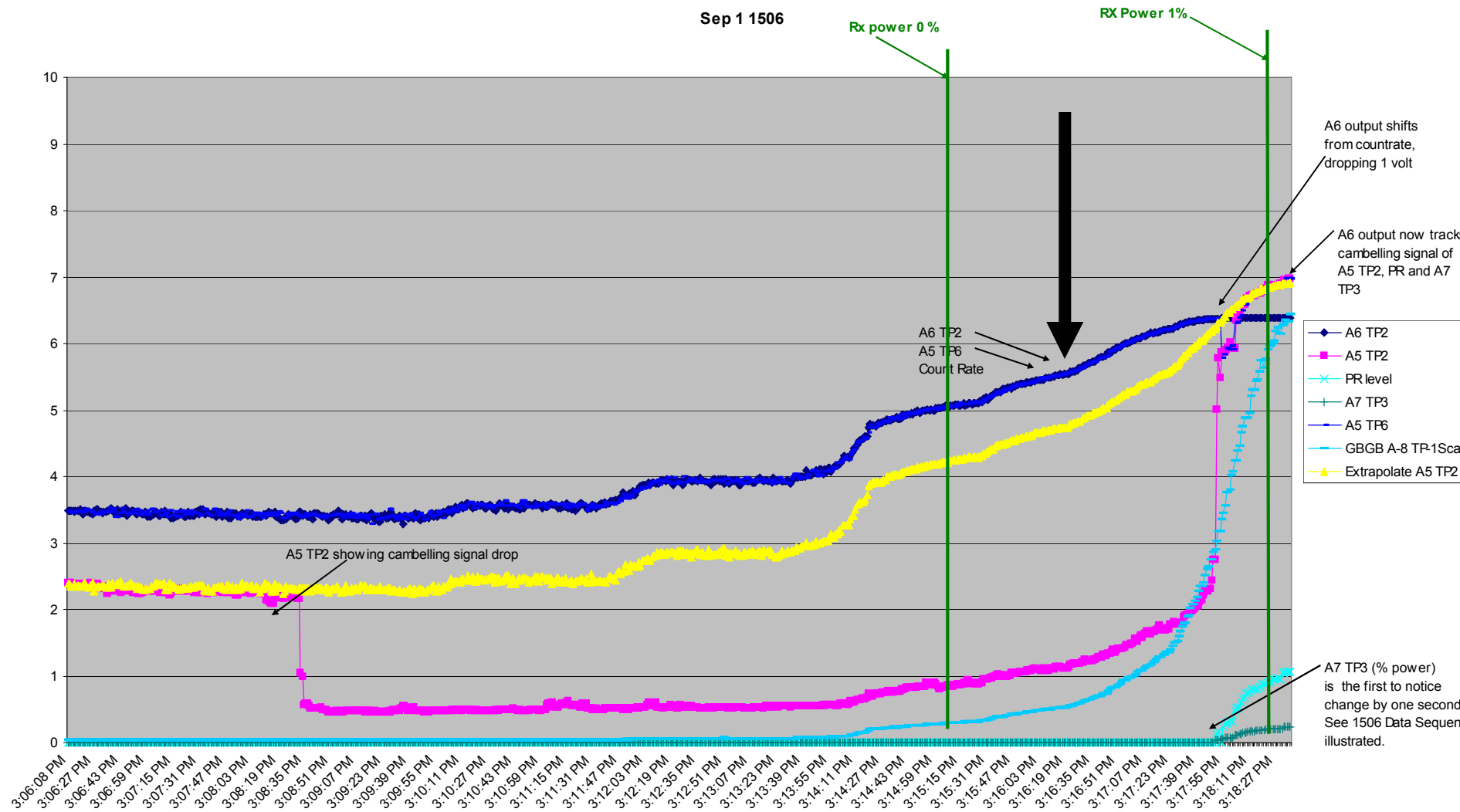








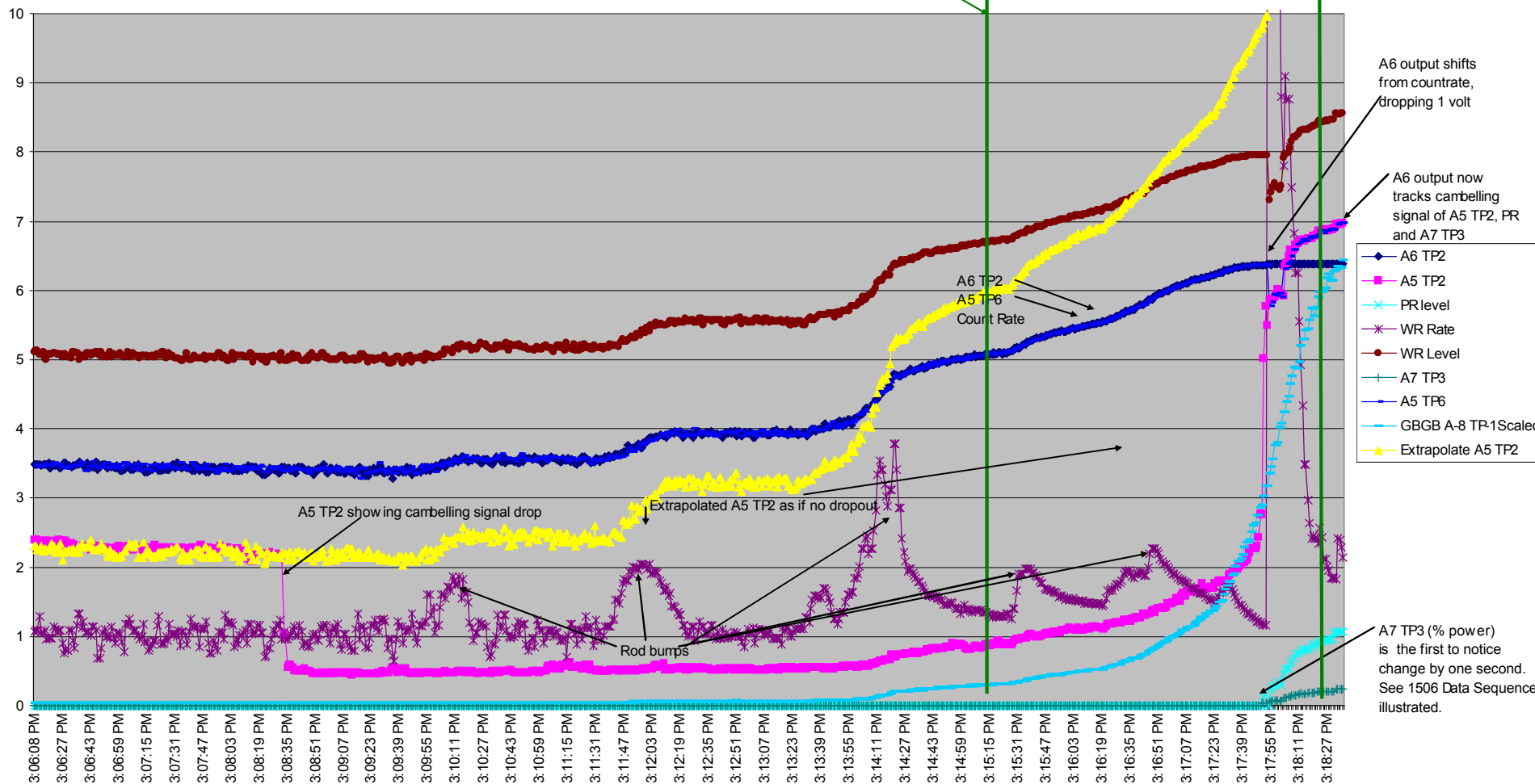




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Rx power 0 %

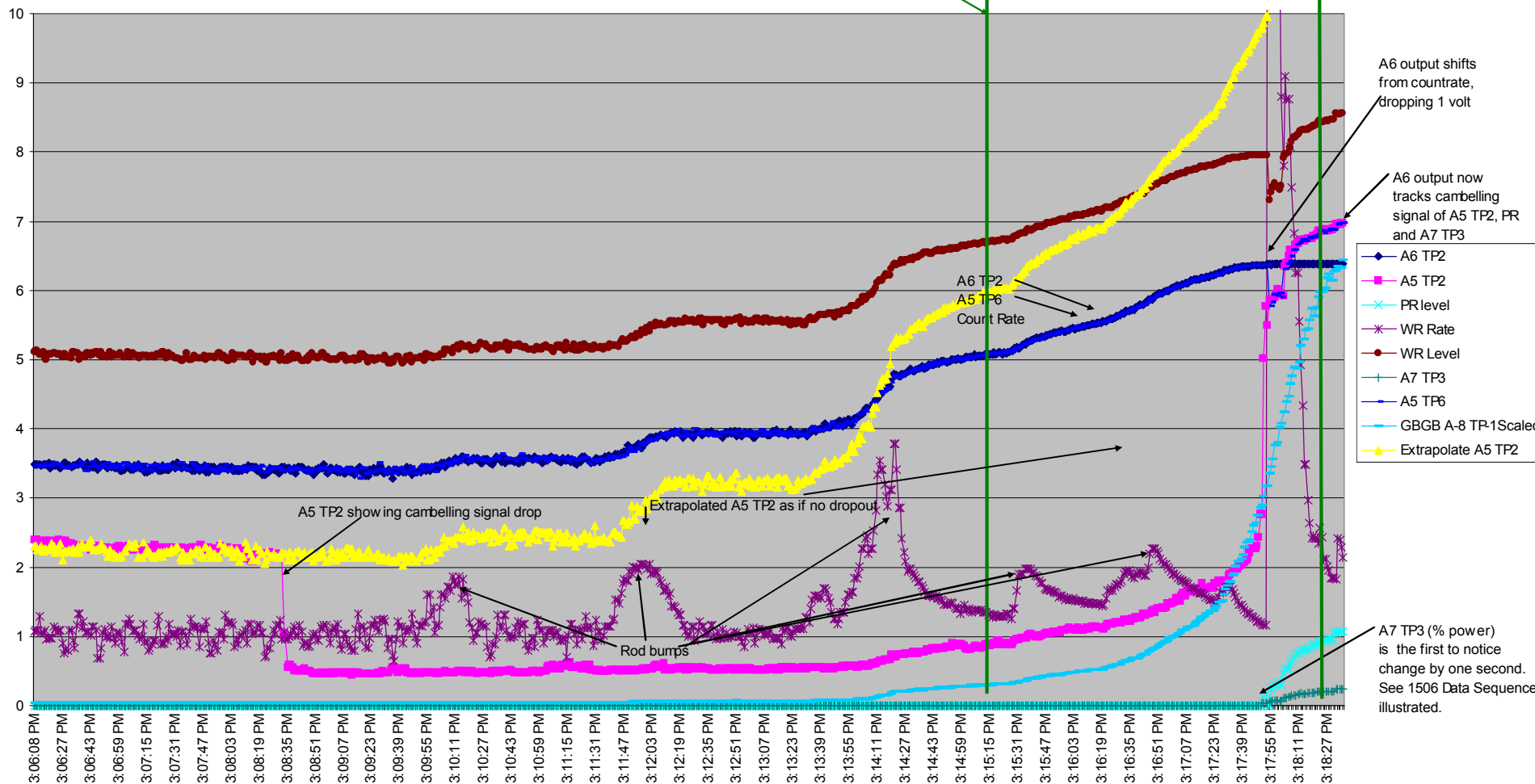
RX Power 1%



Sep 1 1506

Rx power 0 %

RX Power 1%



Sep 1 Facts

- The output from the GBGB (Amp) was good on both the cambelling and countrate signals:
 - The cambelling signal from the A-8 card dropped low
 - This was observed by the auctioneer circuit, preventing the switch to cambelling.
 - The Percent power signal also observed the signal loss and showed no output at ~1% power.
 - The signal from the A-8 returned
 - This immediately caused the auctioneer to shift to cambelling.
 - The Percent power signal returned.

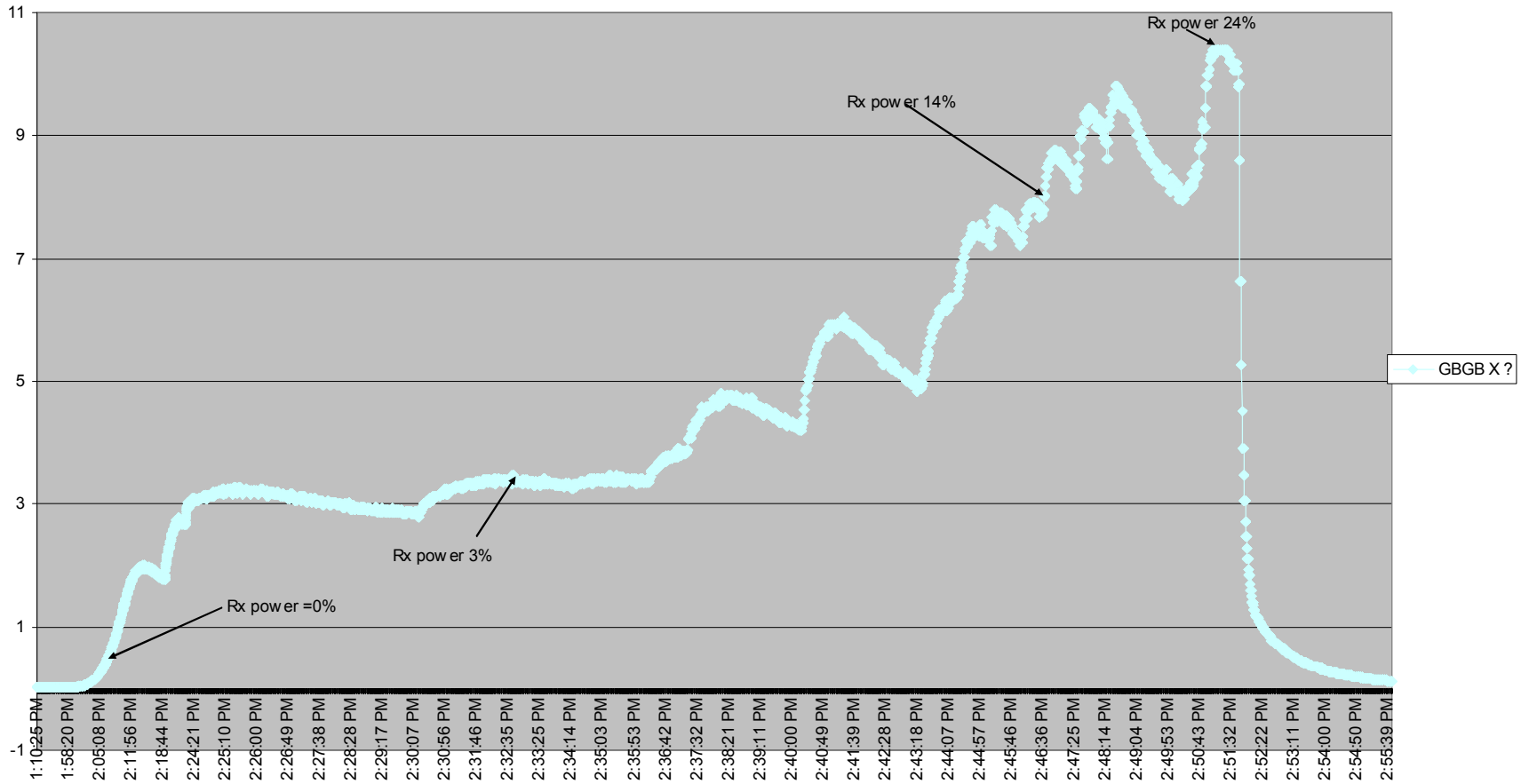
Sep 1 Conclusion

- The signal drop occurred between the GBGB (AMP) and the inputs to the A5 and A7 cards. Components in this path include:
 - PTC2 and Zener CR4 (twisted pair line drivers)
 - The signal processor A8 card
 - This card processes test signals, as commanded by front panel switches.

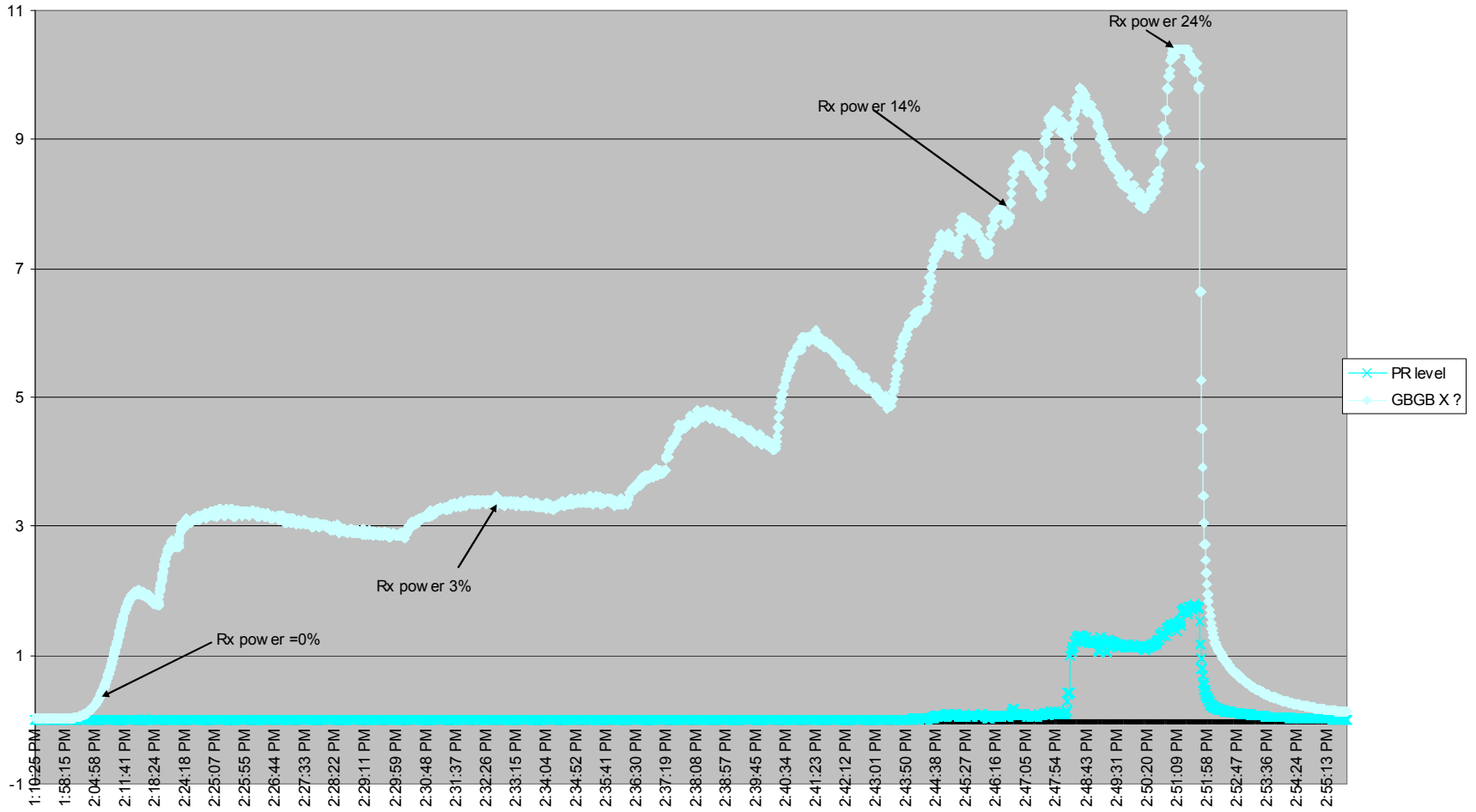
Sep 1 Recommended actions

- Monitor each side of the twisted pair line drivers (PTC2 and Zener CR4)
- Check cabling from front panel switches to the A-8 card
- Test front panel switches.
 - Operate while monitoring WR output
 - Operate slowly, seeking intermediate positions

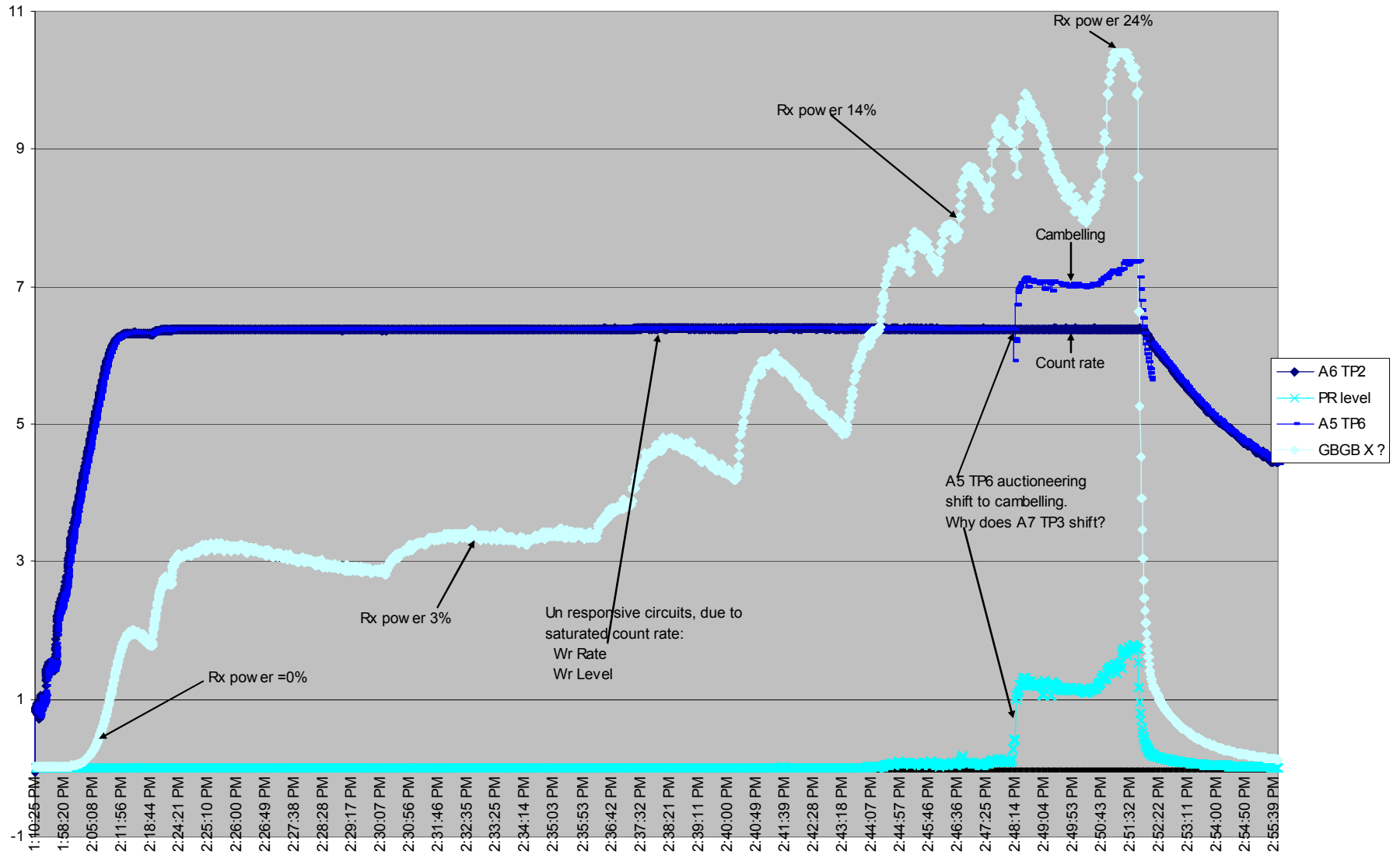
1 Sep 1310



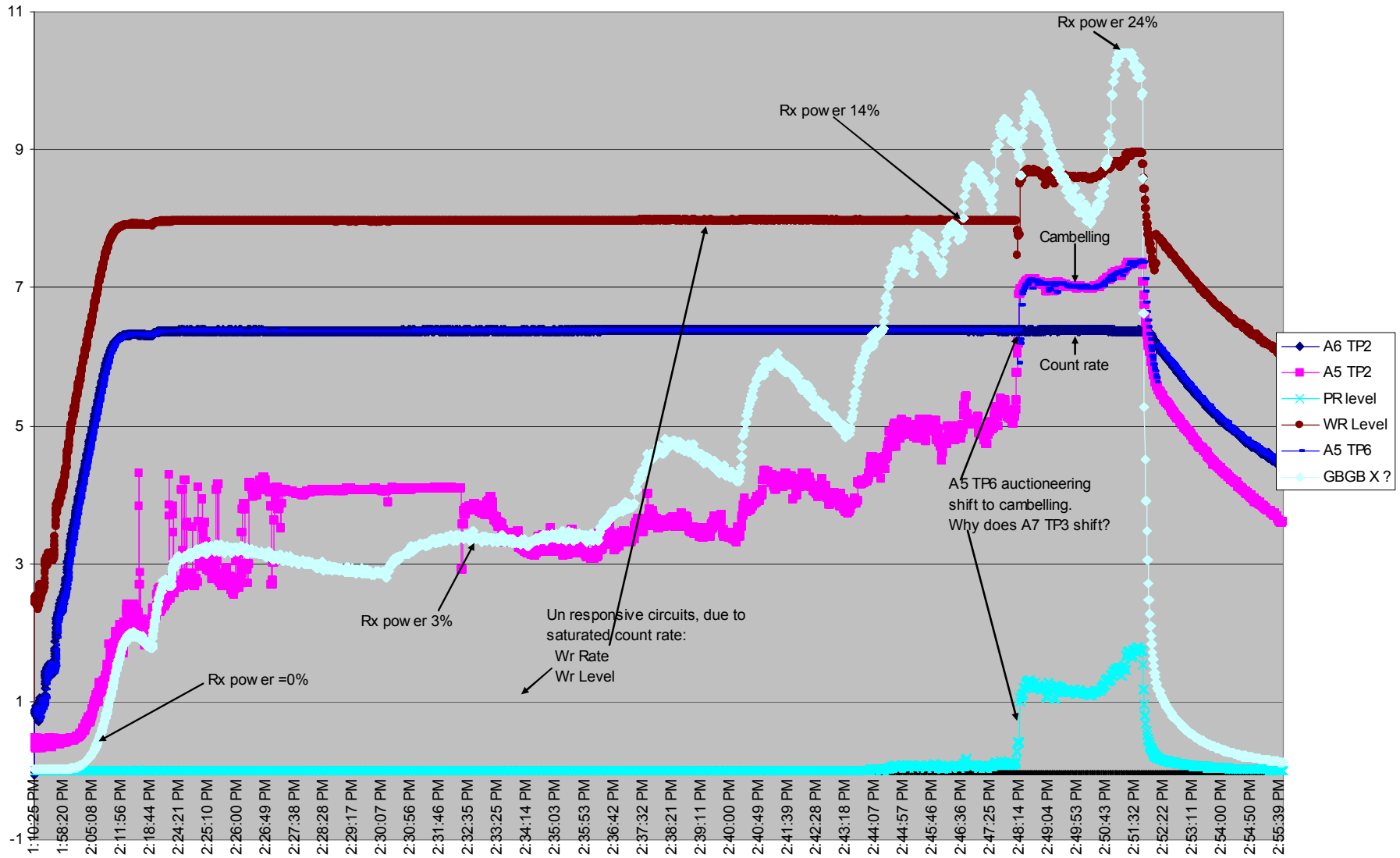
1 Sep 1310



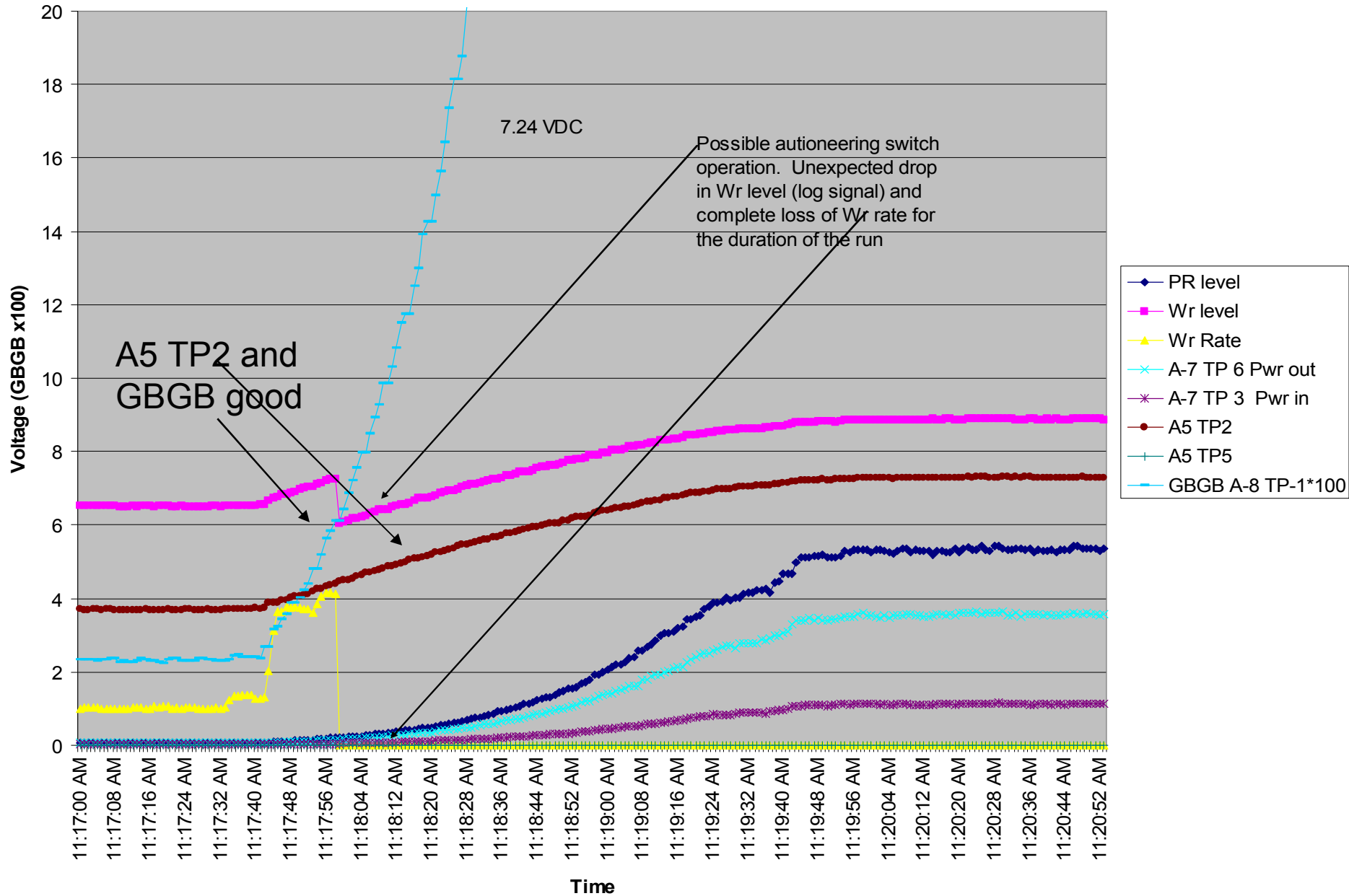
1 Sep 1310

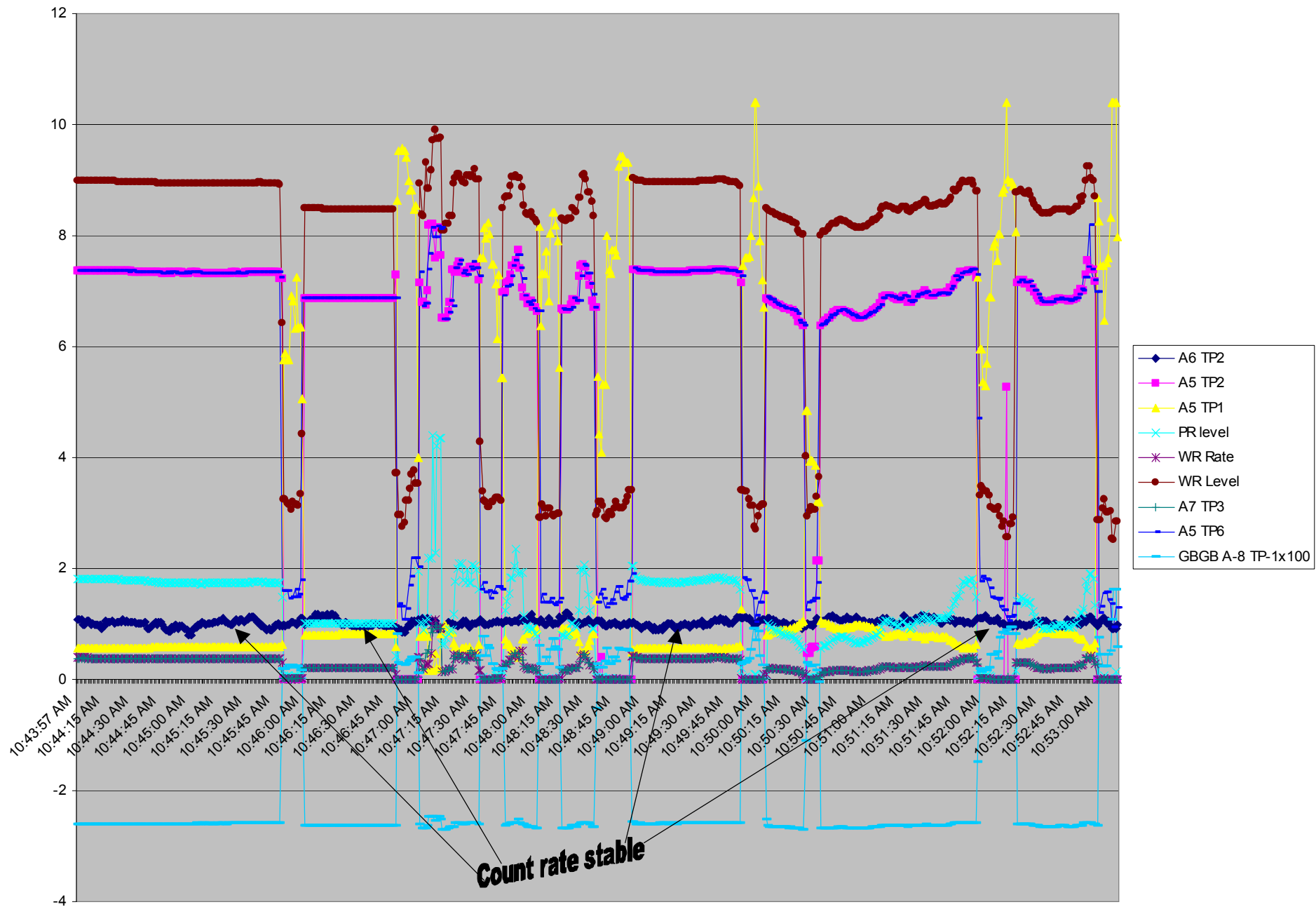


1 Sep 1310

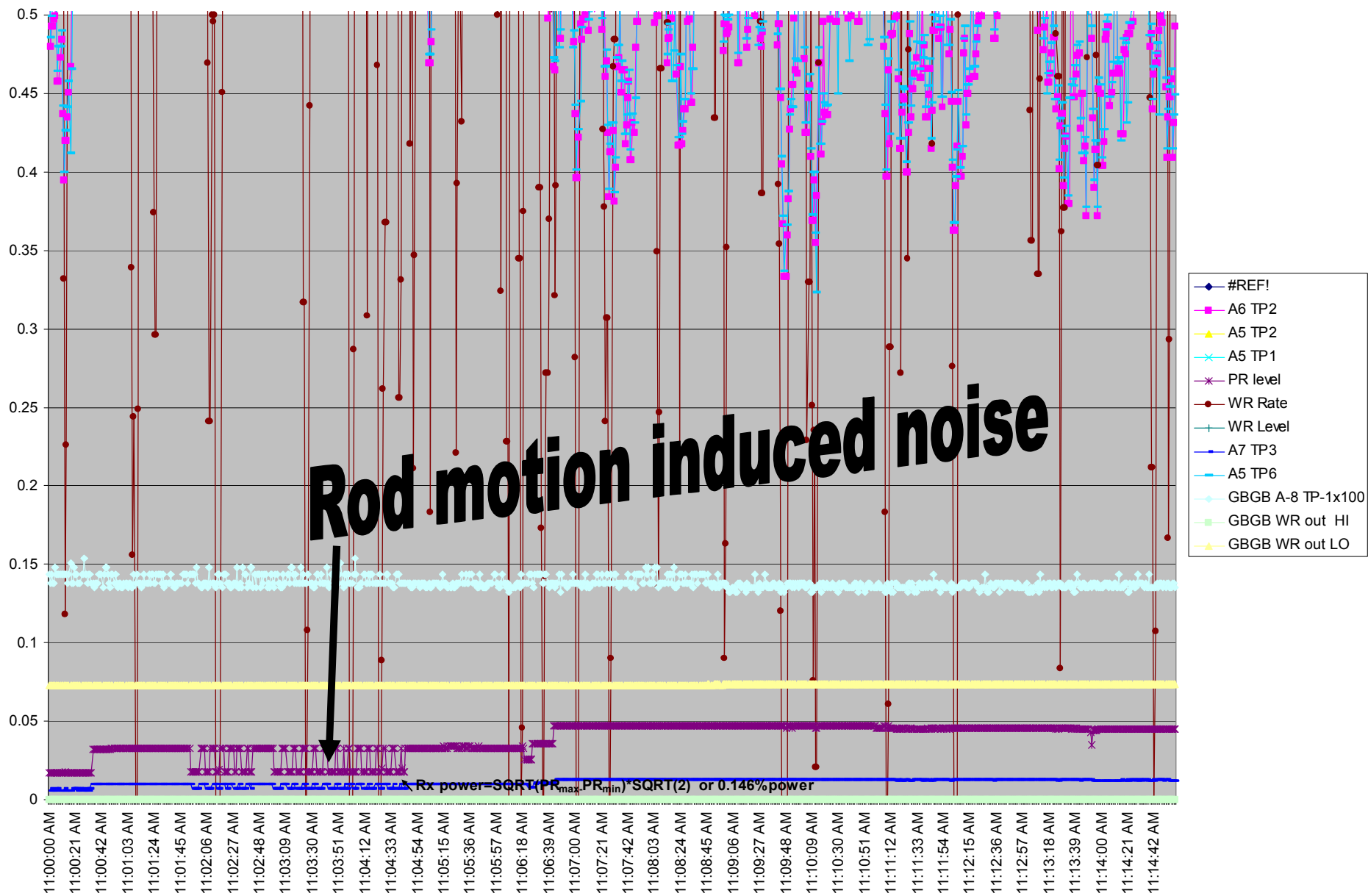


Aug 22









POWER [%]

STEADY STATE RUN 8617 to 20%

