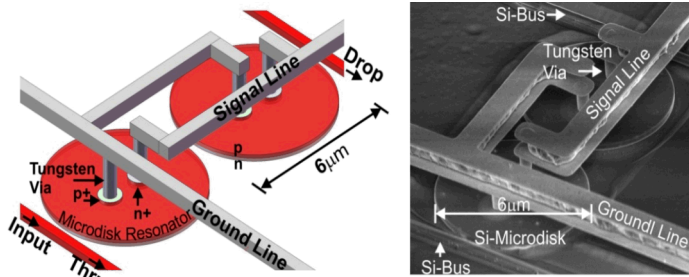


High Speed Travelling Wave Carrier Depletion Silicon Mach- Zehnder Modulator

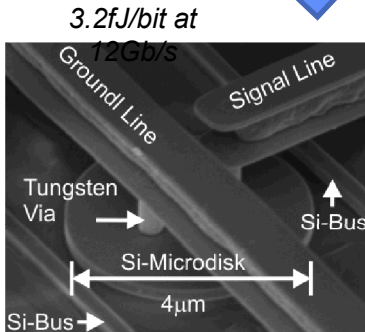
**Christopher T. DeRose, Douglas C. Trotter, William A. Zortman and
Michael R. Watts**

Silicon Photonics At Sandia

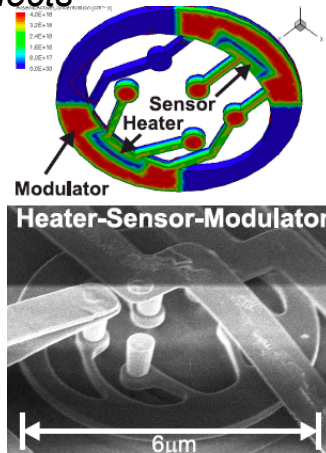
Free-carrier Effect (high-speed)



Fast Reconfigurable Interconnects

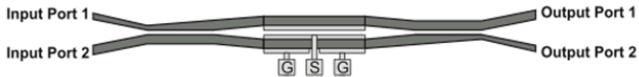


Resonant Optical Modulator/Filter

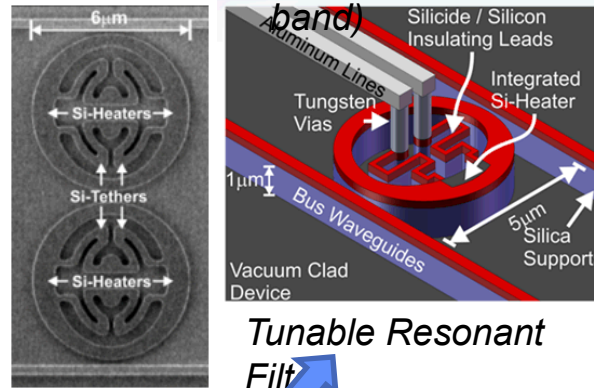


Thermally stabilized modulator

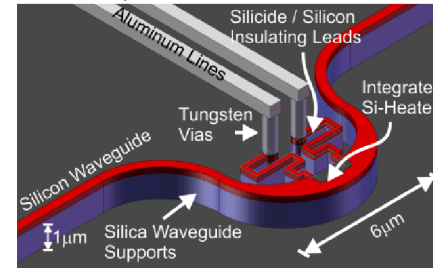
Broadband Mach-Zehnder Filter/Switch <math>< 1V\text{-cm}</math> at 10 Gb/s



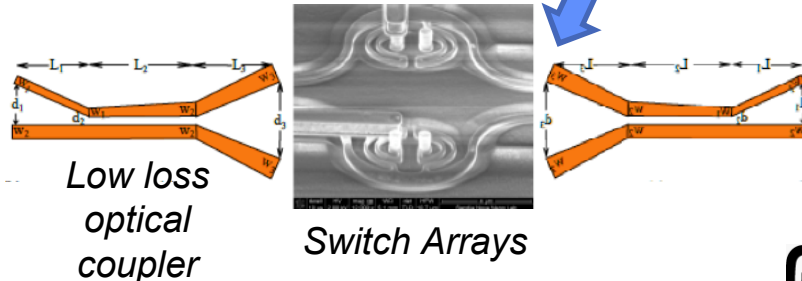
Thermal Optic Effect (wide-band)



Tunable Resonant Filter



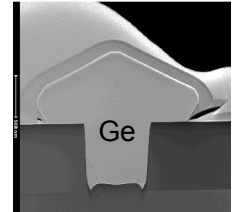
Thermo-optic Phase Shifter



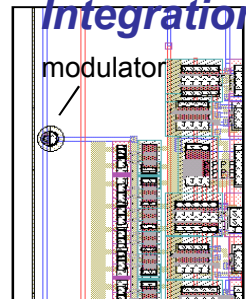
Low loss optical coupler

Switch Arrays

High-speed Ge Detector in



Si Photonics-CMOS Integration



Sandia's MESA Fabrication

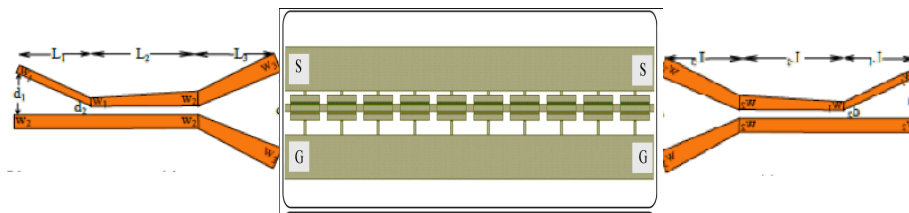
Microsystems Design, Fabrication and Test

- Microfabrication
- Packaging
- Si Micromachining
- Advanced Modeling
- 3D Simulation
- Photonics
- III-V Semiconductors
- Nanotechnology
- Electronics

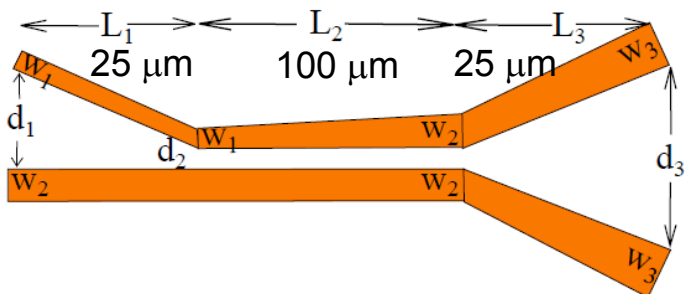
650 people
380,000 sq ft



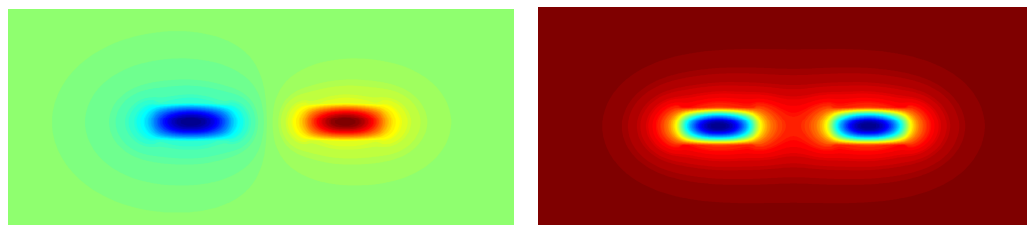
Adiabatic Coupler



Adiabatic 3-dB Splitter



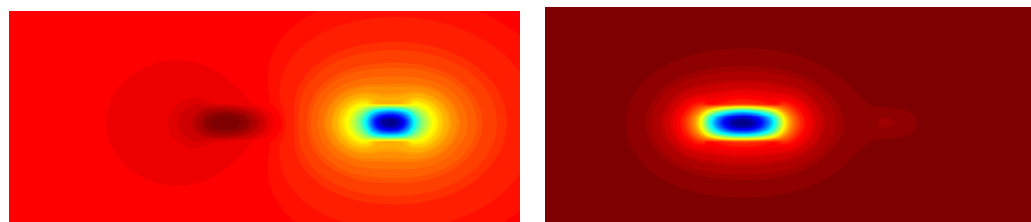
Symmetric Waveguide Coupling



$$n_{\text{eff}} = 1.9745$$

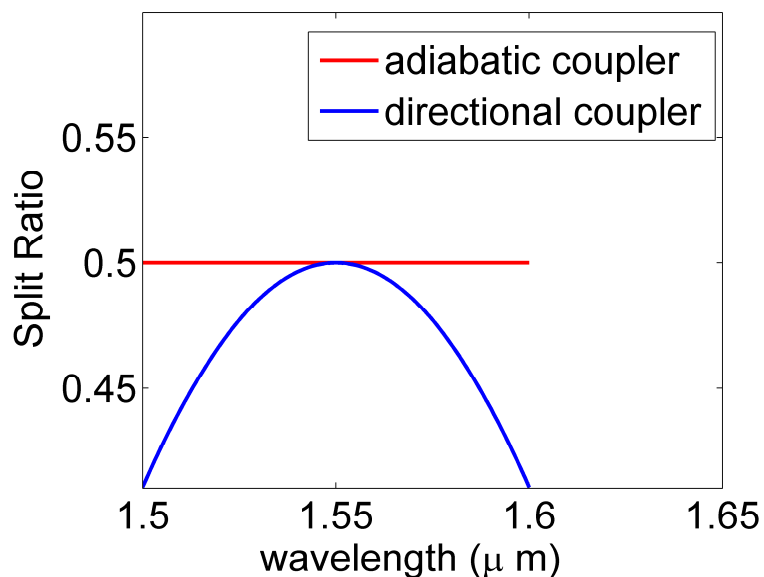
$$n_{\text{eff}} = 1.9417$$

Asymmetric Waveguide Coupling

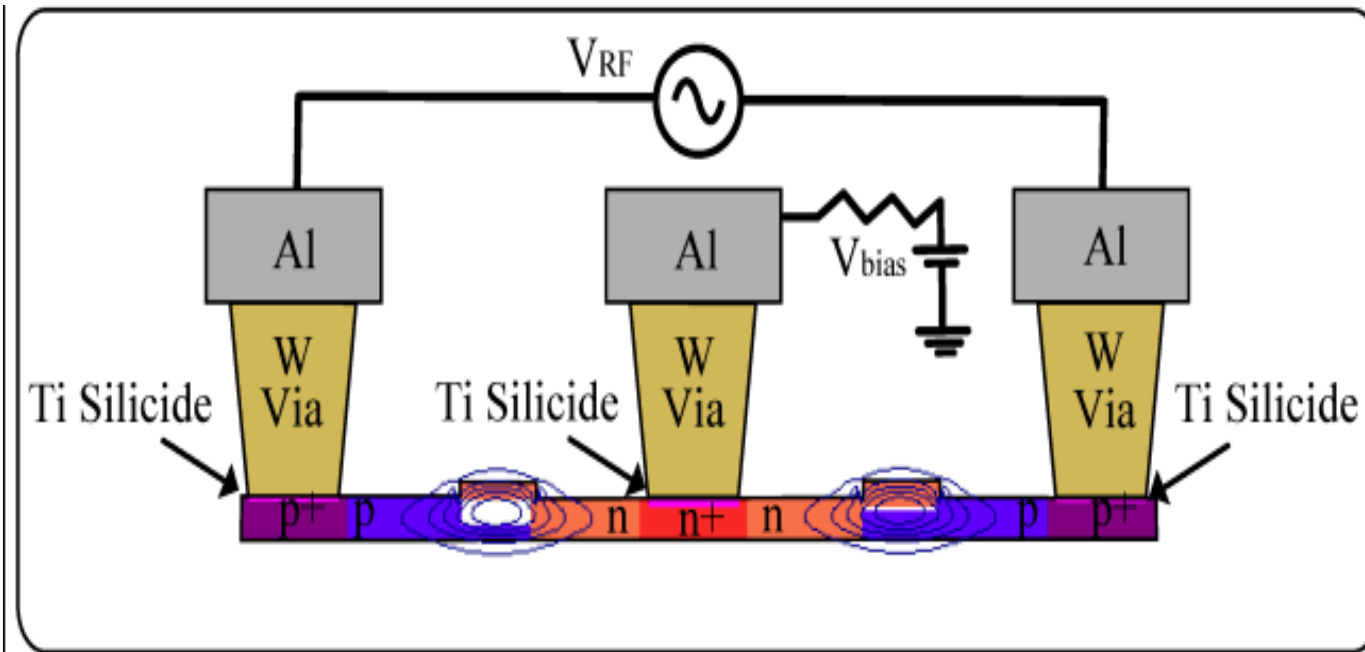
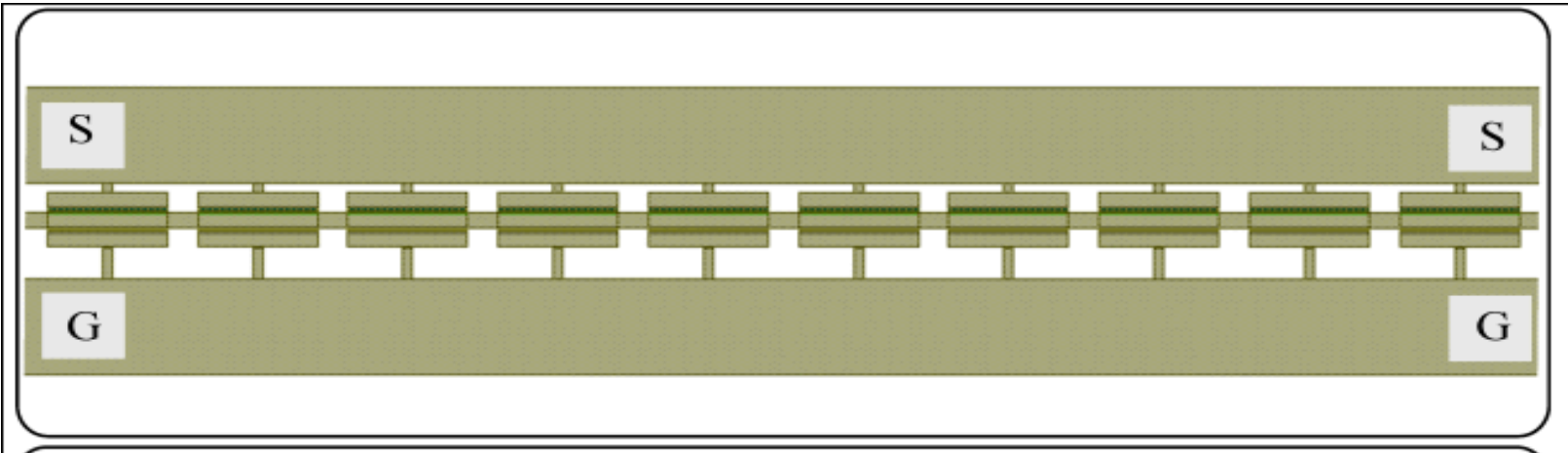


$$n_{\text{eff}} = 1.6248$$

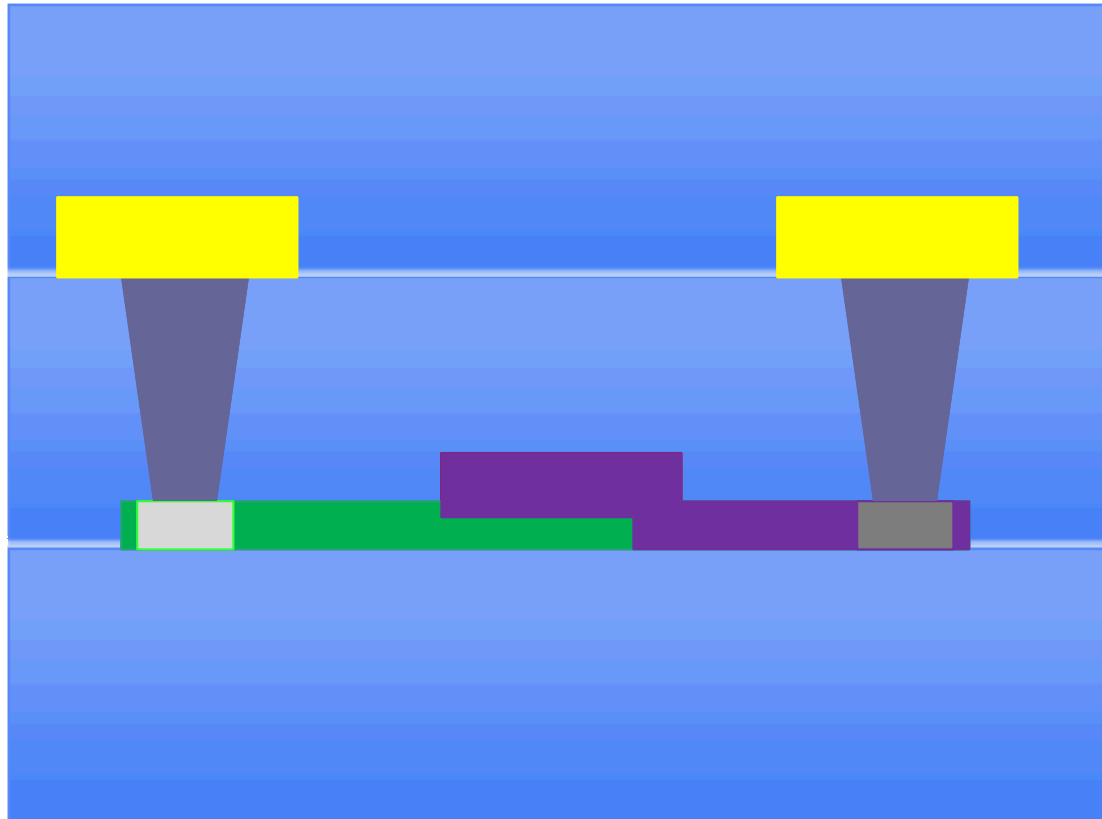
$$n_{\text{eff}} = 1.9592$$



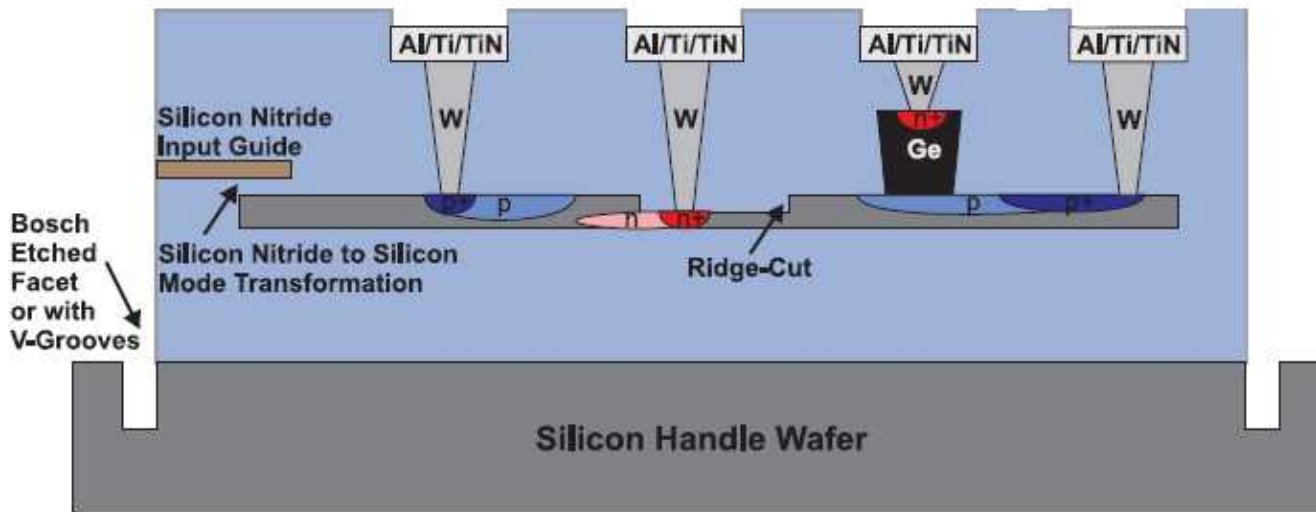
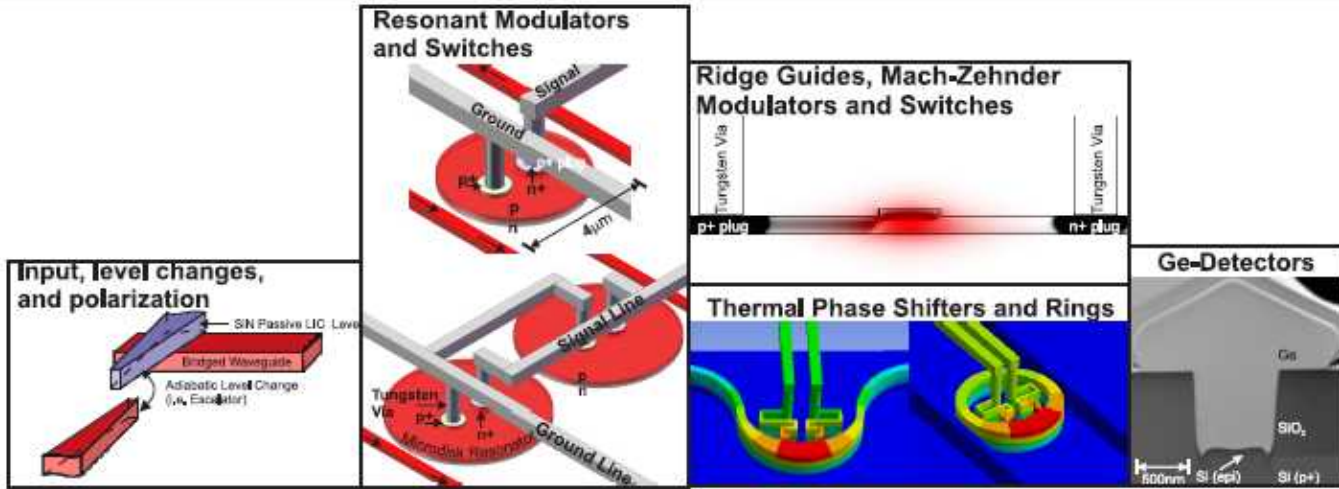
Device Schematic



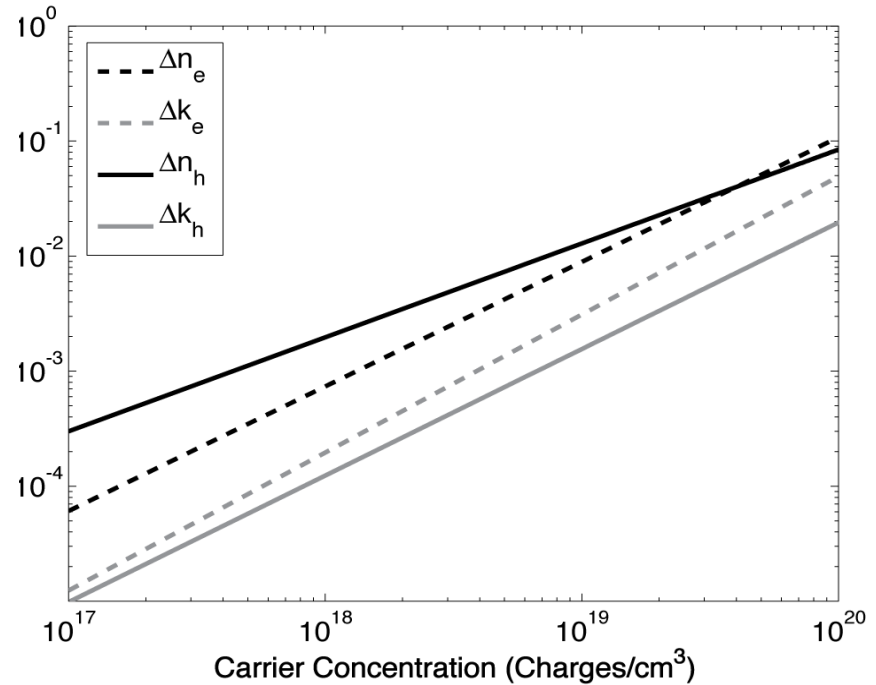
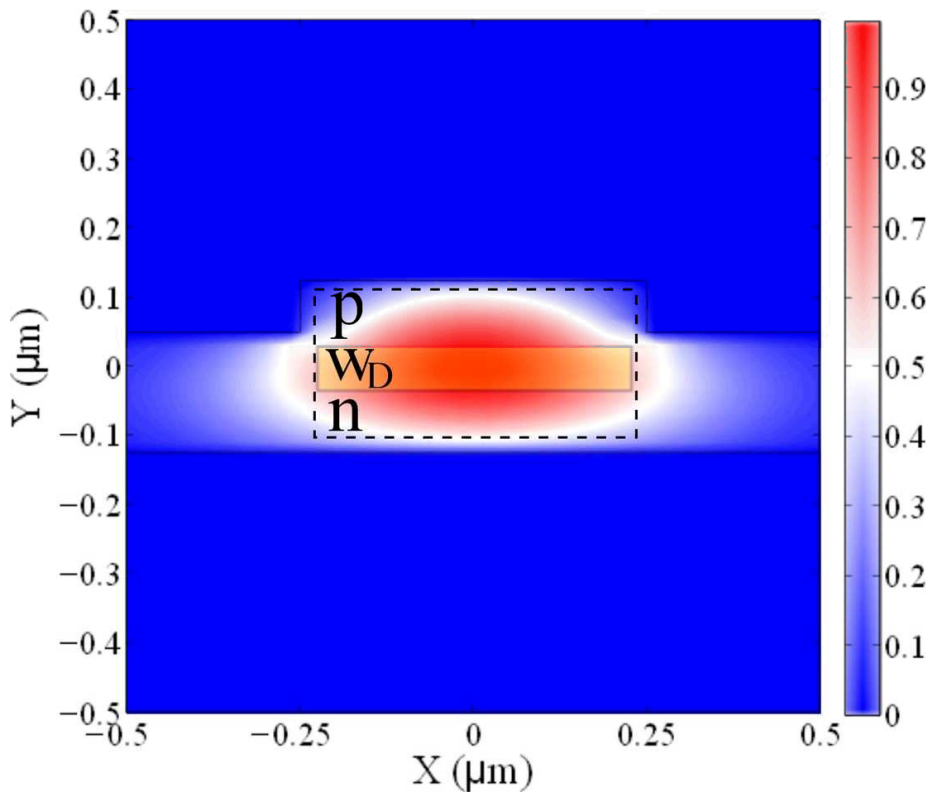
Process Flow



SNL Si Photonics Platform

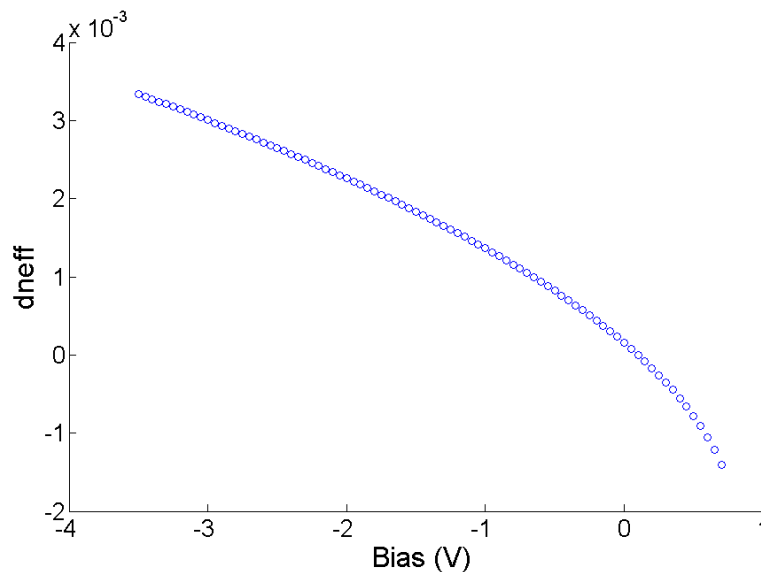
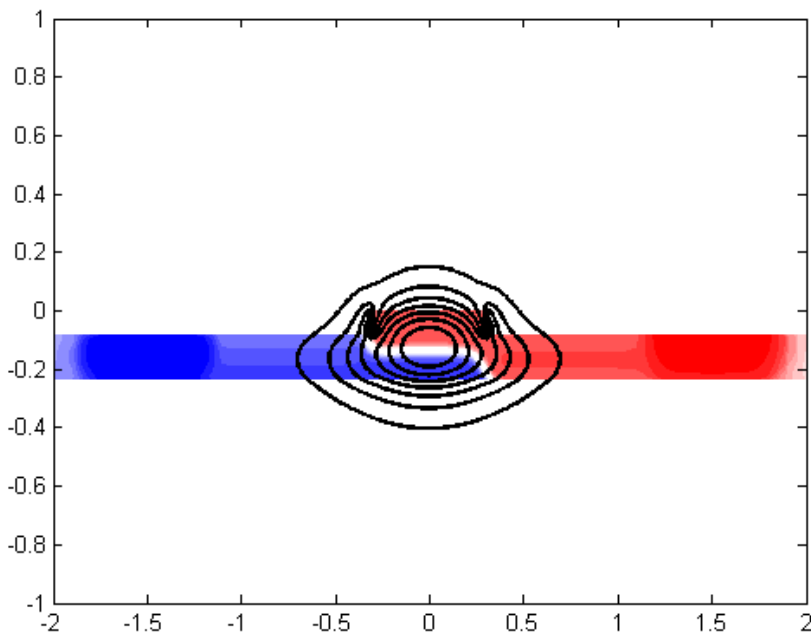


Free Carrier Dispersion Effect



$$\Delta n = A \cdot N^B + jC \cdot N^D$$

Device Model



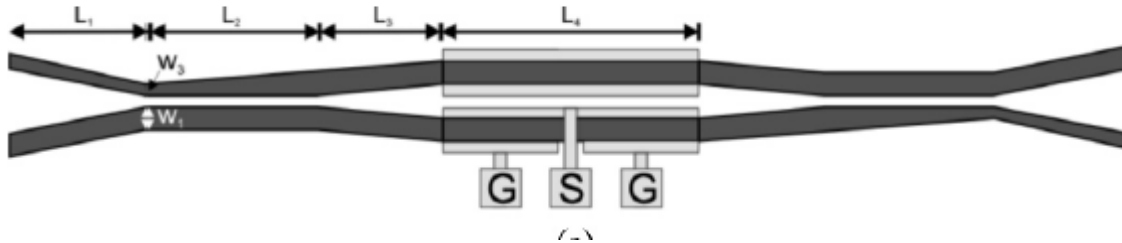
$$\delta\beta = \frac{\omega n_g \iint \Delta\epsilon_{12} |E|^2 dA}{2c \langle E | \epsilon | E \rangle}$$

$$C = 0.53 \text{ fF}/\mu\text{m}$$

$$R = 3.2 \text{ k}\Omega\text{-}\mu\text{m}$$

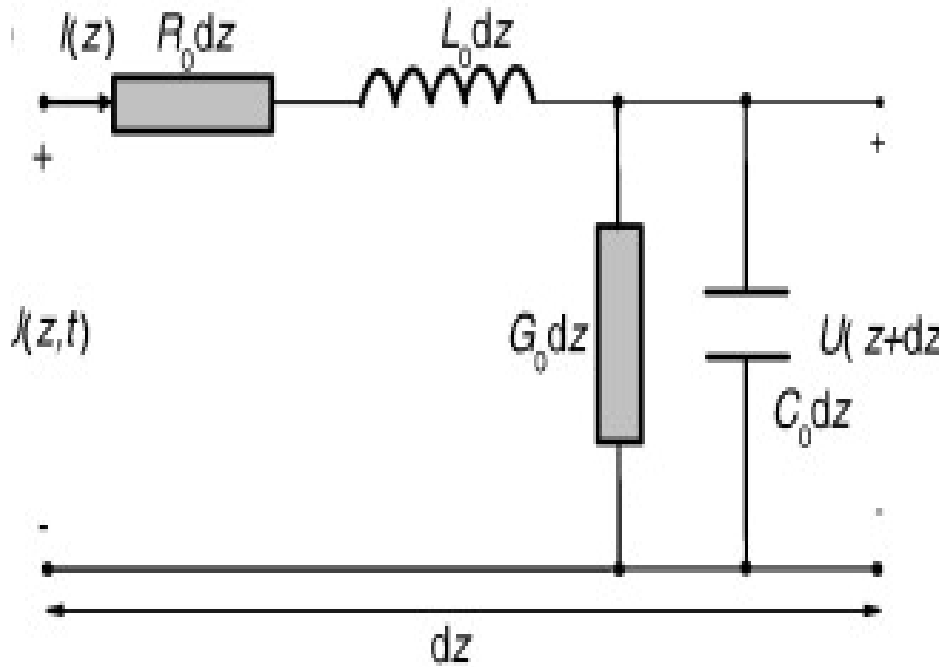
Need δn of $\sim 1\text{e-}3$ for 1 mm device

Why Travelling Wave ?



$$f_{3-dB} = \frac{0.5v_{g,o}}{L} * \frac{1}{|1 - v_{g,o} / v_{g,el}|}$$

Circuit Model of Segmented Inductors TW Modulator



$$Z_0 = \sqrt{\frac{L_0}{C_0}} \quad Z_L = \sqrt{\frac{L_0}{C_0 + C_L}}$$

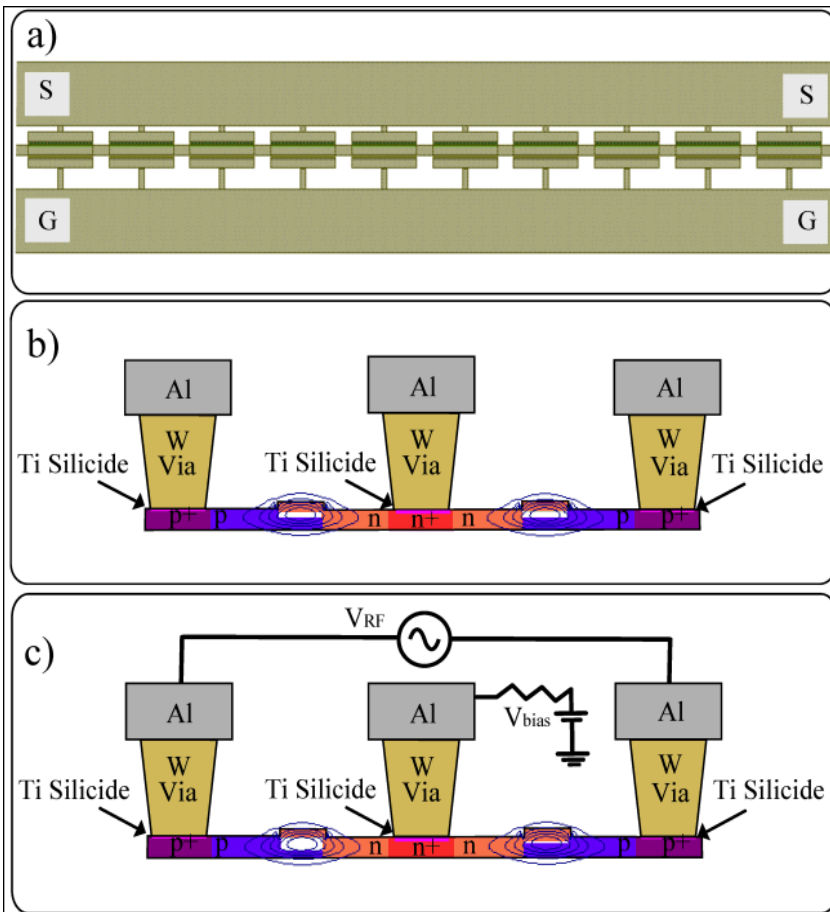
$$n_0 = c\sqrt{L_0 C_0}$$

$$n_L = c\sqrt{L_0(C_0 + C_L)}$$

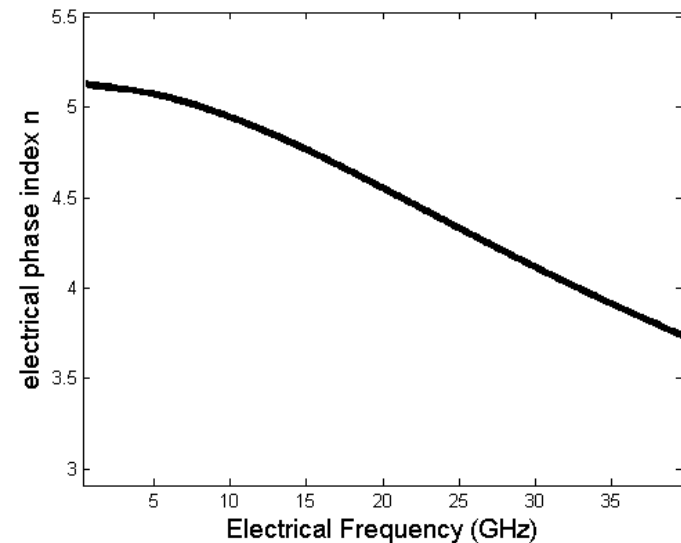
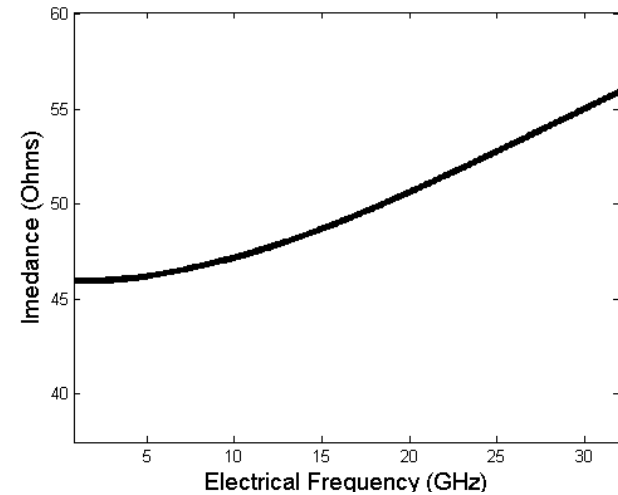
$Z_L = 50 \Omega$ and n_L matched to group index of waveguide

$$C_L = \frac{n_L^2 - n_0^2}{cZ_L n_L}$$

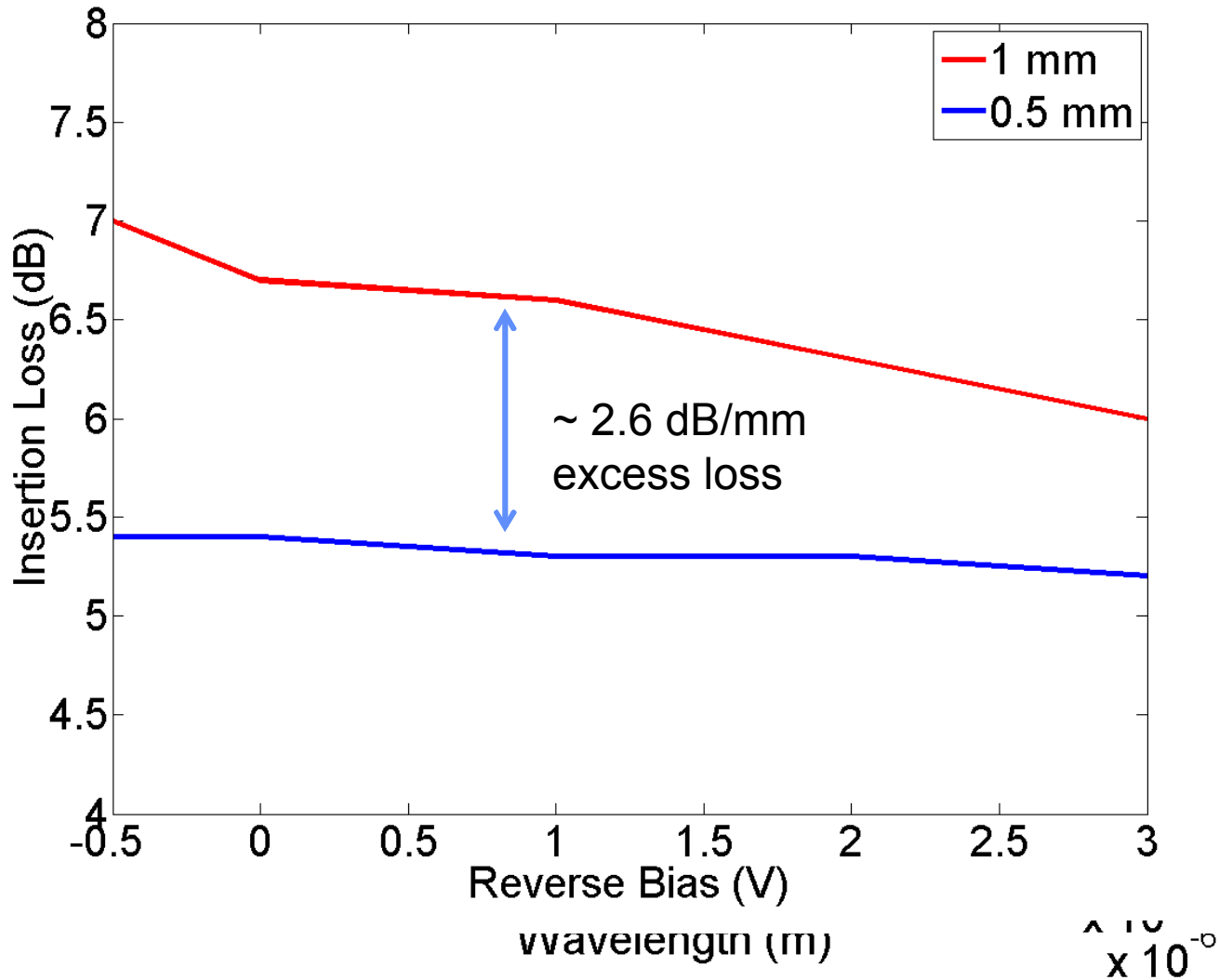
Travelling Wave Design



$Z_0 = 95 \text{ Ohms}$
 $N_0 = 2.3$

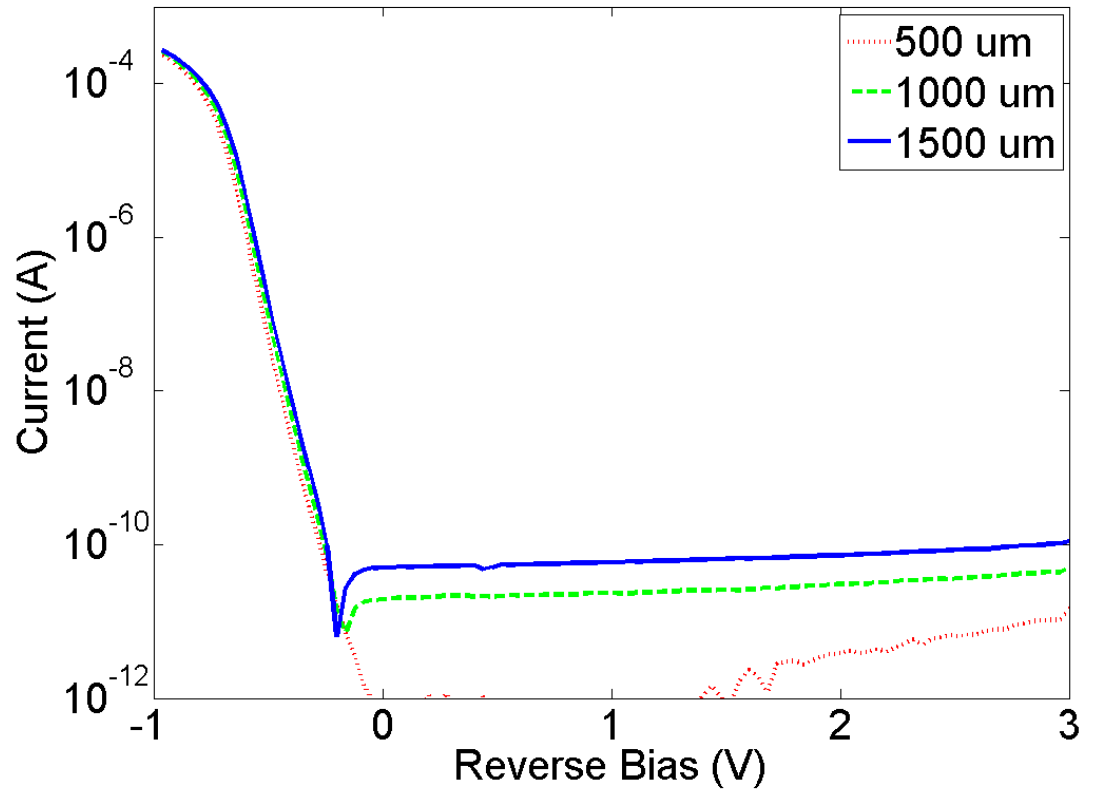


Passive Device Characteristics

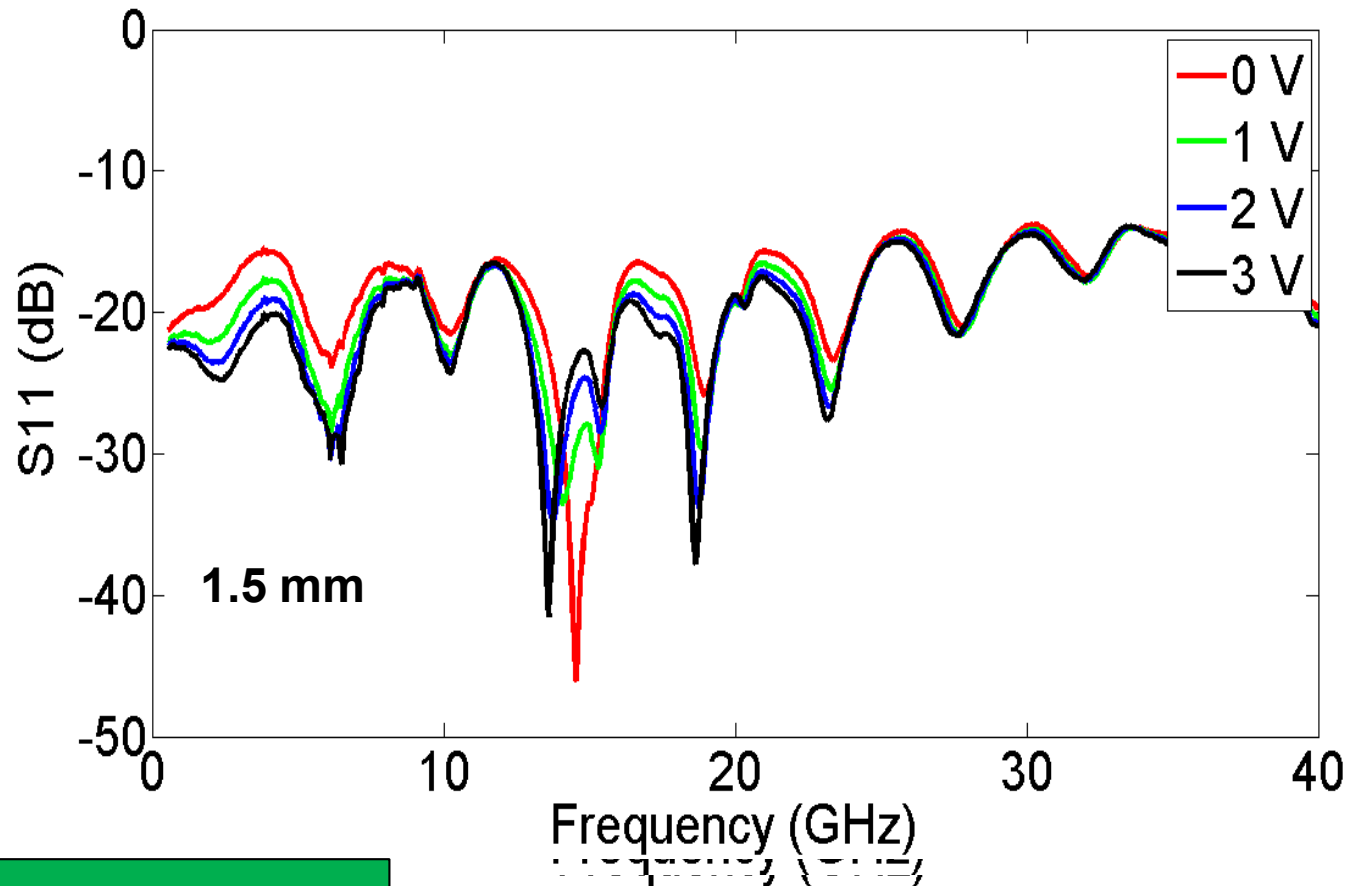


DC device performance

Low leakage
current in reverse
bias

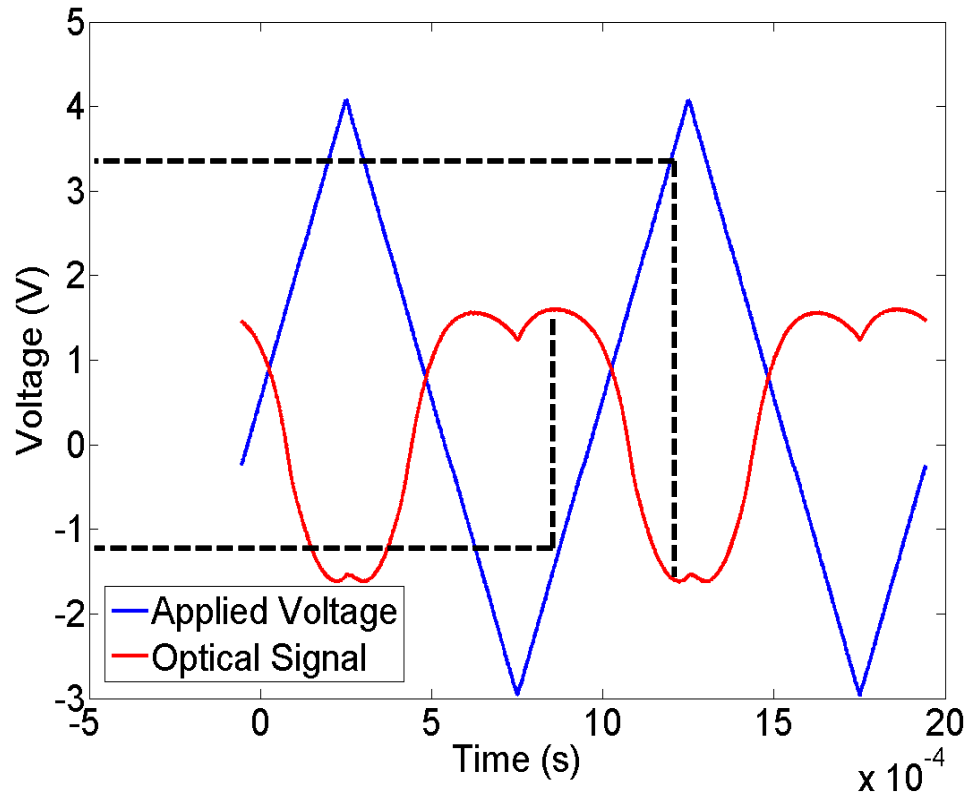


RF Device Performance



$S_{11} < -15$ dB
up to 40 GHz

Halfwave Voltage Measurement



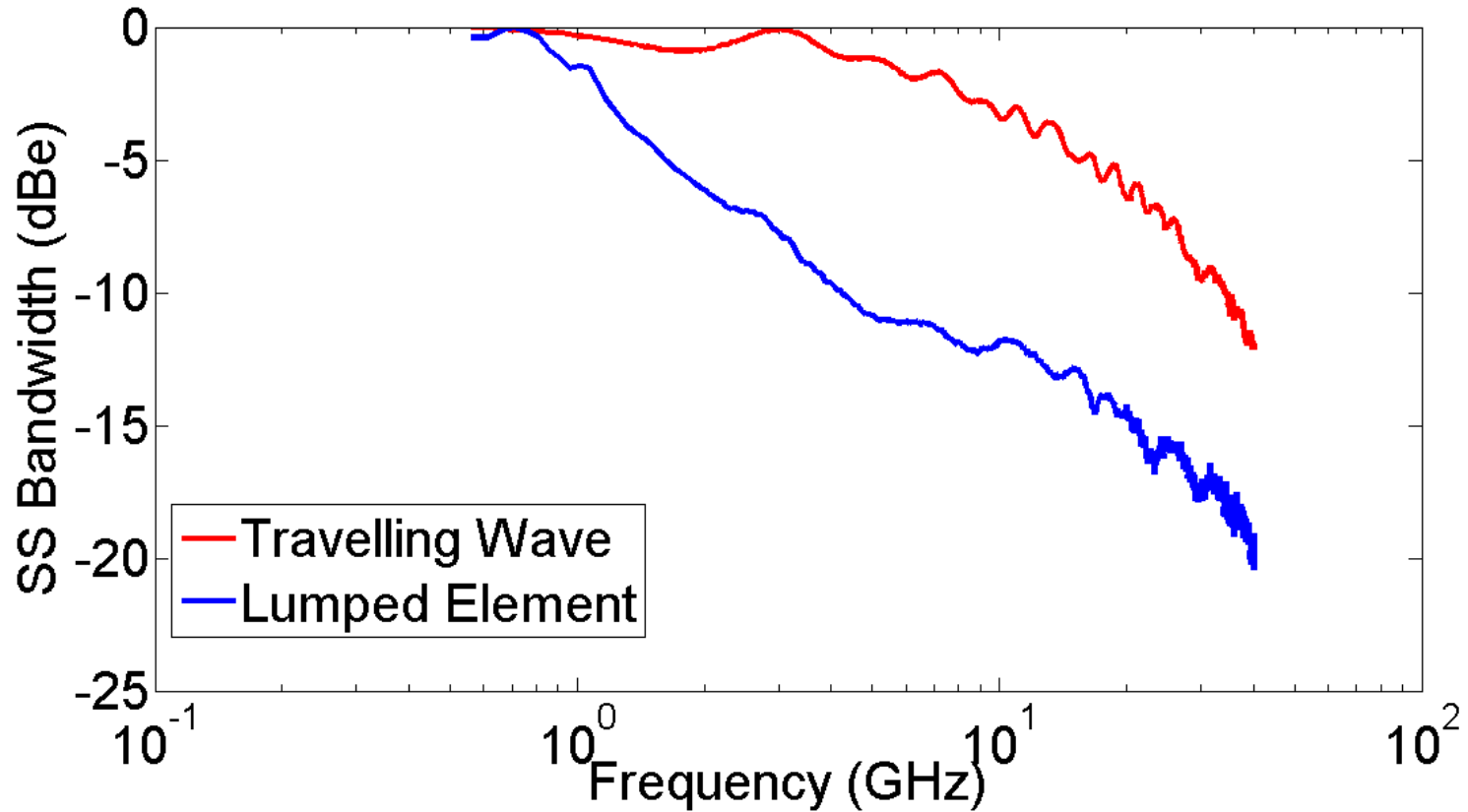
$$5 \text{ V } V_{\pi}$$

$$L_{\text{eff}} = 1 \text{ mm}$$

$$V_{\pi} L = 0.5 \text{ V-cm}$$

Efficiency due to
vertical junction

Small Signal Bandwidth



Conclusions

- Vertical junction allows for efficient modulation
- Segmented approach enables simultaneous impedance and velocity matching
- There is a large trade-off space left to be further explored for further device improvement

