

PACKAGE ID - 001037MLTPL00 VHDL CONTROL ROUTER

KWIC TITLE - VHDL Control Routing Simulator

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LIMITATION CODE -UNL **AUDIENCE CODE** - UNL

COMPLETION DATE - 03/01/1994 **PUBLICATION DATE** - 03/01/1994

DESCRIPTION - The control router simulates a backplane consisting of up to 16 slot. Slot 0, reserved for a control module (cr-ctrl), generates the system clocks and provides the serial interface to the Gating Logic. The remaining 15 slots (1-15) contain routing modules (cr mod), each having up to 64 serial inputs and outputs with FIFOs. Messages to be transmitted to the Control Router are taken from text files. There are currently 17 such source files. In the model, the serial output of each source is connected to multiple receivers, so that there are 8 identical messages transmitted to the router for each message file entry.

PACKAGE CONTENTS - Media Directory; Software Abstract; READ.ME File (1 page); Media Includes Source Code, Sample Problem Input;

SOURCE CODE INCLUDED? - Yes

MEDIA QUANTITY - 1 CD Rom

COMPUTER - MLT-PLTFM

OPERATING SYSTEMS - Any system for which standard VNDL library is available.

PROGRAMMING LANGUAGES - VHDL

SOFTWARE LIMITATIONS - Source files should be replaced by random message generation process. With the current limited set of source files, only a small fraction of the control router bandwidth is used.

SOURCE CODE AVAILABLE (Y/N) - Y

RELATED SOFTWARE - VHDL

HARDWARE REQS - The compilation results are written to a large (35 MByte, >1 million drivers) simulation file.

ABSTRACT STATUS - Released AS-IS 7/10/95.

SUBJECT CLASS CODE - TO

KEYWORDS -

E S T S C
ENERGY SCIENCE & TECHNOLOGY SOFTWARE CENTER
SOFTWARE ABSTRACT

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COMPUTER PROGRAM DOCUMENTATION
V CODES
COMPUTERIZED CONTROL SYSTEMS
EQUIPMENT INTERFACES

EDB SUBJECT CATEGORIES -
990200 426000

SPONSOR - DOE/ER

PACKAGE TYPE - AS - IS